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Brief Biography of Necip Sayiner



Necip Sayiner

Executive Vice President of Renesas Electronics Corporation

President, Chief Executive Officer, and Director of Intersil Corporation

Dr. Sayiner joined Intersil as President, CEO and Director on March 14, 2013. Prior to joining Intersil, from September 2005 to April 2012, he served as President and Chief Executive Officer, and Director of Silicon Laboratories, a fabless semiconductor company engaged in the design of analog-intensive, mixed-signal integrated circuits. Prior to joining Silicon Laboratories, Dr. Sayiner held various leadership positions at Agere Systems Inc., which included Executive Vice President and General Manager, Enterprise and Networking Division from August 2004 to September 2005; and Vice President and General Manager, Networking ICs Division from March 2002 to August 2004.

Dr. Sayiner holds a B.S. in Electrical Engineering and a B.S. in Physics from Bosphorus University in Turkey, an M.S. in Engineering from Southern Illinois University, and a Ph.D. in Electrical Engineering from the University of Pennsylvania.