
October 30, 2013

Brief Biography of Tsuneo Takahashi

Tsuneo Takahashi

Date of Birth February 27, 1959

1983.4	Seiko Instrument CAD/WS system development department
1988.8	Fuji Xerox System technology LAB
1989.9	Regional application engineer, Intel K.K. (Japan) of Intel Corp.
2002.2	Board of Director and General Manager of Communication and Embedded Department, Intel K.K. (Japan) of Intel Corp
2004.2	Motorola company, Metrowerks Corp, Vice President and President of Metrowerks K.K.
2004.6	Vice President of Freescale Semiconductor, Inc. and President of Freescale Semiconductor Japan, Ltd.
2006.3	Senior Vice President of Freescale Semiconductor Inc. and President of Freescale Semiconductor Japan, Ltd.
2010.1	Senior Advisor to TPG capital and Advisor to Audyssey Laboratories, Inc.
2013.10	Vice President, Renesas Electronics (to present)