

Separate Sheet

Product Specifications of the R-Car T2

Product name (product No.)	R-Car T2 (R9A05G011)
Power supply voltage	3.3 V /1.8 V (IO), 1.2 V (core)
CPU core	ARM® Cortex®-M3 processor
On-chip memory	512 KB
External memory interface	Serial flash ROM interface
CMOS sensor interface	Input data format: YUV422, 8-/10-bit
	Input video size: 1,280 × 960 pixels (max.)
	Input data bit width: 16-bit (Y: 8-bit/UV: 8-bit), 10-bit multiplexing, 8-bit multiplexing
H.264 encoder	Input video size: 1,280 × 960 pixels (max.)
	Input frame rate: 30 fps
	Output format: YUV420
	Encode process time : under 1ms
Ethernet interface	On-chip MAC with Ethernet AVB 1.0 support
	Interface: MII
	Ethernet AVB (802.1BA)
	<ul style="list-style-type: none"> • IEEE 802.1AS • IEEE 802.1Qav • IEEE 802.1Qat • IEEE 1722
Other peripheral functions	32-bit compare timer × 4 channels
	Watchdog timer × 1 channel
	I ² C bus interface × 1 channel
	Clock synchronous serial interface (CSI) × 1 channel
	Quad serial peripheral interface (QSPI) × 1 channel (flash ROM interface support)
	Interrupt controller (INTC)
	Clock: Quartz resonator (25 MHz) or external clock input
	Debug function (JTAG)

Power consumption	40mW(Typ.)
Package	121-pin FPBGA (6 mm × 6 mm)

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