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**Separate Sheet**

**Product Specifications of the R-Car V3M**

- Two 800-MHz ARM® Cortex®-A53 cores
- Dual lockstep 800-MHz ARM Cortex-R7 core
- Memory controller for DDR3L-1600 with 32 bits,
- Image recognition engine (IMP-X5-V3M)
- Image signal processor (ISP)
- Low latency video encoder
- Video Output (4 lanes x 1 channel LVDS, 1 channel digital),
- Video Input (4 lanes x 1 channel MIPI-CSI2, 2 channels digital)
- 2 channels CAN-FD interface
- 1 channel Ethernet AVB interface
- 2 channels FlexRay interface

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