

**Separate Sheet**

**Product Specifications of the RX65N and RX651 Groups of Microcontrollers**

Group name	RX65N		RX651	
Pin count	144, 145 pins	100 pins	144, 145 pins	100 pins
Parts name	R5F565NxyDFB R5F565NxyDLK	R5F565NxyDFP R5F565NxyDLJ	R5F5651xyDFB R5F5651xyDLK	R5F5651xyDFP R5F5651xyDLJ
Flash memory	512 KB (x=4), 768 KB (x=7), 1024 KB (x=9)			
RAM	256 KB			
Power supply voltage	2.7 V - 3.6 V			
Operating frequency	Up to 120 MHz			
Operating Temperature	-40°C to +85°C (-40°C to 105°C parts planned)			
CPU Core	32 bit RXv2 CPU			
Floating point unit	Single precision floating point unit (Support add/subtract/compare/multiply/divide and other instructions)			
I/O port	In/Out: 111 In: 1	In/Out: 78 In: 1	In/Out: 111 In: 1	In/Out: 78 In: 1
Data Transfer function	<ul style="list-style-type: none"> <li>● DMA controller 8 ch</li> <li>● data transfer controller</li> <li>● EXDMA controller 2 ch</li> </ul>			
Timer function	<ul style="list-style-type: none"> <li>● 16-bit timer pulse unit 6 ch</li> <li>● multi-function timer pulse unit 9 ch</li> <li>● port output enable</li> <li>● programmable pulse generator</li> <li>● 8-bit timer 4 ch</li> <li>● 16-bit compare match timer 4 ch</li> <li>● 32-bit compare match timer 2 ch</li> <li>● real time clock</li> <li>● watch dog timer</li> <li>● independent watch dog timer</li> </ul>			
Communication function	<ul style="list-style-type: none"> <li>● Ethernet controller 1 ch</li> <li>● DMA controller for Ethernet controller 1 ch</li> </ul>		-	
	<ul style="list-style-type: none"> <li>● USB2.0 FS Host/Function module 1 ch</li> </ul>			
	● Serial	● Serial	● Serial	● Serial

	Communication interface 11 ch	Communication interface 9 ch	Communication interface 11 ch	Communication interface 9 ch
	<ul style="list-style-type: none"> <li>● serial communication interface with 16 byte FIFO buffer 2 ch</li> <li>● I<sup>2</sup>C bus interface 2 ch</li> <li>● serial peripheral interface 3 ch</li> <li>● CAN module 2 ch</li> <li>● quad serial peripheral interface 1 ch</li> <li>● SD host interface 1 ch (Yes: y=B or F, No: y=A or E)</li> <li>● SD slave interface 1 ch (Yes: y=B or F, No: y=A or E)</li> <li>● MMC host interface 1 ch</li> <li>● CMOS camera interface (parallel data capture unit) 1 ch</li> </ul>			
Analogue function	● 12-bit A/D Converter 8 ch			
	● 12-bit A/D Converter 21 ch	● 12-bit A/D Converter 14 ch	● 12-bit A/D Converter 21 ch	● 12-bit A/D Converter 14 ch
	● 12-bit D/A Converter 2 ch	● 12-bit D/A Converter 1 ch	● 12-bit D/A Converter 2 ch	● 12-bit D/A Converter 1 ch
	● Temperature Sensor			
Security Yes (y=E or F) No (y=A or B)	<ul style="list-style-type: none"> <li>● AES</li> <li>● TRNG</li> </ul>			
Other	<ul style="list-style-type: none"> <li>● CRC calculator</li> <li>● data operation circuit</li> <li>● clock frequency accuracy measurement circuit</li> <li>● event link controller</li> </ul>			
Package	144-pin LQFP 0.5mm pitch 145-pin TFLGA 0.5mm pitch	100-pin LQFP 0.5mm pitch 100-pin TFLGA 0.65mm pitch	144-pin LQFP 0.5mm pitch 145-pin TFLGA 0.5mm pitch	100-pin LQFP 0.5mm pitch 100-pin TFLGA 0.65mm pitch

###

(Remarks) All registered trademarks or trademarks are the property of their respective owners.