

IDT PLL CHIP CARRIER

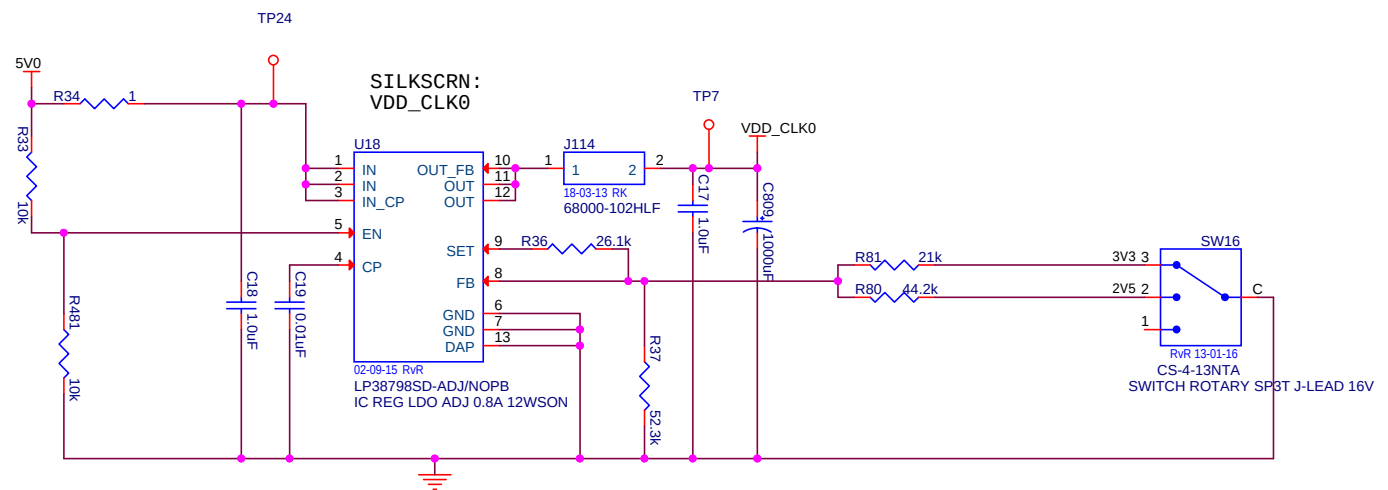
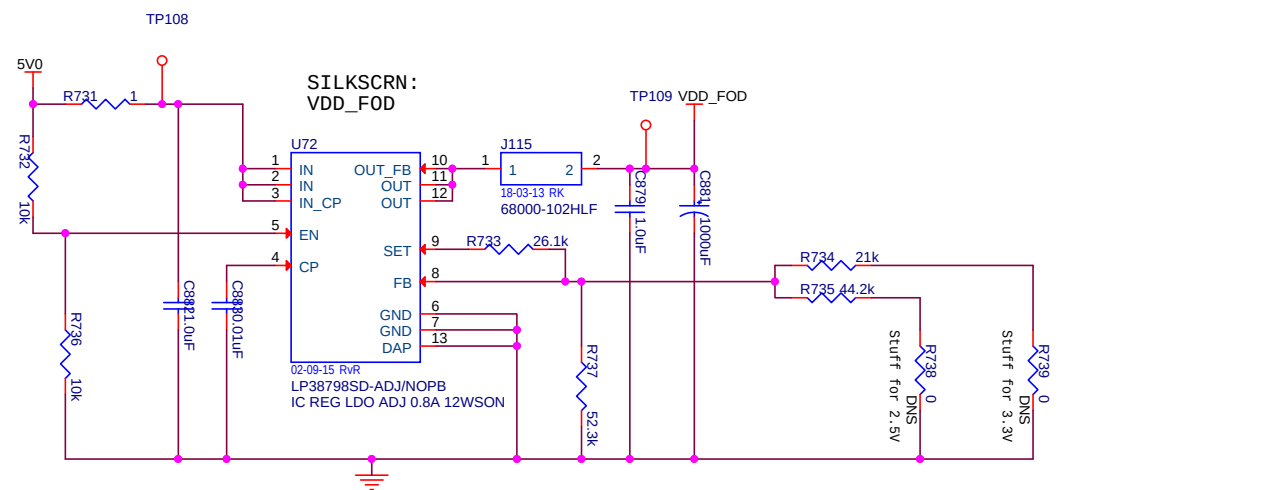
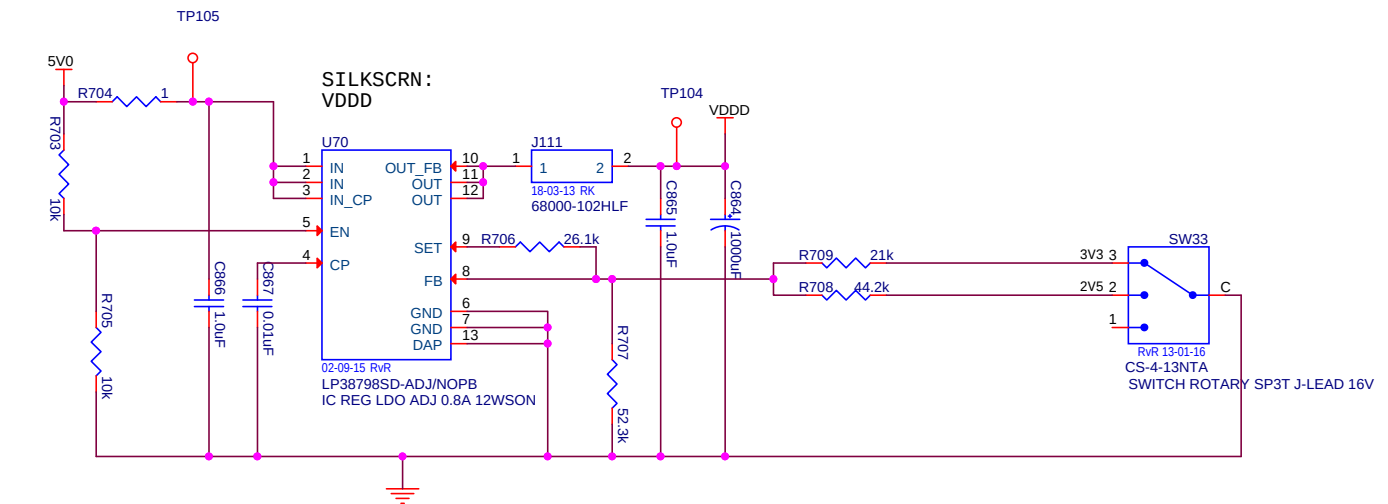
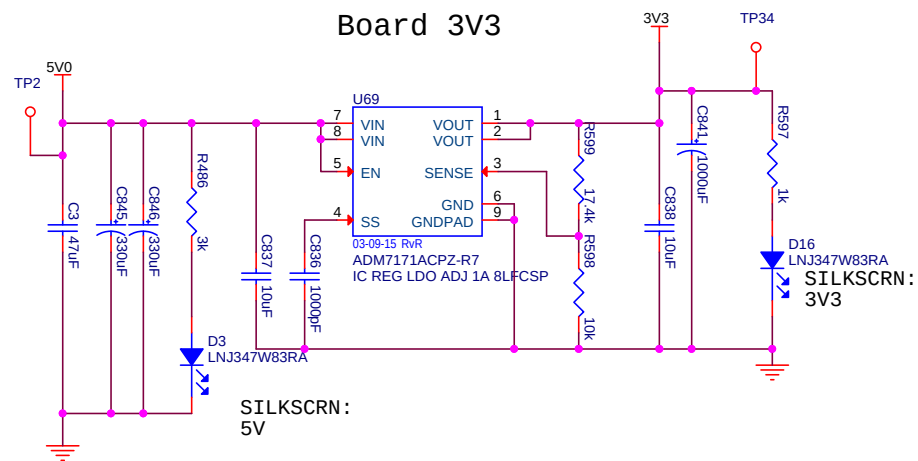
TABLE OF CONTENTS

PAGE	DESCRIPTION
1	Table of Contents and Revision History
2	PWR SUPPLIES 1
3	PWR SUPPLIES 2
4	FTDI USB, RST
5	PLL CORE, CLOCK IN
6	PLL PWR, CLOCK OUT

REVISION HISTORY

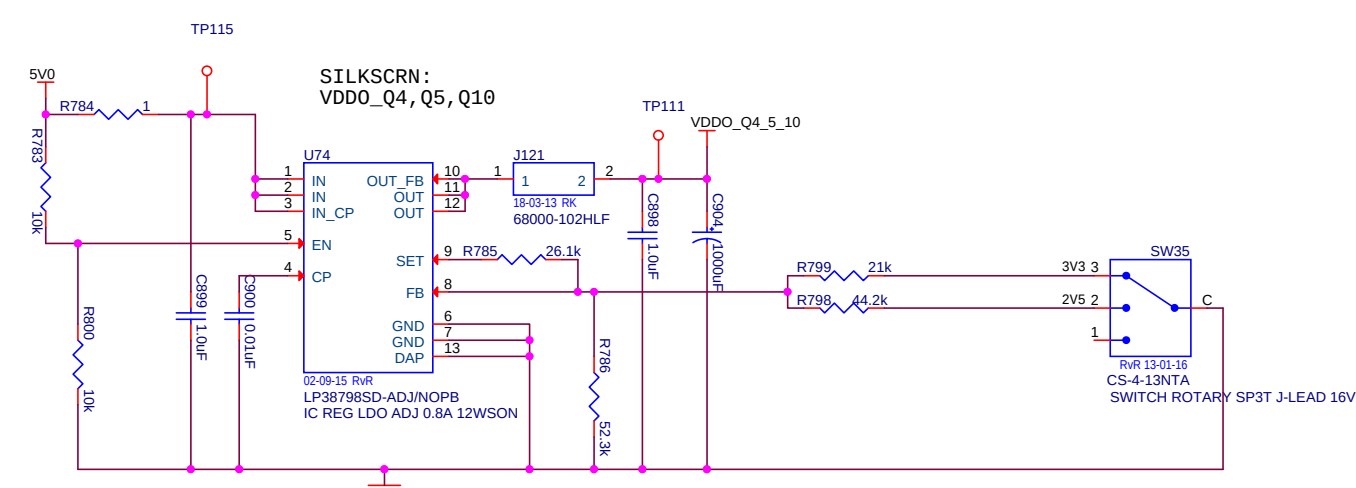
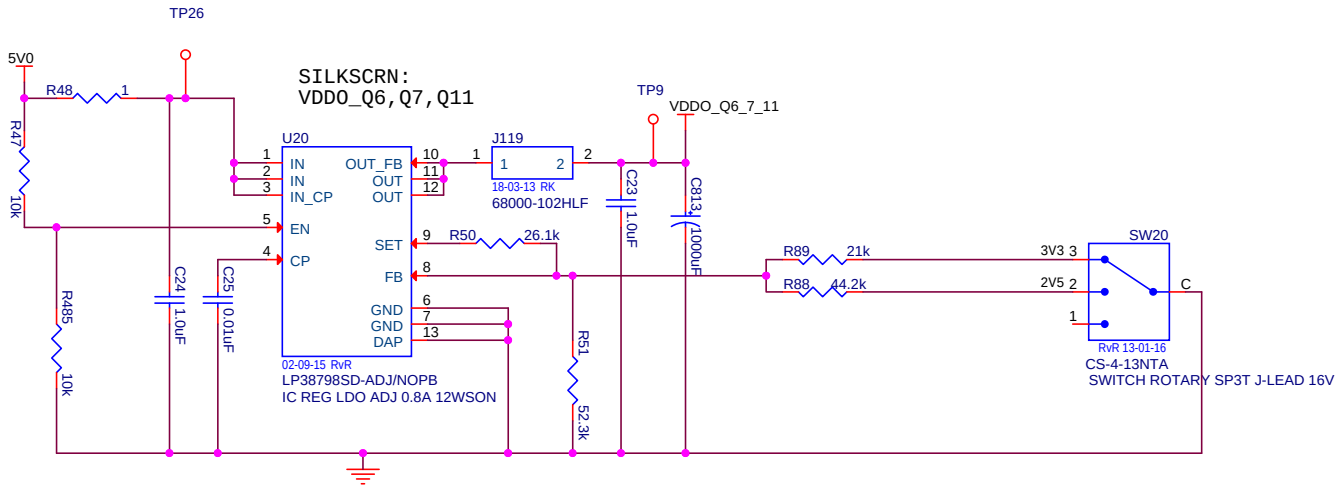
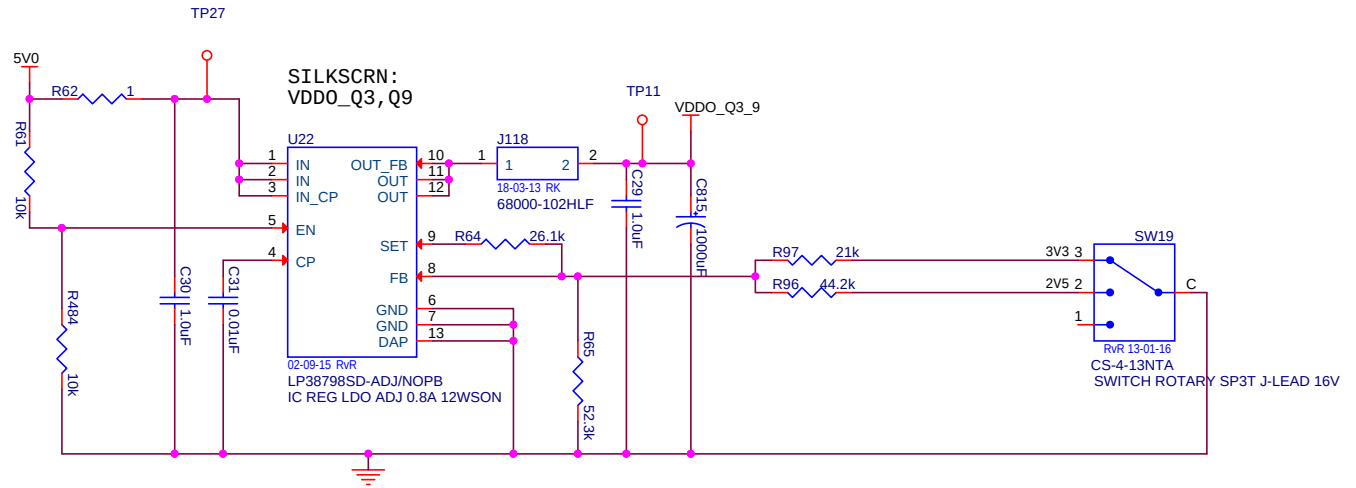
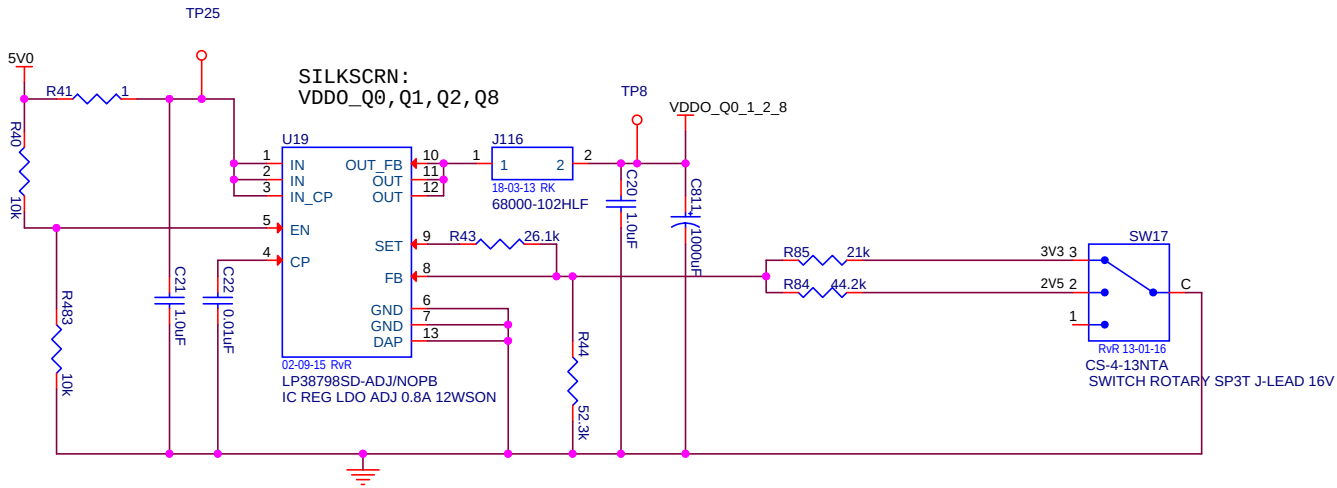
REVISION	DATE	CHANGE DETAILS
0.1	5 December 2017	First Release
0.2	28 February 2018	For details re changes done, refer to: \\navar\z_10397-01_sch\reviews\DN-10397-01_Changes_Feb 28 2018.xlsx

PROJECT NAME	Fidus Systems 375 Terry Fox Drive, Ottawa, ON K2K 0J8	
Inavar		
DESIGN	TITLE	
BM	IDT PLL Chip Carrier	
DRAWN	SUBTITLE	
BM	Table of Contents and Revision History	
CHECK	DRAWING NUMBER	REVISION
CL	SK-10397-01	0.2
	RELEASE DATE	SHEET
	5 December 2017	1 OF 6

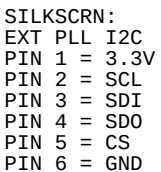


POS 3 = 3V3
POS 2 = 2V5
POS 1 = 1V8/FPGA control

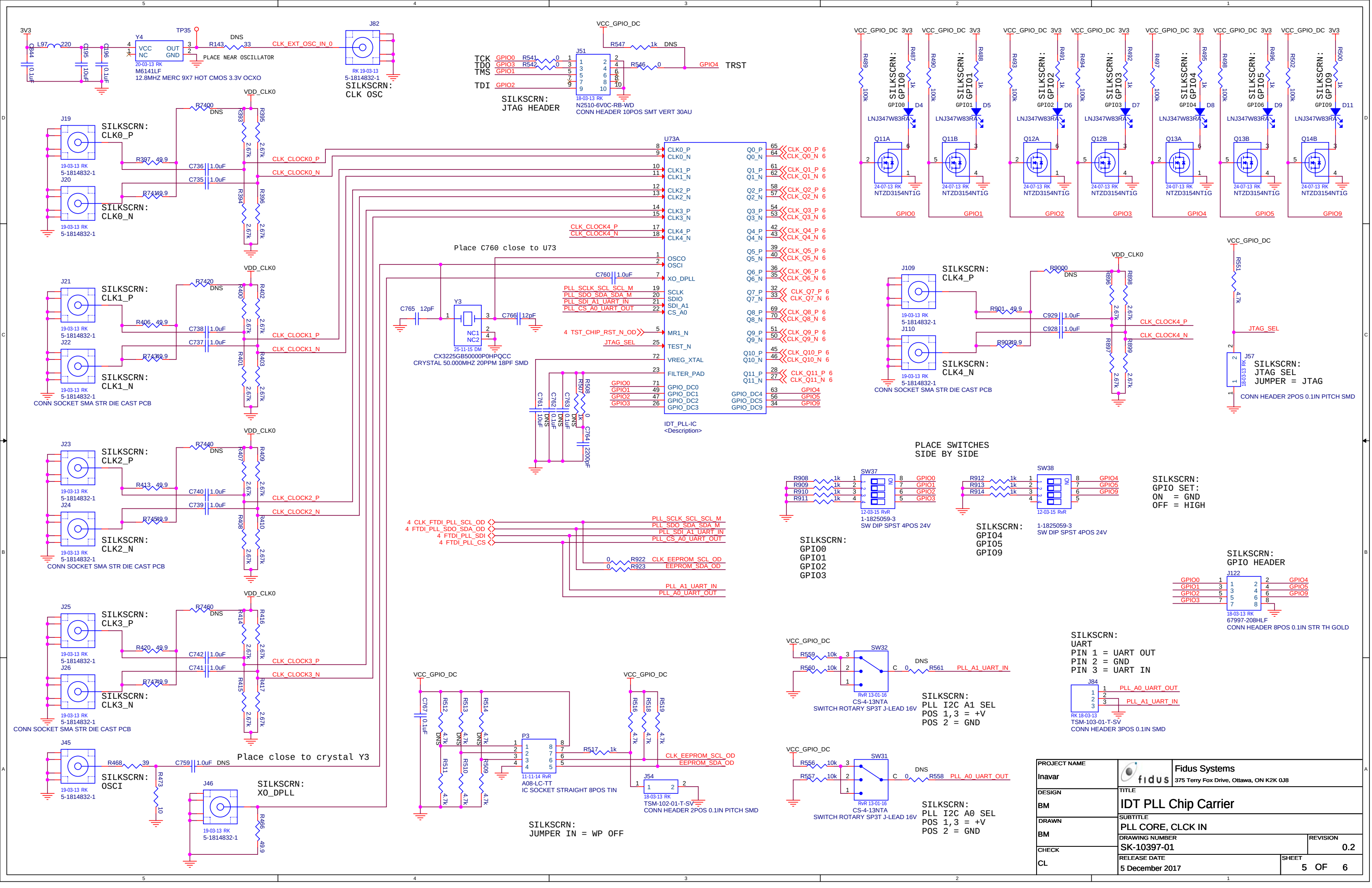
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DRAWN	SUBTITLE	
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CHECK	DRAWING NUMBER	REVISION
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CL	RELEASE DATE	SHEET
	5 December 2017	2 OF 6



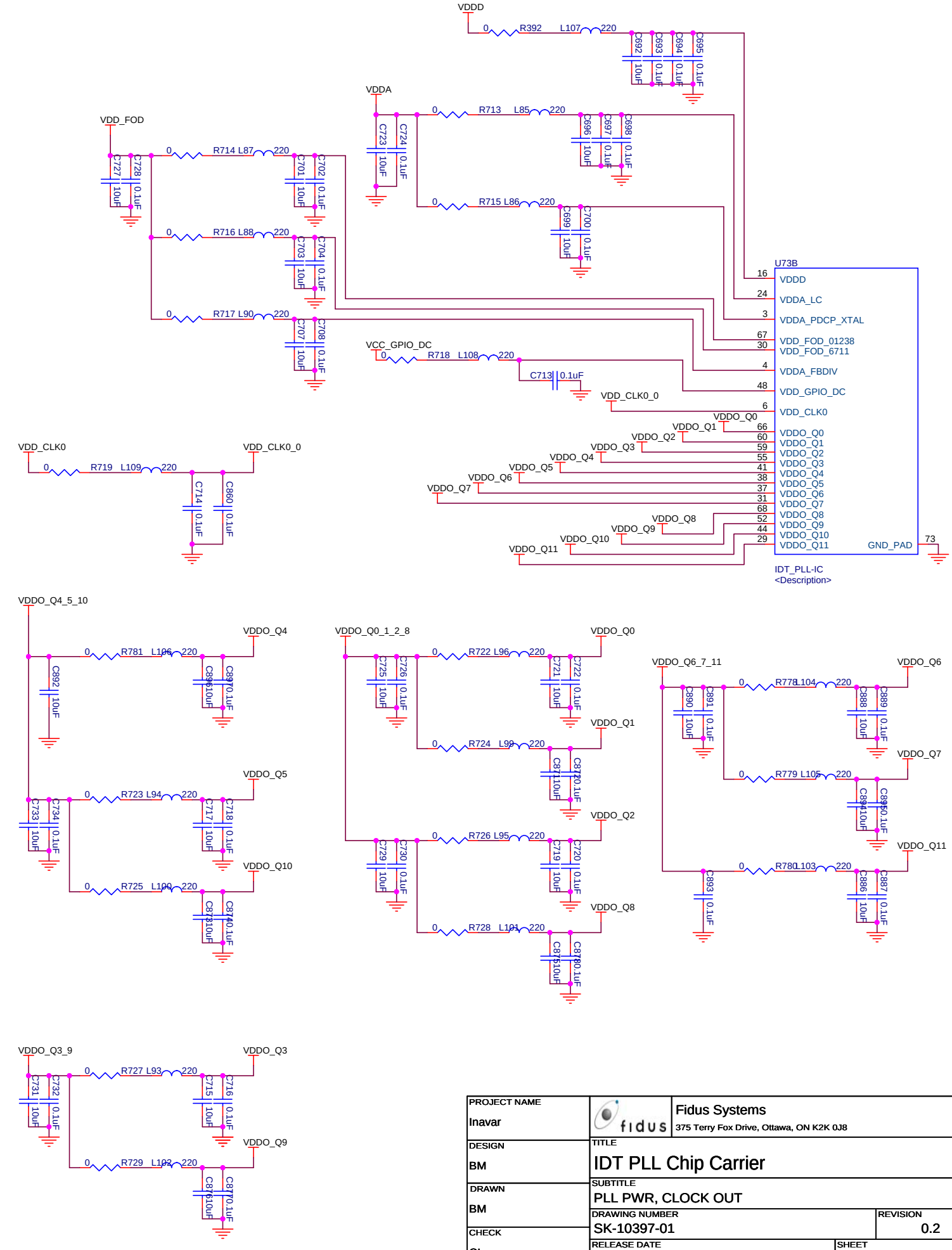
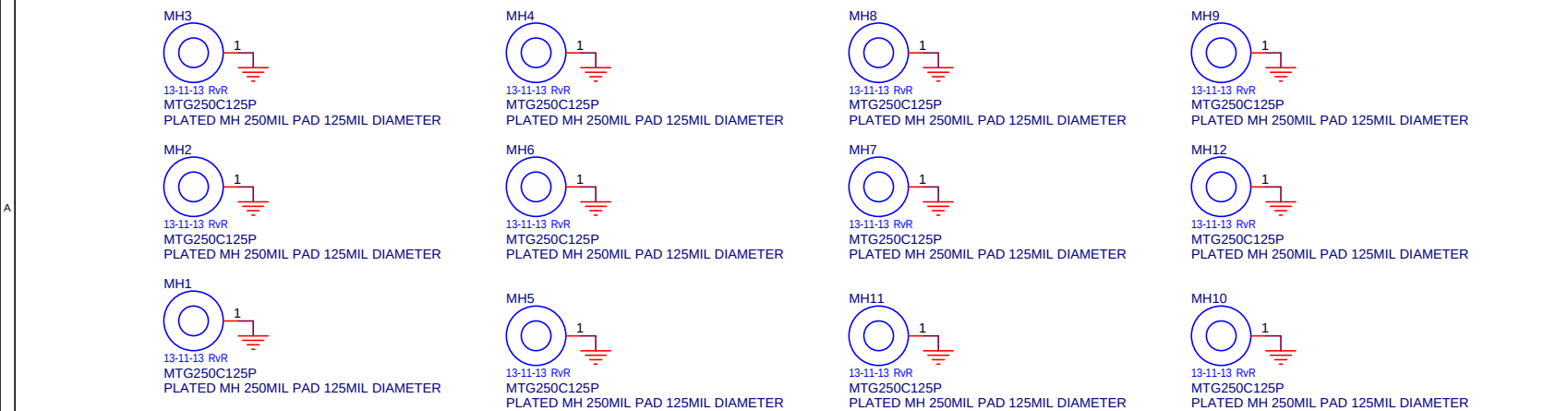
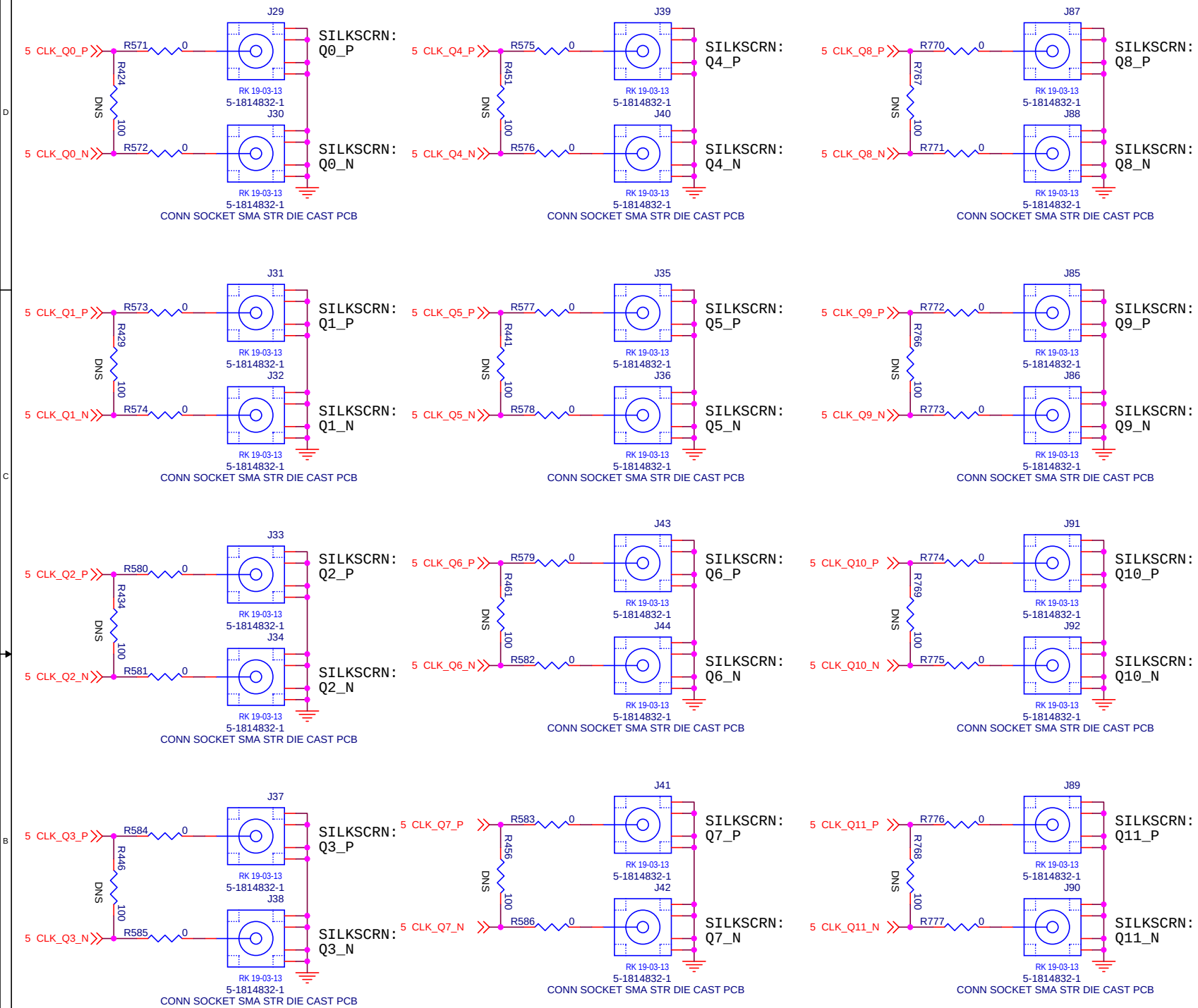
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		SUBTITLE	
		PWR SUPPLIES 2	
		DRAWING NUMBER	REVISION
		SK-10397-01	0.2
		RELEASE DATE	SHEET
		5 December 2017	3 OF 6



PROJECT NAME	 Fidus Systems 375 Terry Fox Drive, Ottawa, ON K2K 0J8	
Inavar		
DESIGN	TITLE	
BM	IDT PLL Chip Carrier	
DRAWN	SUBTITLE	
BM	FTDI USB, RST	
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	SK-10397-01	0.2
CL	RELEASE DATE	SHEET
	5 December 2017	4 OF 6



PLACE PARALLEL TERMINATIONS CLOSE TO U73



PROJECT NAME	Fidus Systems	
Inavar	375 Terry Fox Drive, Ottawa, ON K2K 0J8	
DESIGN	TITLE	
BM	IDT PLL Chip Carrier	
DRAWN	SUBTITLE	
BM	PLL PWR, CLOCK OUT	
CHECK	DRAWING NUMBER	REVISION
CL	SK-10397-01	0.2
RELEASE DATE		SHEET
5 December 2017		6 OF 6