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Note on Using CS+ Device Information for the RX Family

When using the CS+ device information for MCUs of the RX family, take note of the following problem:

- With using of the RX64M group

Notification: The product name of CubeSuite+, an integrated development environment from Renesas, has been changed to "CS+."

1. Product Concerned

CubeSuite+ DevInfo_RX V. 1.01.00 and V. 1.02.00 and
CS+ Device Information for RX V. 1.04.00

To check the version numbers, refer to the following URL:

https://www.renesas.com/cs+_ver

2. Description

In the startup routine created by using this product, option setting memory for the trusted memory (hereinafter referred to as the TM) of MCUs of the RX64M group is not described.

Supplemental information: Support for debugging the trusted memory function has started from CS+ V. 3.00.00.

3. Workaround

If you are using an MCU of the RX64M group, add settings for the TM enable flag register (TMEF) and TM identification data register (TMINF) to the created startup routine whether or not you will be using the trusted memory function.

File to be modified: vecttbl.c

Contents to be added

(1) If you will not be using trusted memory

```
-----  
#pragma address __TMEFreg=0x00120048    // TMEF register  
const unsigned long __TMEFreg = 0xffffffff;  
  
#pragma address __TMINFreg=0x00120060    // TMINF register  
const unsigned long __TMINFreg = 0xffffffff;  
-----
```

(2) If you will be using trusted memory

Enter the same directive and declaration as above, but change the values with reference to the user's manual for the RX64M group.

4. Schedule for Fixing the Problem

This problem will be fixed in the next version of device information for the RX family.

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