RENESAS Tool News

RENESAS TOOL NEWS on April 16, 2014: 140416/tn4

Note on Using Device Information for RX Family (Managed by CubeSuite+)

When using "CubeSuite+ DevInfo_RX," the Device Information for the RX family (managed by CubeSuite+), take note of the following problems:

- With IOR panel display for the RX64M group
- With I/O header file for the RX64M group

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1. Problem with IOR Panel Display for the RX64M Group

1.1 Product Concerned

CubeSuite+ DevInfo_RX V1.01.00

To check to see the version number, refer to the following URL:
http://www.renesas.com/cubesuite+_ver

1.2 Descriptions

Note the following for the registers displayed in the IOR panel of either the E1, the E20 emulator, or the RX simulator.

(1) The following register is not displayed:

ICU.IRQCR0

(2) Incorrect values are displayed for the following registers:

ICU.IRQCR1
ICU.IRQCR2
ICU.IRQCR3
ICU.IRQCR4
ICU.IRQCR5
ICU.IRQCR6
ICU.IRQCR7
ICU.IRQCR8
ICU.IRQCR9
ICU.IRQCR10
ICU.IRQCR11

ICU.IRQCR12
ICU.IRQCR13
ICU.IRQCR14
ICU.IRQCR15
ICU.IRQFLTE0

1.3 Workaround

To display the above registers, use the watch panel with the following steps:

(1) Get the import file (watch.csv) for the watch panel.
Download the watch.zip file (310 Byte) from the following URL and decompress it.
http://tool-support.renesas.com/eng/toolnews/140416/watch.zip

(2) Open the watch panel from CubeSuite+, right-click on the watch panel to open the context menu, select Import Watch Expression, and specify the csv file created in step (1).

(3) The registers are displayed in the watch panel.
The register names are shown in the rightmost column "Memo" in the watch panel.

1.4 Schedule for Fixing the Problem

This problem will be fixed in the next version of CubeSuite+ DevInfo_RX.

2. Problem with I/O Header File for the RX64M Group

2.1 Product Concerned

CubeSuite+ DevInfo_RX V1.01.00

To check to see the version number, refer to the following URL:
http://www.renesas.com/cubesuite+_ver

2.2 Description

The I/O header file (iodefine.h) for the RX64M group, which is copied when a project for the RX64M group is created with CubeSuite+, has the errors shown below.
Hereafter, the header file including these errors is called "iodefine.h V0.8A".

(1) For the EPTPC0 and EPTPC1 modules, the SYIPADDRR and subsequent registers are not allocated to correct addresses.
(2) For the FLASH module, the FWEPROR and subsequent registers are not allocated to correct addresses.

2.3 Workaround

Replace the header file with the new correct header file using

the following steps:
Hereafter, the new header file is called "iodefine.h V0.8C".
(1) Get the iodefine.h V0.8C file.
Download the rx64m_iodefine_v080c.zip file (31.8 KB) from the following URL and decompress it.
http://tool-support.renesas.com/eng/toolnews/140416/rx64m_iodefine_v080c.zip

(2) Replace iodefine.h V0.8A in the created project
with iodefine.h V0.8C.

2.4 Schedule for Fixing the Problem

This problem will be fixed in the next version of CubeSuite+ DevInfo_RX.
