

IGBT • FRD

How to use PLECS Half Bridge 3Phase Inverter

Introduction

This document explains how to use the Half_Bridge_3Phase_Inverter with the PLECS device model of Renesas IGBT and FRD products.

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1. Preparations

1.1 PLECS Model Installation

To use Half_Bridge_3Phase_Inverter, the PLECS model of IGBT RJP6831JWS and RJU6832JWS must be installed.

For installation, please refer to "How to install PLECS IGBTFRD model".

As shown in Figure 1-1, if the "Renesas Component - date" category is found in the library browser and "RJP6831_RJU6832" models are available to use.

🔁 Library Brows	er	—		\times		
File Window	Help					
Q Search compone	ents					
 > System > Assertions > Control > Electrical > Thermal > Magnetic > Mechanical > PLECS RT Box > Renesas Compon 	ent - JulSth2024					
-WILL RIP	831_RJU6832					
RJP6831_RJU6832 [RJP6831_RJU6832 [RJP6831JWS Features] 750 V Trench & field stop high AE4 technology Low collector to emitter saturation voltage VCE(sat) = 1.4 V typ. (at IC = 300 A, VGE = 15 V, Tj = 25 • C) Low Switching loss Easy paralleling by internal Rg AEC Q101 (HTRB, HTGB) qualified [RJU6832JWS Features] Fast Recovery Diode technology Low Forward voltage						
VF = 1.5 V typ. (at IF AEC Q101 (HTRB) q [Applications] Hybrid and electric v						

Figure 1-1 PLECS Component Library



2. How to use three-phase inverter models

2.1 Open the three-phase inverter model

Open the downloaded three-phase inverter model. (Fig. 2-1)

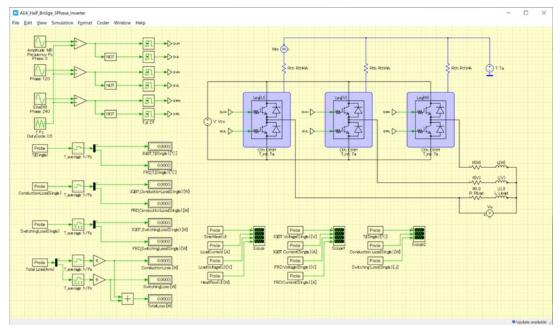


Figure 2-1 Three-phase inverter model

2.2 Setting Simulation Parameters

Select Simulation \rightarrow Simulation parameters to open the Simulation Parameters window, as shown in Fig. 2-2, and set the parameters in the Initialization tab.

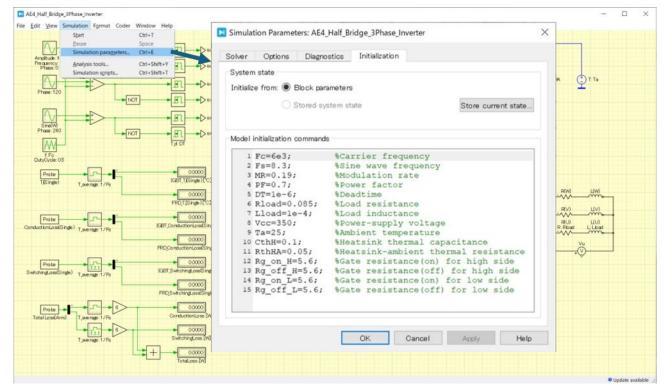


Figure 2-2 Simulation Parameters

The simulation parameters are as follows.

Fc	Carrier frequency		
Fs	Sine wave frequency		
MR	Modulation rate		
PF	Power Factor		
DT	Deadtime		
Rload	Load resistance		
Lload	Load inductance		
Vcc	Power-supply voltage		
Та	Ambient temperature		
CthH	Heatsink thermal capacitance		
RthHA	Heatsink-ambient thermal resistance		
Rg_on_H	Gate resistance (on) for high side		
Rg_off_H	Gate resistance (off) for high side		
Rg_on_L	Gate resistance (on) for low side		
Rg_off_L	Gate resistance (off) for low side		

In this model, three devices are connected in parallel on each of the high and low sides of the inverter, so a total of six devices are connected to one inverter.

The parameters CthH and RthHA are values for one inverter circuit, so please set appropriate values according to the number of devices included in the inverter.

2.3 Simulation Execution

Select Simulation \rightarrow Start to run the simulation. (Fig. 2-3)

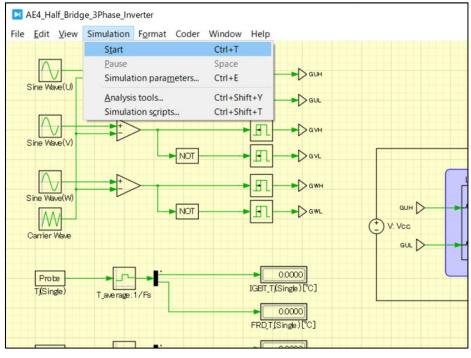


Figure 2-3 Fig. 2-3 Star Simulation



2.4 Monitoring the simulation results

Once the simulation is started, the simulation time is displayed in real time on the junction temperature and loss monitor in Fig. 2-4.

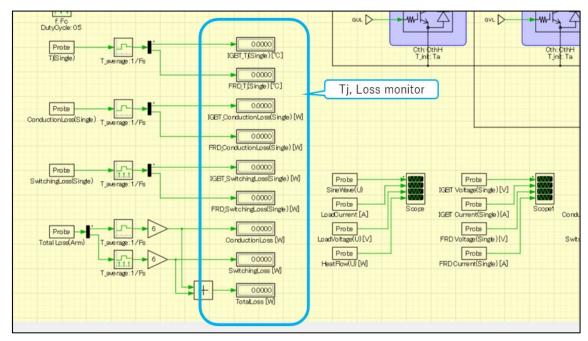


Figure 2-4 Simulation result monitor

2.5 Simulation Result Waveform

The simulation result waveforms can be seen by the scope shown in Figure 2-5.

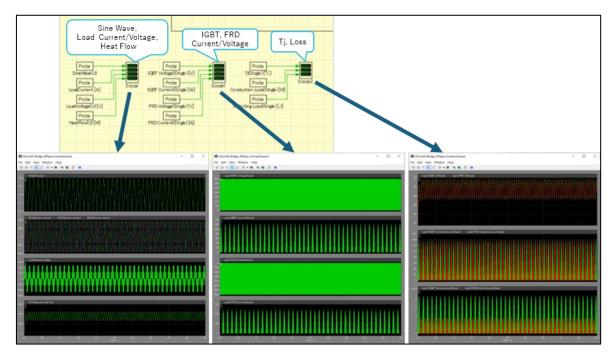


Figure 2-5 Waveform Results

Revision History

		Description		
Rev.	Date	Page	Summary	
1.00	Jul.31.24	-	First edition	



RENESAS

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