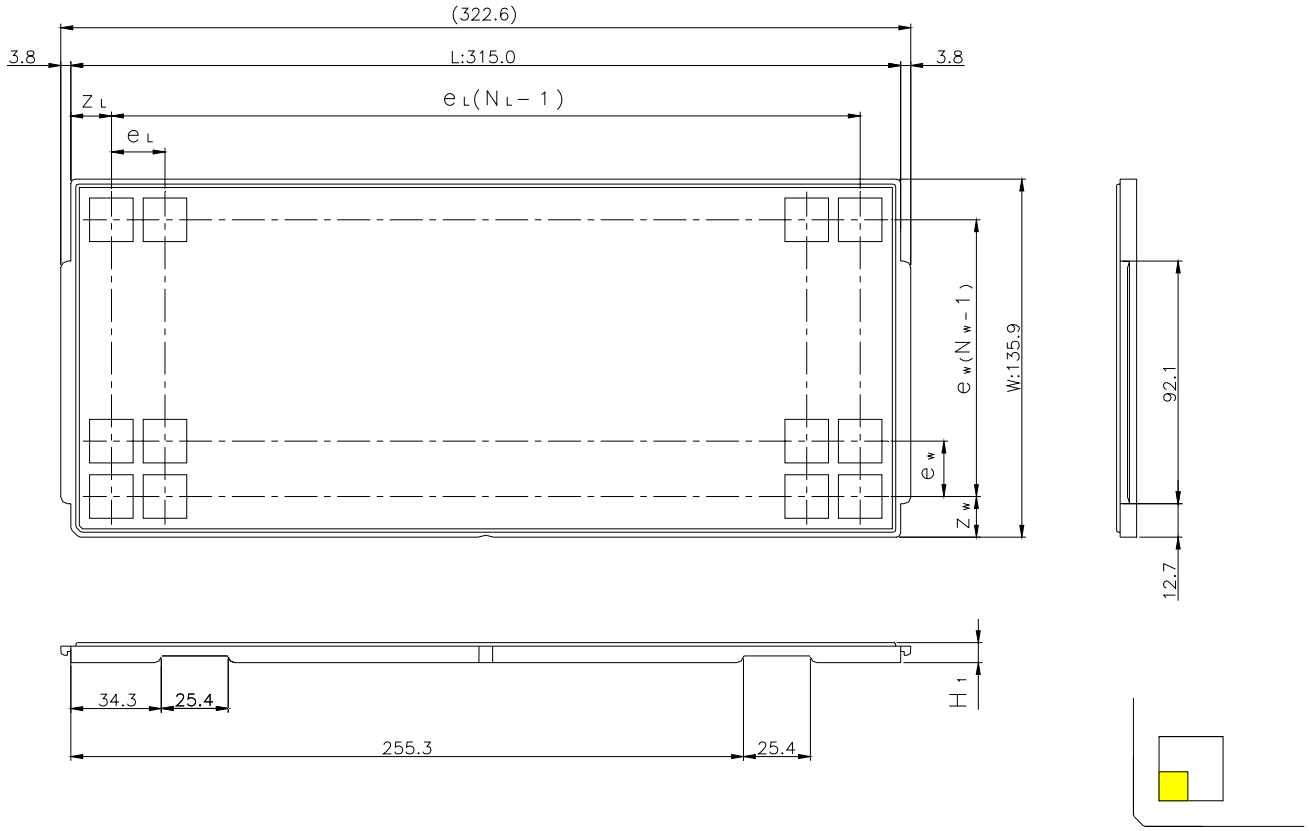
JEDEC TRAY

Tray Code	RENESAS Code	Previous Code
L196-24	PTSB0052LB-A	52PTG-A

Unit:mm



The pin1 is located in the hatching portion

Note : Numbers in parentheses are the numbers of Ics contained in five trays per inner box, and those without parentheses are the numbers of Ics contained in eight trays per inner box.

Position dimension of cells (mm)	Z_W	10.8
	Z_L	12.3
	e_W	12.7
	e_L	13.2
Thickness (mm)	H_1	7.62
Number of cells	N_W	10
	N_L	23
Maximum storage No. IC/Tray		230
Maximum storage No. IC/Inner box (Note)		1840(1150)
Material		Carbon PPE
Surface resistance		Less than $1x10^{11}\Omega/\square$
Heat resistant temperature		125℃24h
Packing form		Dry pack

© 2012 Renesas Electronics Corporation. All rights reserved.