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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SJ130(L), 2SJ130(S)

Silicon P Channel MOS FET

REJ03G0846-0200 (Previous: ADE-208-1181) Rev.2.00 Sep 07, 2005

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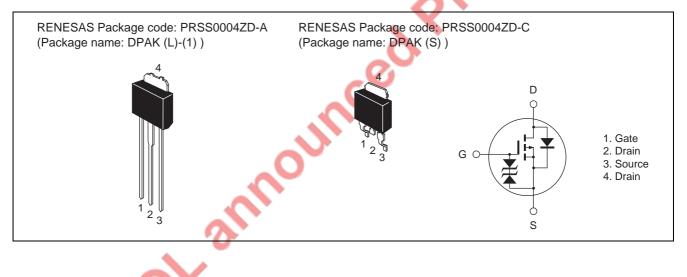
Description

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter and ultrasonic power oscillators

Outline





Absolute Maximum Ratings

		$(Ta = 25^{\circ}C)$
Symbol	Value	Unit
V _{DSS}	-300	V
V _{GSS}	±20	V
ID	-1	А
I _{D (pulse)}	-2	А
I _{DR}	-1	А
Pch Note 1	20	W
Tch	150	°C
Tstg	-55 to +150	٥C
	V _{DSS} V _{GSS} I _D I _{D (pulse)} I _{DR} Pch ^{Note 1} Tch	VDSS -300 VGSS ±20 ID -1 ID(pulse) -2 IDR -1 Pch 20 Tch 150

Note: 1. Value at $Tc = 25^{\circ}C$

Electrical Characteristics

Electrical Characteristics						(Ta = 25°C)
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	-300	—		V	$I_{\rm D} = -10$ mA, $V_{\rm GS} = 0$
Gate to source breakdown voltage	V (BR) GSS	±20	—		V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	—	±10	μA	$V_{GS} = \pm 16 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	—	-100	μA	$V_{DS} = -240 \text{ V}, \text{ V}_{GS} = 0$
Gate to source cutoff voltage	V _{GS (off)}	-2.0	—	-4.0	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	R _{DS (on)}	—	6.0	8.5	Ω	$I_D = -0.5 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note 2}}$
Forward transfer admittance	y _{fs}	0.25	0.4	—	S	$I_D = -0.5 \text{ A}, V_{DS} = -20 \text{ V}^{\text{Note 2}}$
Input capacitance	Ciss	—	235	<u> </u>	pF	$V_{DS} = -10 \text{ V}$
Output capacitance	Coss		65		pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		🥬 16		pF	f = 1 MHz
Turn-on delay time	t _{d (on)}	0	10		ns	I _D = -0.5 A
Rise time	tr	-	25		ns	$V_{GS} = -10 V$
Turn-off delay time	t _{d (off)}	_	35		ns	$R_L = 60 \Omega$
Fall time	tf	_	45		ns	
Body to drain diode forward voltage	VDF		-0.9		V	$I_F = -1 A, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}		200		ns	$I_F = -1 A, V_{GS} = 0$
	T I					$di_F/dt = 50 A/\mu s$

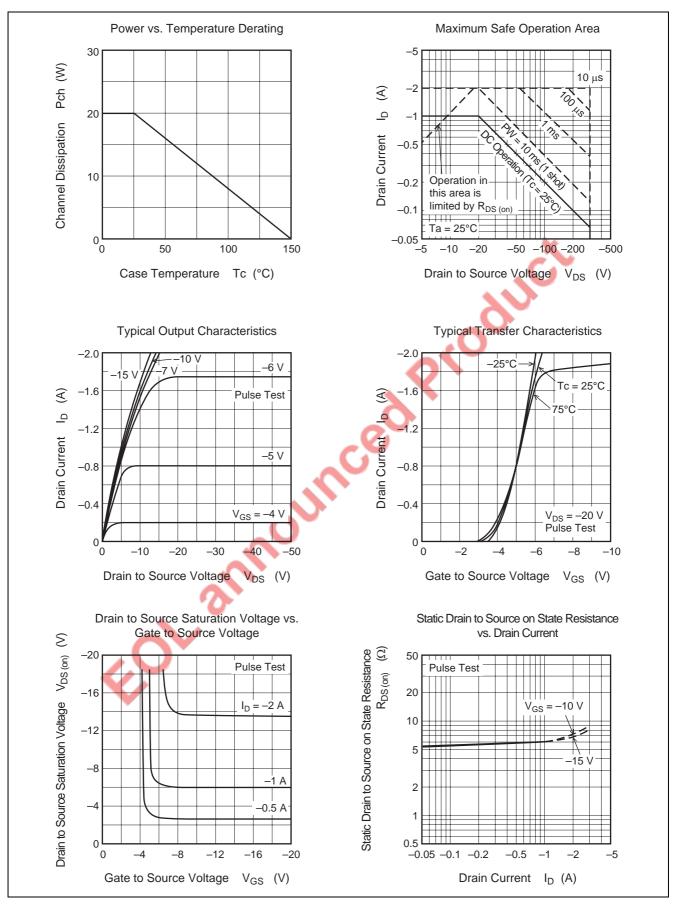
Note: 2. Pulse test

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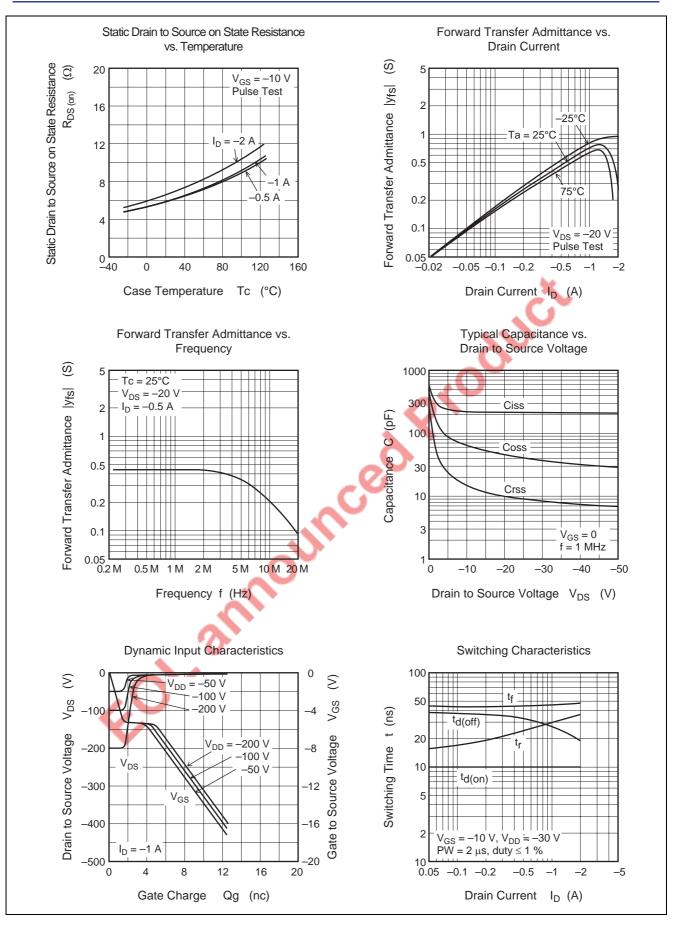
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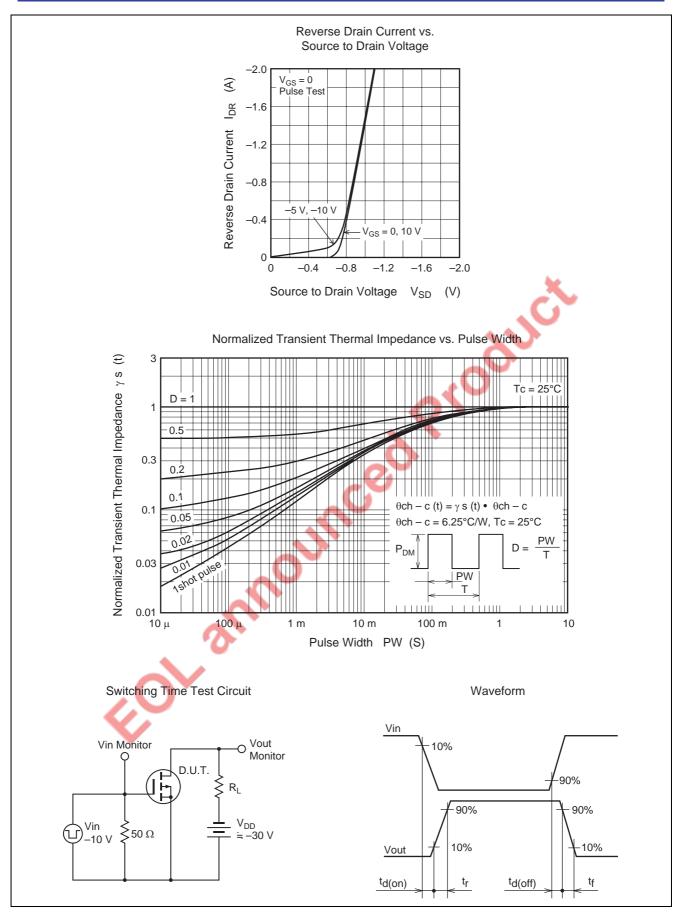
Main Characteristics





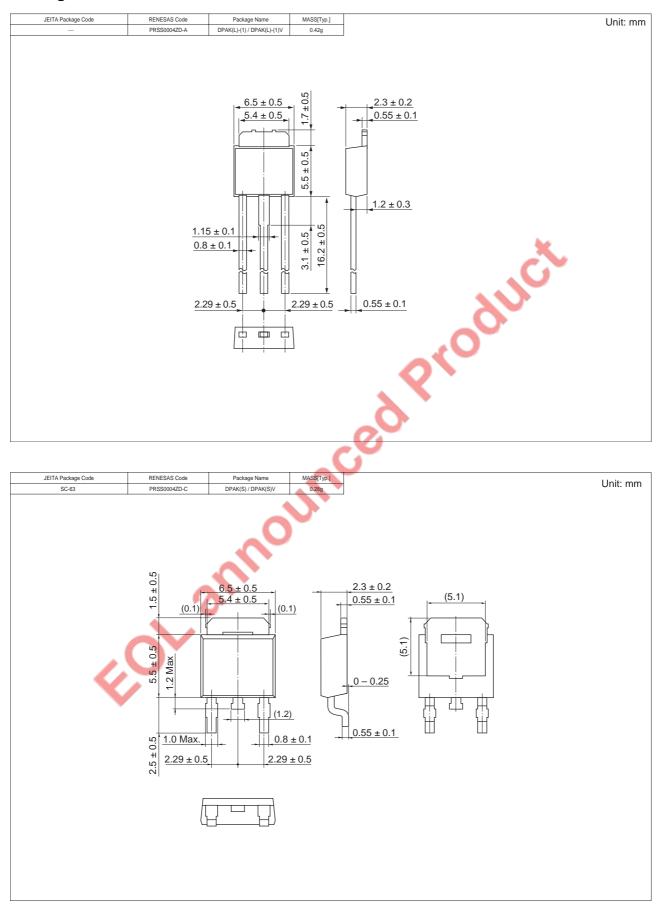






RENESAS

Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SJ130L-E	3200 pcs	Box (Sack)
2SJ130STL-E	3000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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