To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SK1837

Silicon N Channel MOS FET

REJ03G0979-0300
Rev.3.00
May 13, 2009

Application

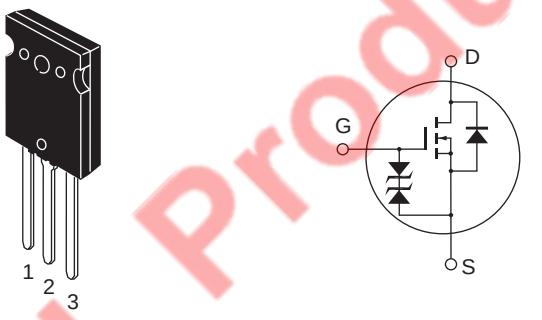
High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter

Outline

RENESAS Package code: PRSS0004ZF-A
(Package name: TO-3PL)



1. Gate
2. Drain (Flange)
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	500	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I_D	50	A
Drain peak current	$I_{D(pulse)}$ [*1]	200	A
Body to drain diode reverse drain current	I_{DR}	50	A
Channel dissipation	Pch [*2]	250	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 µs, duty cycle ≤ 1 %
2. Value at Tc = 25°C

Electrical Characteristics

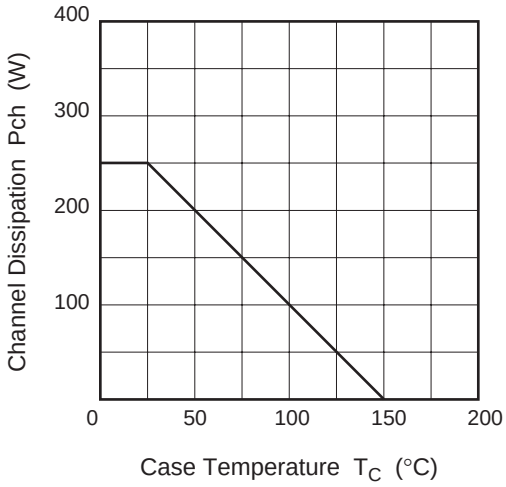
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	500	—	—	V	I_D = 10 mA, V_{GS} = 0
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30	—	—	V	I_G = ±100 µA, V_{DS} = 0
Gate to source leak current	I_{GSS}	—	—	±10	µA	V_{GS} = ±25 V, V_{DS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	250	µA	V_{DS} = 400 V, V_{GS} = 0
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	—	3.0	V	I_D = 1 mA, V_{DS} = 10 V
Static drain to source on state resistance	$R_{DS(on)}$	—	0.085	0.11	Ω	I_D = 25 A, V_{GS}= 10 V*[3]
Forward transfer admittance	$\|y_{fs}\|$	22	35	—	S	I_D = 25 A, V_{DS} = 10 V*[3]
Input capacitance	Ciss	—	8150	—	pF	V_{DS} = 10 V, V_{GS} = 0, f = 1 MHz
Output capacitance	Coss	—	2100	—	pF	
Reverse transfer capacitance	Crss	—	180	—	pF	
Turn-on delay time	$t_{d(on)}$	—	80	—	ns	I_D = 25 A, V_{GS} = 10 V, R_L = 1.2 Ω
Rise time	t_r	—	250	—	ns	
Turn-off delay time	$t_{d(off)}$	—	550	—	ns	
Fall time	t_f	—	220	—	ns	
Body to drain diode forward voltage	V_{DF}	—	1.1	—	V	I_F = 50 A, V_{GS} = 0
Body to drain diode reverse recovery time	t_{rr}	—	620	—	ns	I_F = 50 A, V_{GS} = 0, di_F/dt = 100 A/µs

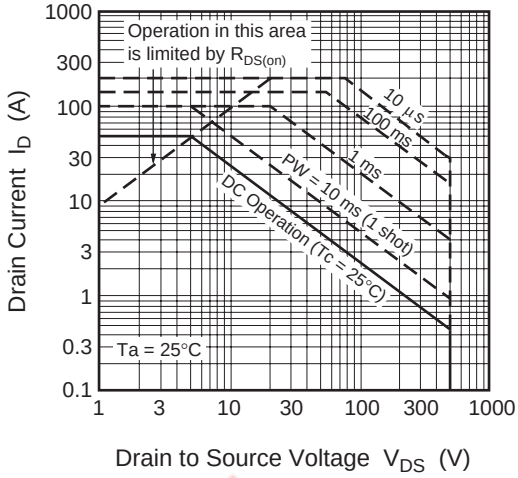
Note: 3. Pulse Test

Main Characteristics

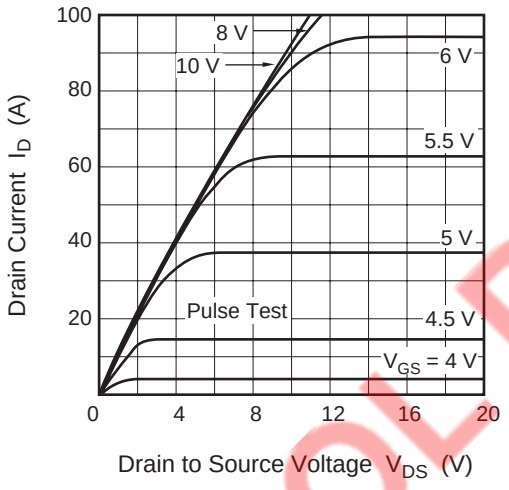
Power vs. Temperature Derating



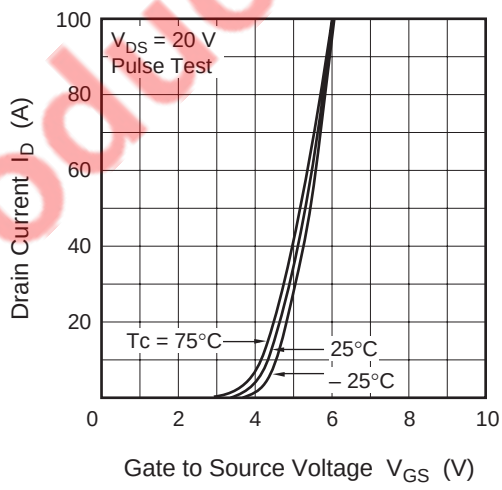
Maximum Safe Operation Area



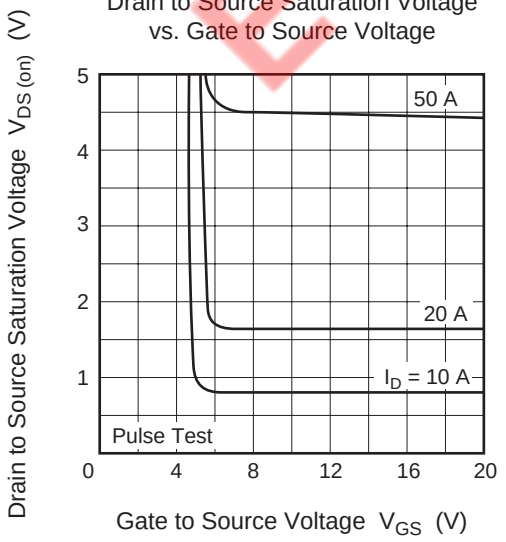
Typical Output Characteristics



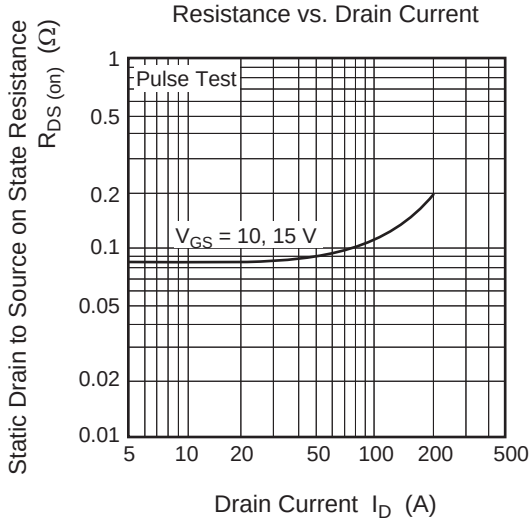
Typical Transfer Characteristics



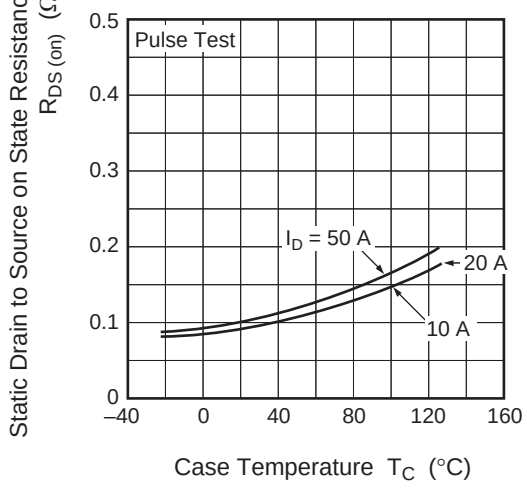
Drain to Source Saturation Voltage vs. Gate to Source Voltage



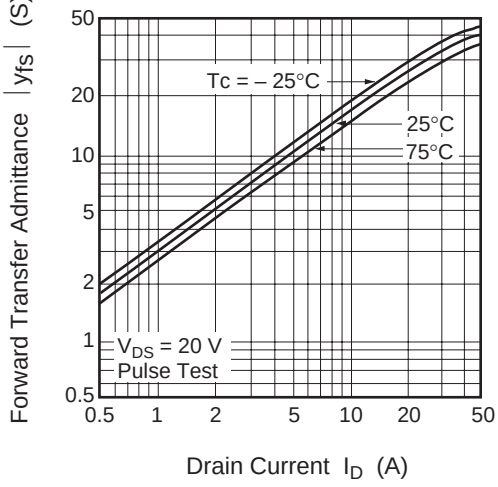
Static Drain to Source on State Resistance vs. Drain Current



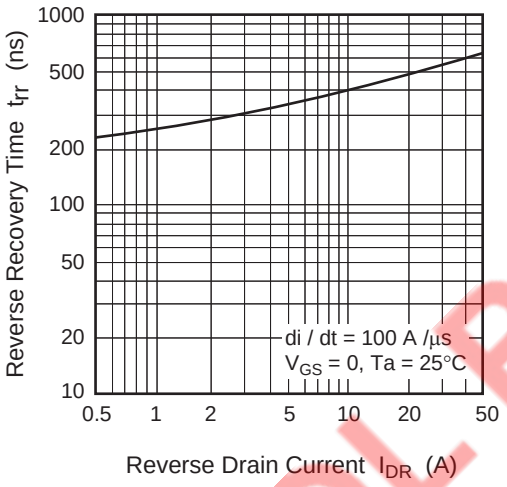
Static Drain to Source on State Resistance vs. Temperature



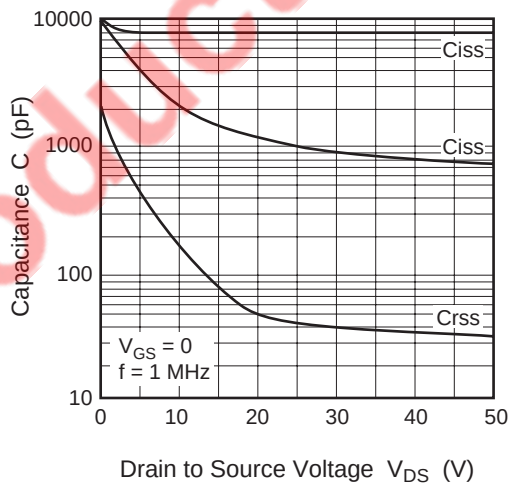
Forward Transfer Admittance vs. Drain Current



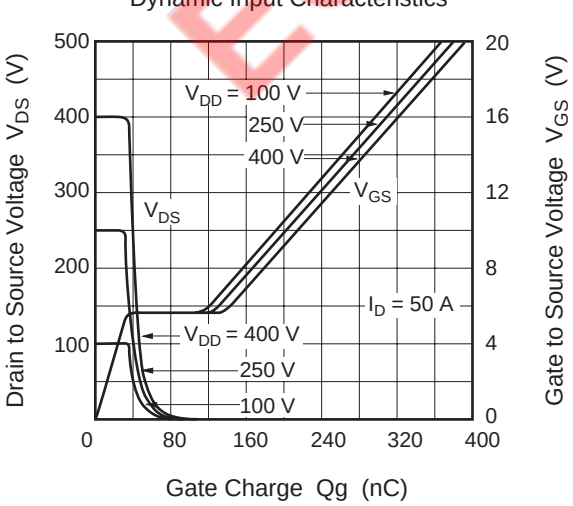
Body to Drain Diode Reverse Recovery Time



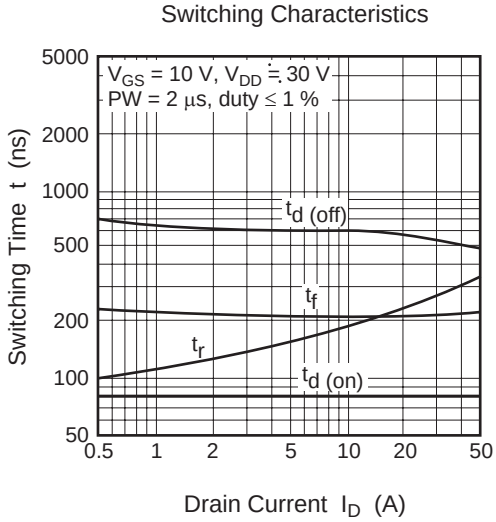
Typical Capacitance vs. Drain to Source Voltage



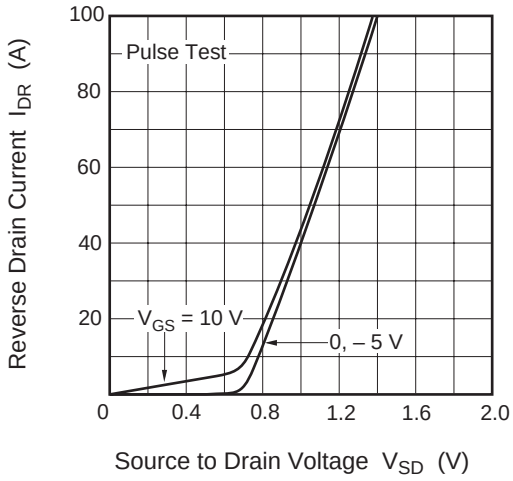
Dynamic Input Characteristics



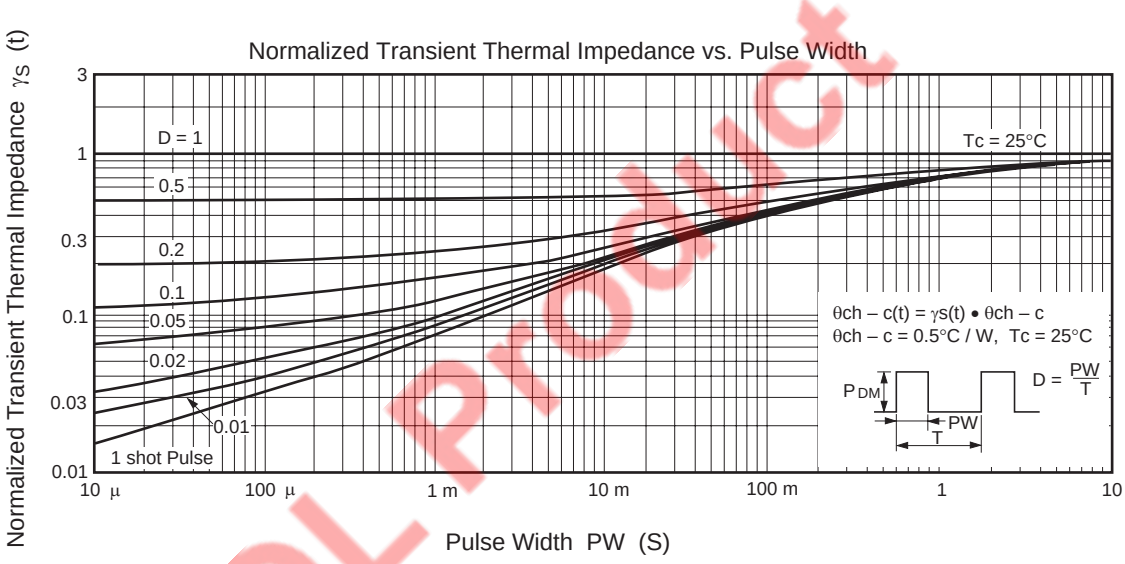
Switching Characteristics



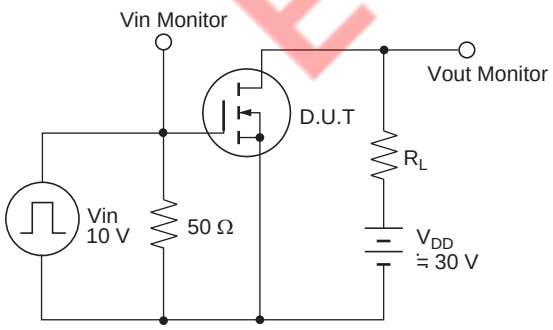
Reverse Drain Current vs.
Source to Drain Voltage



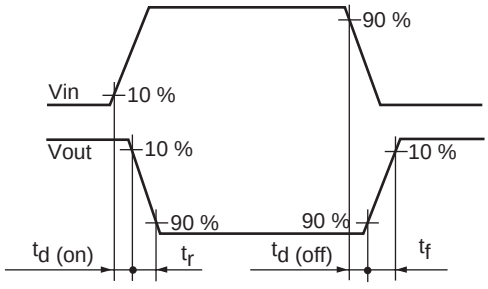
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



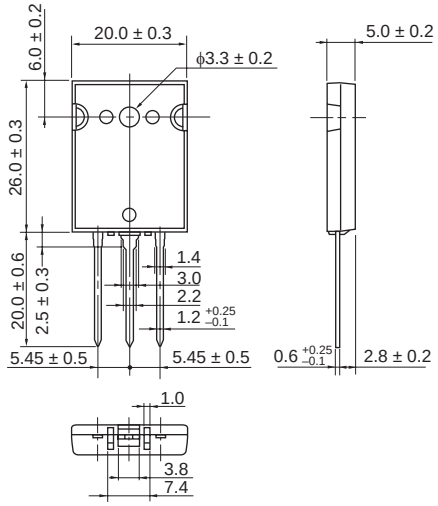
Waveforms



Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
TO-3PL	—	PRSS0004ZF-A	TO-3PL / TO-3PLV	9.9g

Unit: mm



Ordering Information

Part Name	Quantity	Shipping Container
2SK1837-E	250 pcs	Box (Tube)

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