Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SK2144 Silicon N Channel MOS FET

REJ03G1001-0200 (Previous: ADE-208-1349) Rev.2.00 Sep 07,2005

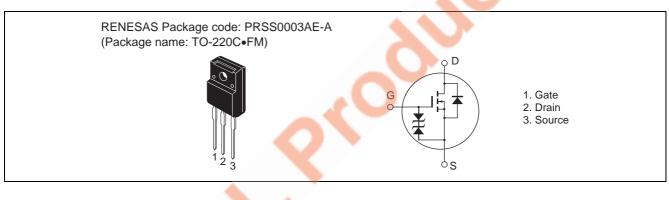
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No Secondary Breakdown
- Suitable for switching regulator, DC-DC converter

Outline





Absolute Maximum Ratings

			(Ta = 25°C)
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	600	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	ID	3	A
Drain peak current	I _{D(pulse)} * ¹	6	A
Body to drain diode reverse drain current	I _{DR}	3	A
Channel dissipation	Pch* ²	25	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1 \ \%$

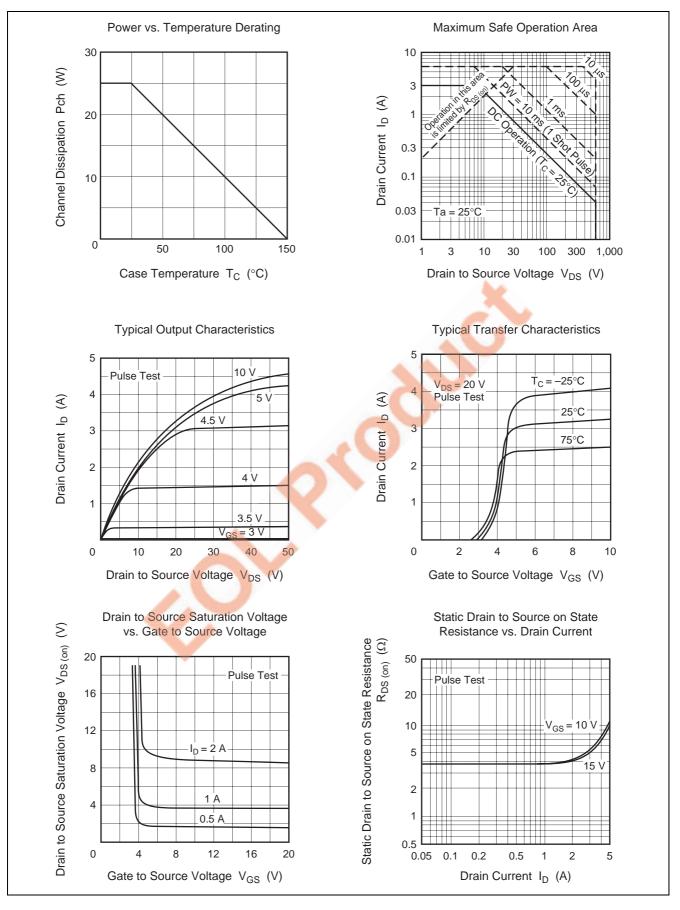
2. Value at $Tc = 25^{\circ}C$

Electrical Characteristics

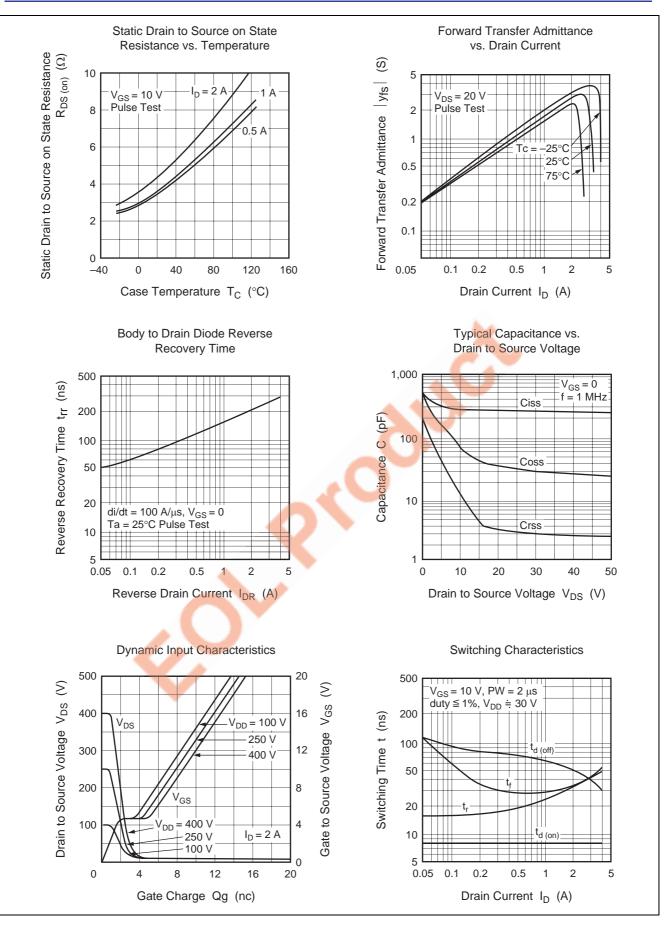
						$(Ta = 25^{\circ}C)$
ltem	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	600	—	—	V	$I_{D} = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	±30	—	—	V	$I_{G} = \pm 100 \ \mu A, V_{DS} = 0$
Gate to source leak current	I _{GSS}	—	—	±10	μA	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	—	250	μA	$V_{DS} = 500 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	2.0	—	3.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to Source on state resistance	R _{DS(on)}	_	3.8	5.0	Ω	$I_D = 1 \text{ A}, V_{GS} = 10 \text{ V}^{*3}$
Forward transfer admittance	y _{fs}	1.2	2.0) —	S	$I_D = 1 \text{ A}, V_{DS} = 10 \text{ V}^{*3}$
Input capacitance	Ciss		295	_	pF	$V_{DS} = 10 V, V_{GS} = 0,$
Output capacitance	Coss		70	—	pF	f = 1 MHz
Reverse transfer capacitance	Crss		12	—	pF	
Turn-on delay time	t _{d(on)}		8	—	ns	$I_{D} = 1 \text{ A}, \text{ V}_{\text{GS}} = 10 \text{ V},$ $R_{\text{L}} = 30 \Omega$
Rise time	tn	_	25	—	ns	
Turn-off delay time	t _{d(off)}		65	—	ns	
Fall time	tr	-	30	—	ns	
Body to drain diode forward voltage	Vdf	_	0.9	_	V	$I_F = 2 A, V_{GS} = 0$
Body to drain diode reverse recovery time	trr		220	_	ns	$I_F = 2 A, V_{GS} = 0,$ $di_F / dt = 100 A / \mu s$
Note: 3. Pulse Test		-	-		-	



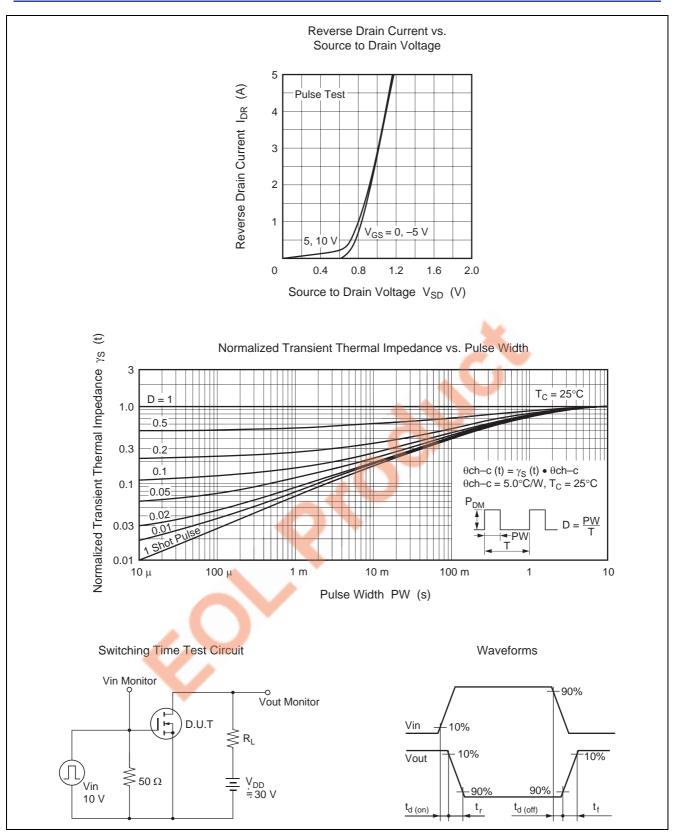
Main Characteristics



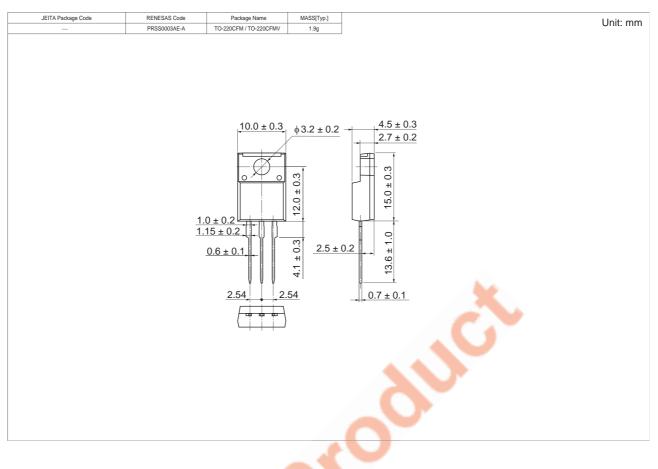








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK2144-E	600 pcs	Box (Tube)

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