Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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BCR30AM-12LA

Triac

Medium Power Use

REJ03G0342-0300 Rev.3.00 Nov 30, 2007

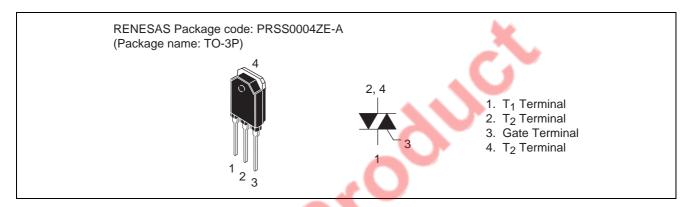
Features

• $I_{T(RMS)}$: 30 A V_{DRM} : 600 V

 $I_{FGT I}$, $I_{RGT I}$, $I_{RGT III}$: 50 mA

- Non-Insulated Type
- Planar Passivation Type

Outline



Applications

Contactless AC switch, electric heater control, light dimmer, on/off and speed control of small induction motor, on/off control of copier lamp

Maximum Ratings

Parameter	Symbol	Voltage class	Unit	
raidiletei	Symbol	12		
Repetitive peak off-state voltage Note1	V_{DRM}	600	V	
Non-repetitive peak off-state voltage Note1	V_{DSM}	720	V	

BCR30AM-12LA

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T(RMS)}	30	A	Commercial frequency, sine full wave, Tc = 75°C
Surge on-state current	I _{TSM}	300	А	60Hz sinewave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	378	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P_{GM}	5	W	
Average gate power dissipation	$P_{G(AV)}$	0.5	W	
Peak gate voltage	V_{GM}	10	V	
Peak gate current	I _{GM}	2	Α	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	4.8	g	Typical value

Notes: 1. Gate open.

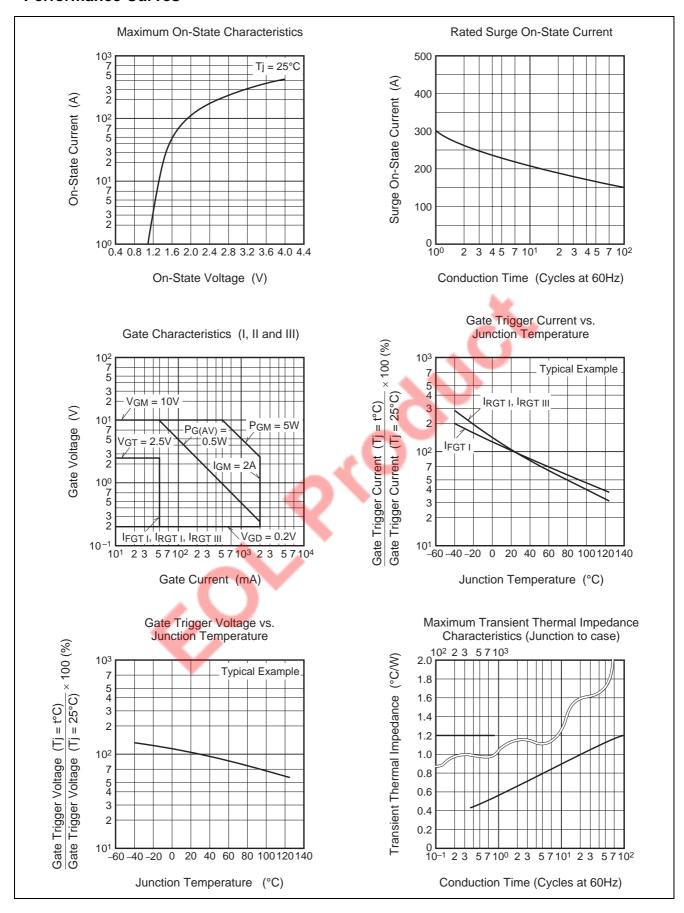
Electrical Characteristics

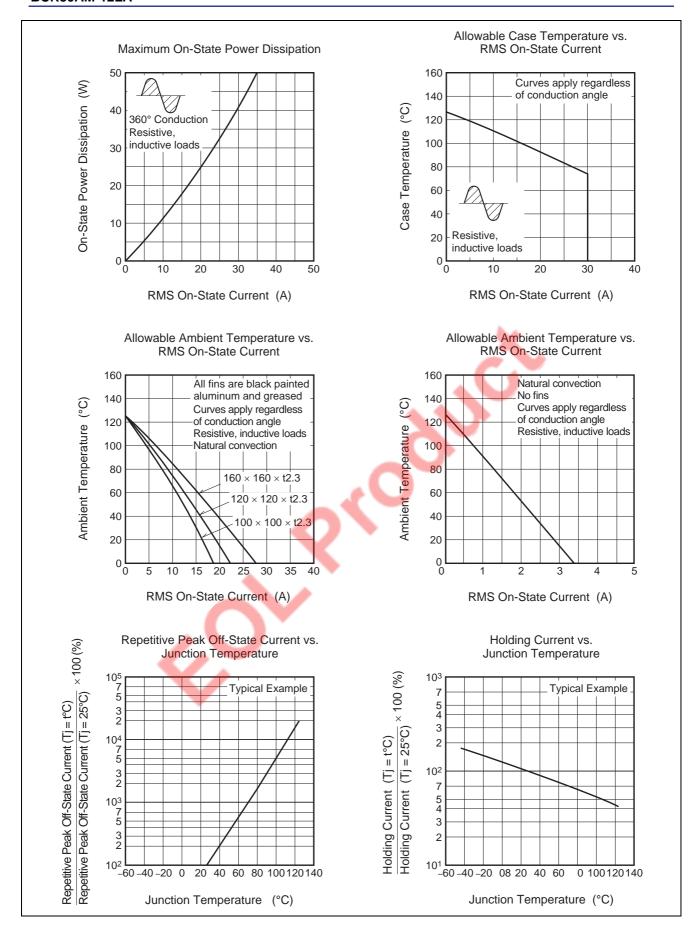
Parameter		Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak off-state current		I _{DRM}	_	_	3.0	mA	Tj = 125°C, V _{DRM} applied
On-state voltage		V_{TM}	_	_	1.6	V	$Tc = 25^{\circ}C, I_{TM} = 45A$
Gate trigger voltage ^{Note2}	I	V_{FGTI}			2.5	V	Tj = 25°C, $V_D = 6 \text{ V}$, $R_L = 6 \Omega$,
	II	V_{RGTI}			2.5	>	$R_G = 330 \Omega$
	III	V_{RGTIII}			2.5	V	
Gate trigger current ^{Note2}	I	I_{FGTI}			50	mA	$Tj = 25$ °C, $V_D = 6$ V, $R_L = 6$ Ω,
	II	I_{RGTI}			50	mA	$R_G = 330 \Omega$
	III	I_{RGTIII}		4	50	mA	
Gate non-trigger voltage		V_{GD}	0.2	J		>	$Tj = 125$ °C, $V_D = 1/2 V_{DRM}$
Thermal resistance		$R_{th(j-c)}$	_	P	1.2	°C/W	Junction to case ^{Note3}
Critical-rate of rise of off-state commutating voltage ^{Note4}		(dv/dt)c	20	_	_	V/μs	Tj = 125°C

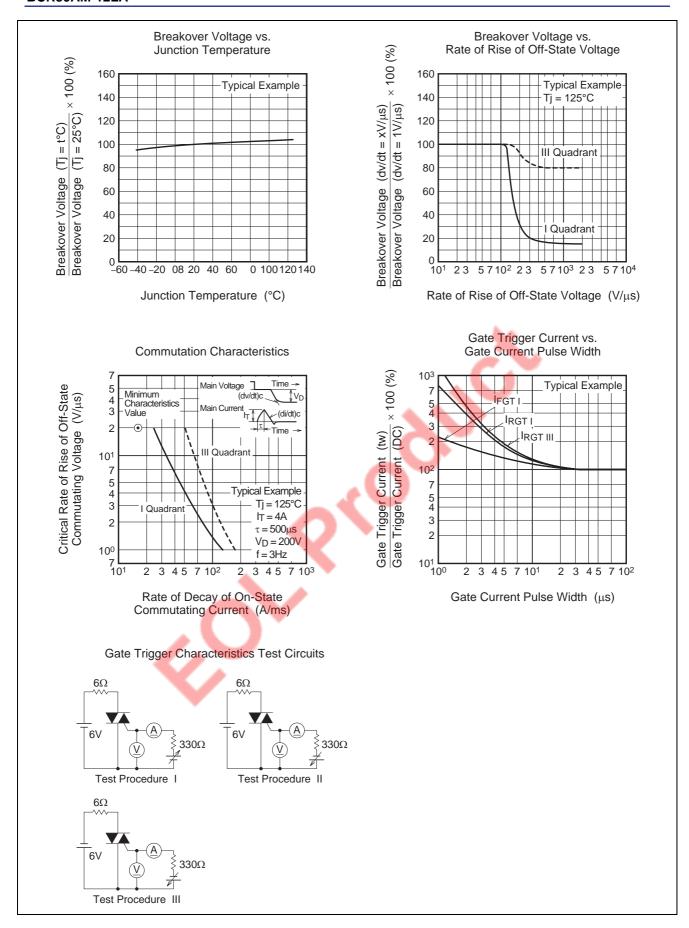
- Notes: 2. Measurement using the gate trigger characteristics measurement circuit.
 - 3. The contact thermal resistance $R_{\text{th (c-f)}}$ in case of greasing is 0.3°C/W.
 - 4. Test conditions of the critical-rate of rise of off-state commutating voltage is shown in the table below.

Test conditions	Commutating voltage and current waveforms inductive load
1. Junction temperature Tj = 125°C	Supply Voltage →Time
 2. Rate of decay of on-state commutating current (di/dt)c = -16 A/ms 3. Peak off-state voltage V_D = 400V 	Main Current → Time Main Voltage → Time (dv/dt)c ∨ V _D

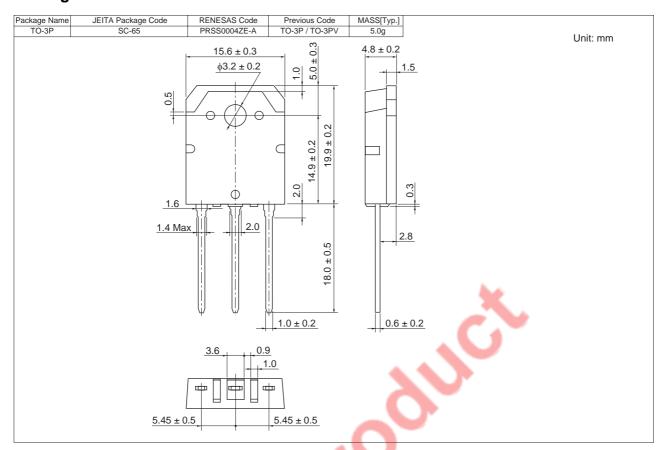
Performance Curves







Package Dimensions



Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Straight type	Vinyl sack	20	Type name	BCR30AM-12LA
Lead form	Plastic Magazine (Tube)	30	Type name – Lead forming code	BCR30AM-12LA-A8

Note: Please confirm the specification about the shipping in detail.

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