To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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CR05AM-12

Thyristor

Low Power Use

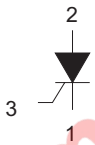
REJ03G0353-0100
Rev.1.00
Aug.20.2004

Features

- $I_{T\,(AV)}$: 0.3 A
- V_{DRM} : 600 V
- I_{GT} : 100 µA
- Non-Insulated Type
- Glass Passivation Type

Outline

TO-92



1. Cathode
2. Anode
3. Gate

Applications

Leakage protector, timer, and gas igniter

Maximum Ratings

Parameter	Symbol	Voltage class	Unit
		12	
Repetitive peak reverse voltage	V_{RRM}	600	V
Non-repetitive peak reverse voltage	V_{RSM}	800	V
DC reverse voltage	$V_{R(DC)}$	480	V
Repetitive peak off-state voltage Note1	V_{DRM}	600	V
Non-repetitive peak off-state voltage Note1	V_{DSM}	800	V
DC off-state voltage Note1	$V_{D(DC)}$	480	V

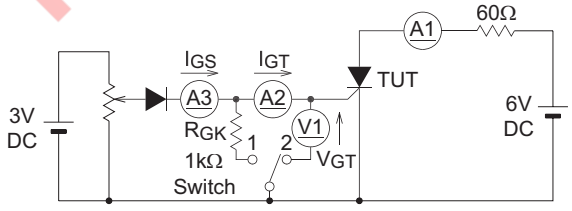
Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	$I_{T\ (RMS)}$	0.47	A	
Average on-state current	$I_{T\ (AV)}$	0.3	A	Commercial frequency, sine half wave 180° conduction, Ta = 47°C
Surge on-state current	I_{TSM}	10	A	60Hz sine half wave 1 full cycle, peak value, non-repetitive
I^2t for fusing	I^2t	0.4	A^2s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P_{GM}	0.5	W	
Average gate power dissipation	$P_{G\ (AV)}$	0.1	W	
Peak gate forward voltage	V_{FGM}	6	V	
Peak gate reverse voltage	V_{RGM}	6	V	
Peak gate forward current	I_{FGM}	0.3	A	
Junction temperature	Tj	– 40 to +110	°C	
Storage temperature	Tstg	– 40 to +125	°C	
Mass	—	0.23	g	Typical value

Notes: 1. With gate to cathode resistance R_{GK} = 1 kΩ.

Electrical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test conditions
Repetitive peak reverse current	I_{RRM}	—	—	0.1	mA	Tj = 110°C, V_{RRM} applied
Repetitive peak off-state current	I_{DRM}	—	—	0.1	mA	Tj = 110°C, V_{DRM} applied, R_{GK} = 1 kΩ
On-state voltage	V_{TM}	—	—	1.8	V	Ta = 25°C, I_{TM} = 4 A, instantaneous value
Gate trigger voltage	V_{GT}	—	—	0.8	V	Tj = 25°C, V_D = 6 V, I_T = 0.1 A Note2
Gate non-trigger voltage	V_{GD}	0.2	—	—	V	Tj = 110°C, V_D = 1/2 V_{DRM}, R_{GK} = 1 kΩ
Gate trigger current	I_{GT}	1	—	100	µA	Tj = 25°C, V_D = 6 V, I_T = 0.1 A Note2
Holding current	I_H	—	1.5	3	mA	Tj = 25°C, V_D = 12 V, R_{GK} = 1 kΩ
Thermal resistance	$R_{th\ (j-a)}$	—	—	180	°C/W	Junction to ambient

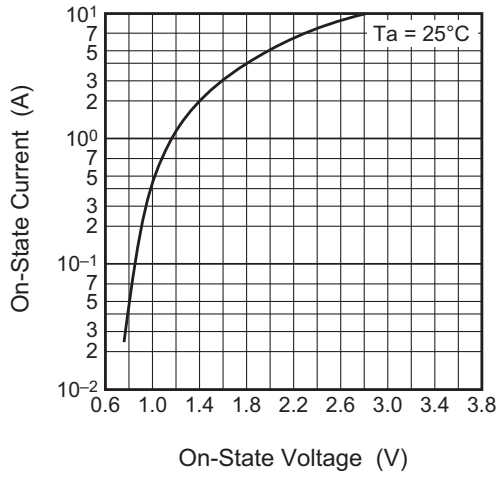
Notes: 2. I_{GT}, V_{GT} measurement circuit.



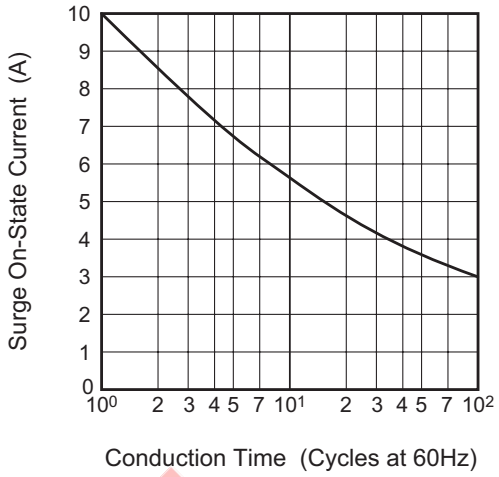
Switch 1 : I_{GT} measurement
Switch 2 : V_{GT} measurement
(Inner resistance of voltage meter is about 1kΩ)

Performance Curves

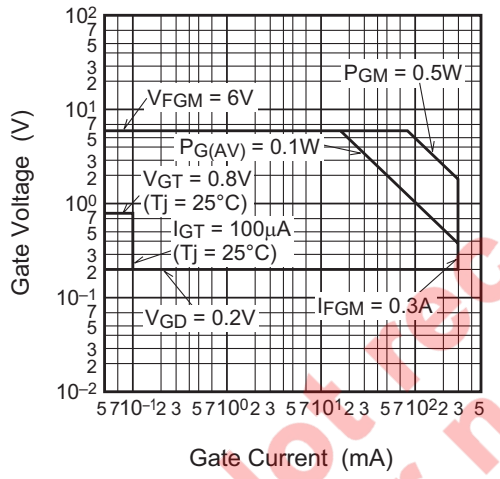
Maximum On-State Characteristics



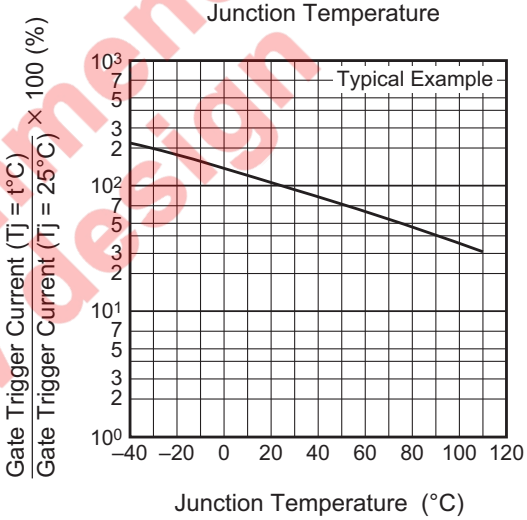
Rated Surge On-State Current



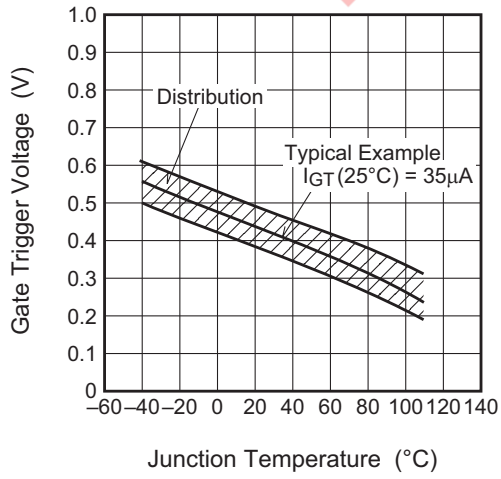
Gate Characteristics



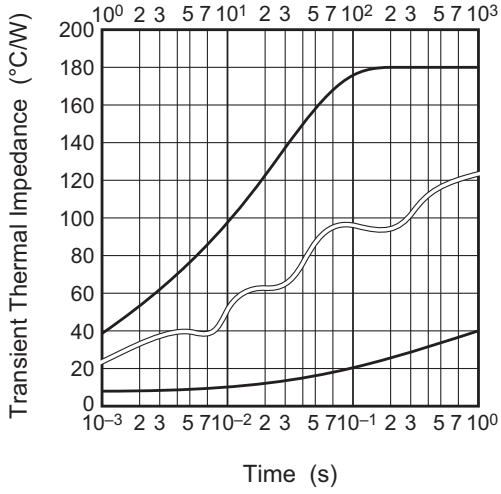
Gate Trigger Current vs. Junction Temperature



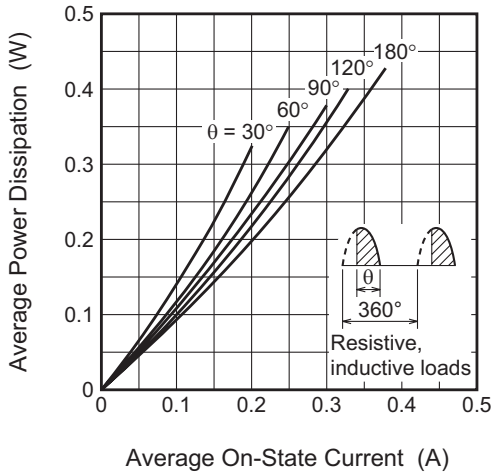
Gate Trigger Voltage vs. Junction Temperature



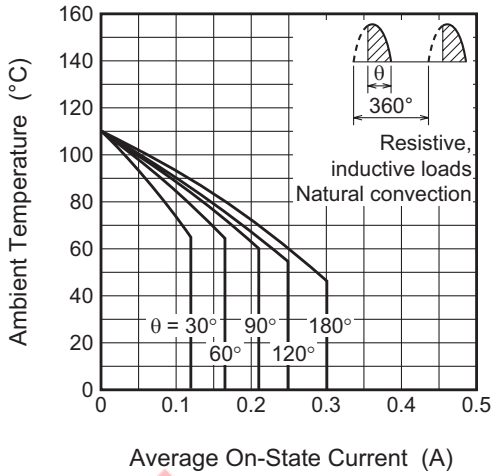
Maximum Transient Thermal Impedance Characteristics (Junction to ambient)



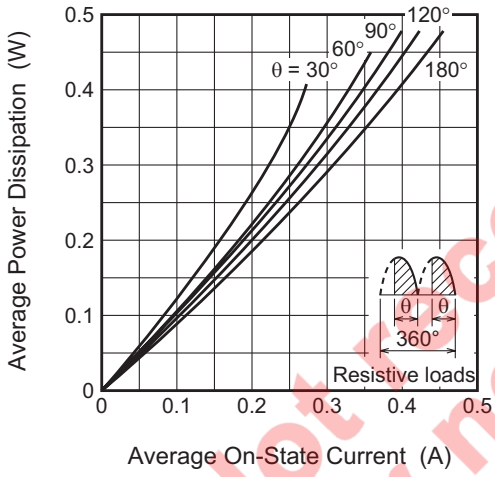
Maximum Average Power Dissipation
(Single-Phase Half Wave)



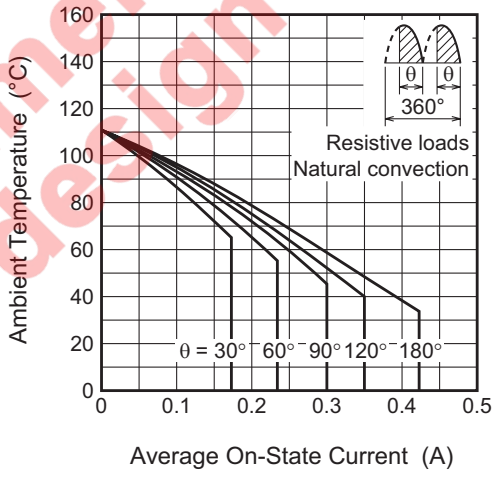
Allowable Ambient Temperature vs.
Average On-State Current
(Single-Phase Half Wave)



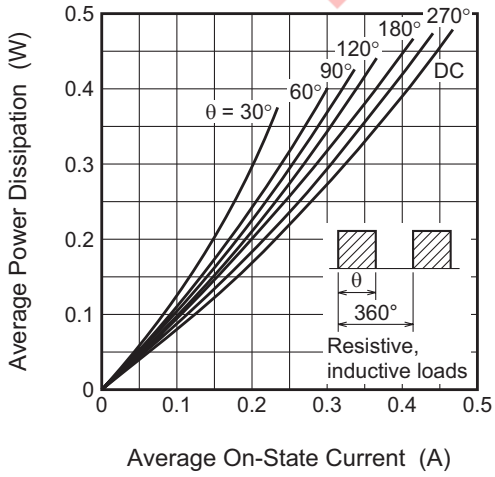
Maximum Average Power Dissipation
(Single-Phase Full Wave)



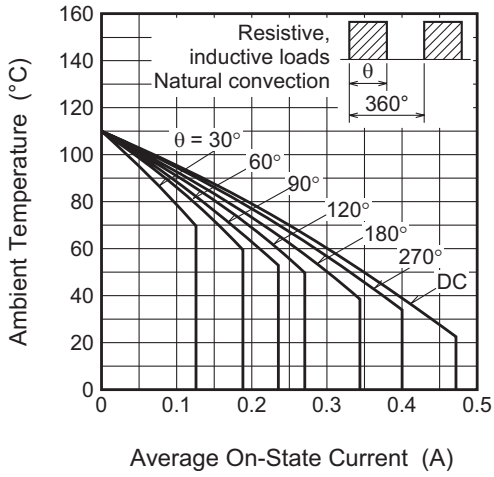
Allowable Ambient Temperature vs.
Average On-State Current
(Single-Phase Full Wave)



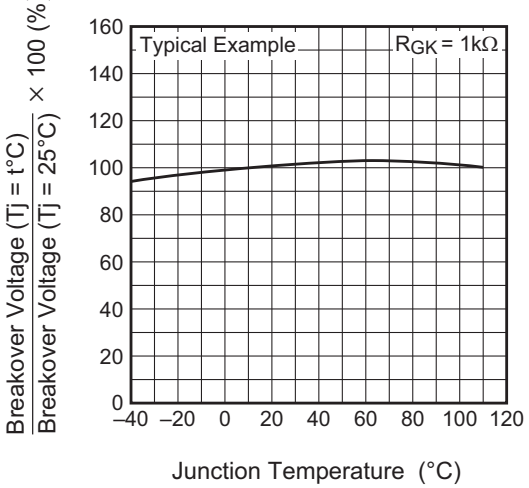
Maximum Average Power Dissipation
(Rectangular Wave)



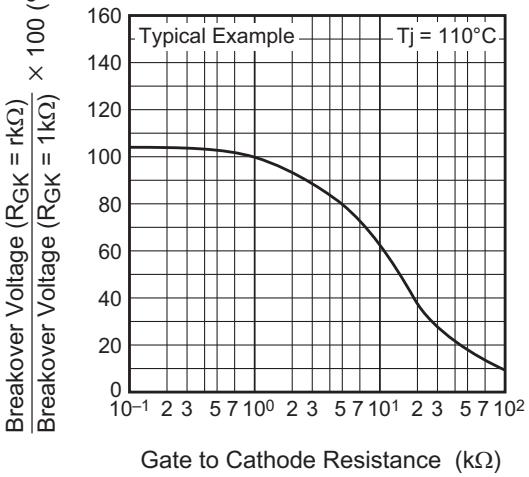
Allowable Ambient Temperature vs.
Average On-State Current
(Rectangular Wave)



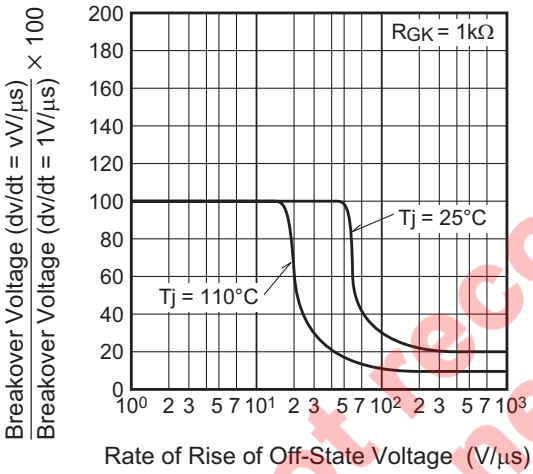
Breakover Voltage vs.
Junction Temperature



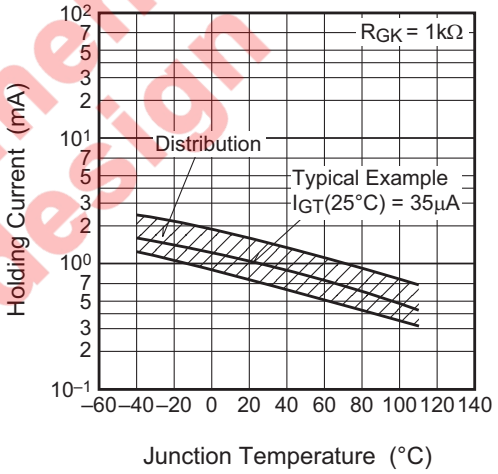
Breakover Voltage vs.
Gate to Cathode Resistance



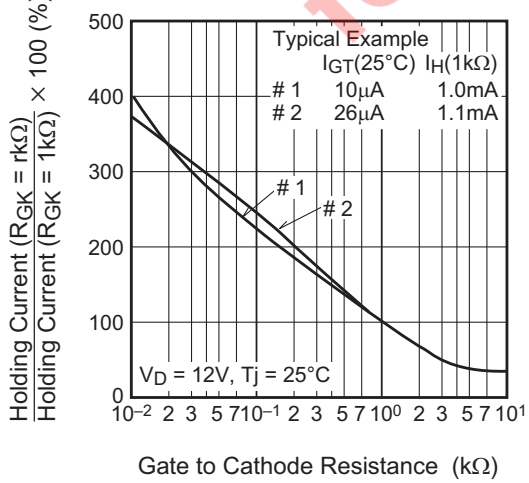
Breakover Voltage vs.
Rate of Rise of Off-State Voltage



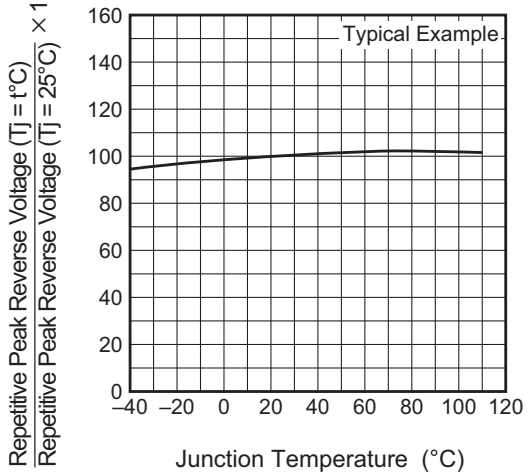
Holding Current vs.
Junction Temperature



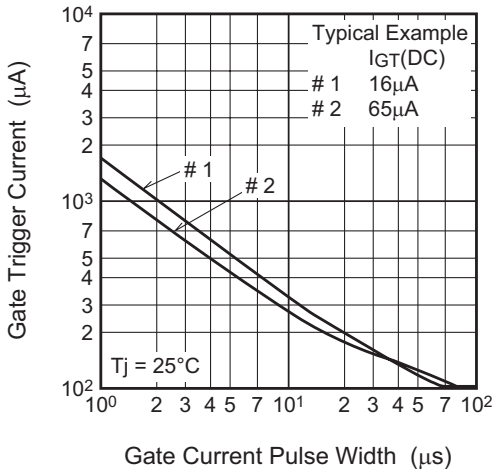
Holding Current vs.
Gate to Cathode Resistance



Repetitive Peak Reverse Voltage vs.
Junction Temperature



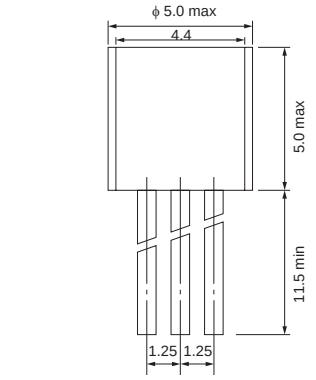
Gate Trigger Current vs.
Gate Current Pulse Width

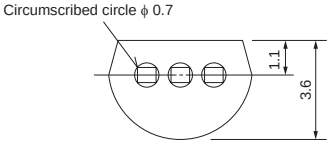


Package Dimensions

TO-92

EIAJ Package Code	JEDEC Code	Mass (g) (reference value)	Lead Material
Conforms	Conforms	0.23	Cu alloy





Note 1) The dimensional figures indicate representative values unless otherwise the tolerance is specified.

Symbol	Dimension in Millimeters		
	Min	Typ	Max
A			
A_1			
A_2			
b			
D			
E			
e			
x			
y			
y_1			
ZD			
ZE			

Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Straight type	Vinyl sack	500	Type name	CR05AM-12
Lead form	Vinyl sack	500	Type name – Lead forming code	CR05AM-12-A6
Form A8	Taping	2000	Type name – TB	CR05AM-12-TB

Note : Please confirm the specification about the shipping in detail.

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