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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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CY25BAH-8F

Nch IGBT for Strobe Flash

REJ03G0284-0300 Rev.3.00 Nov 29, 2005

Features

• Small surface mount package (TSSOP-8)

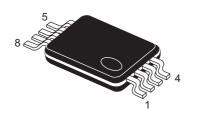
Terminal Pb free: PTSP0008JA-A (8P2J-A)Complete Pb free: PTSP0008JB-B (TTP-8DV)

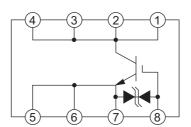
V_{CES}: 400 V
 I_{CM}: 150 A

• Drive voltage: 2.5 V

Outline

TSSOP-8





1,2,3,4 : Collector 5,6 : Emitter 7 : Emitter

(for the gate drive)

8 : Gate

Note: Pin 7 is for the gate drive only.

Note that current from the main circuit cannot flow into this section. (Please see page 3.)

Applications

Strobe flash for cameras

Maximum Ratings

 $(Tc = 25^{\circ}C)$

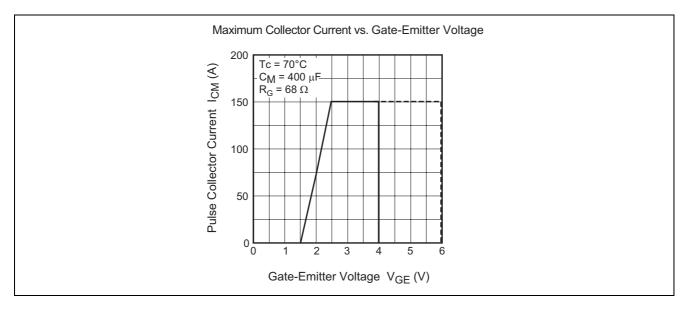
Parameter	Symbol	Ratings	Unit	Conditions
Collector-emitter voltage	V _{CES}	400	V	V _{GE} = 0 V
Gate-emitter voltage	V_{GES}	±4	V	V _{CE} = 0 V
Peak gate-emitter voltage	V_{GEM}	±6	V	$V_{CE} = 0 \text{ V}, \text{ tw} = 10 \text{ s}$
Collector current (Pulse)	I _{CM}	150	Α	C _M = 400 μF
				(see performance curve)
Junction temperature	Tj	- 40 to +150	°C	
Storage temperature	Tstg	- 40 to +150	°C	

Electrical Characteristics

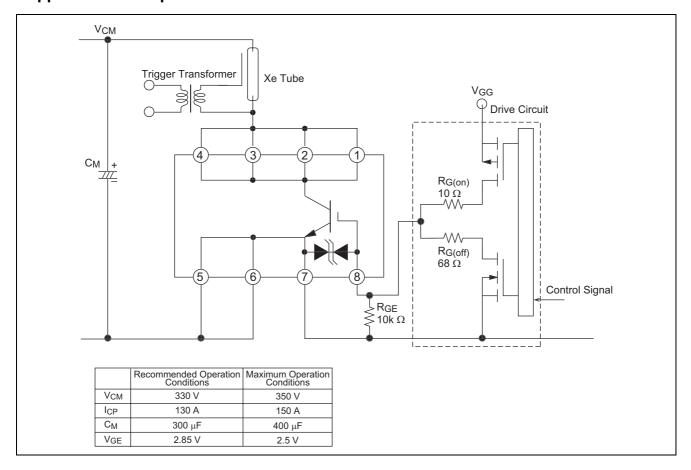
 $(Tj = 25^{\circ}C)$

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
Collector-emitter breakdown voltage	$V_{(BR)CES}$	450	_	_	V	$I_C = 1 \text{ mA}, V_{GE} = 0 \text{ V}$
Collector-emitter leakage current	I _{CES}	_	_	10	μΑ	$V_{CE} = 400 \text{ V}, V_{GE} = 0 \text{ V}$
Gate-emitter leakage current	I_{GES}	_	_	±10	μΑ	$V_{GE} = \pm 6 \text{ V}, V_{CE} = 0 \text{ V}$
Gate-emitter threshold voltage	$V_{\text{GE(th)}}$	0.4	0.6	1.2	V	$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ mA}$
Collector-emitter saturation voltage	$V_{CE(sat)}$	_	3.5	7.0	V	$I_C = 150 \text{ A}, V_{GE} = 2.5 \text{ V}$
Input capacitance	Cies	_	6500	_	pF	$V_{CE} = 25 \text{ V}, V_{GE} = 10 \text{ V},$
						f = 1MHz

Performance Curves



Application Example



Precautions on Usage

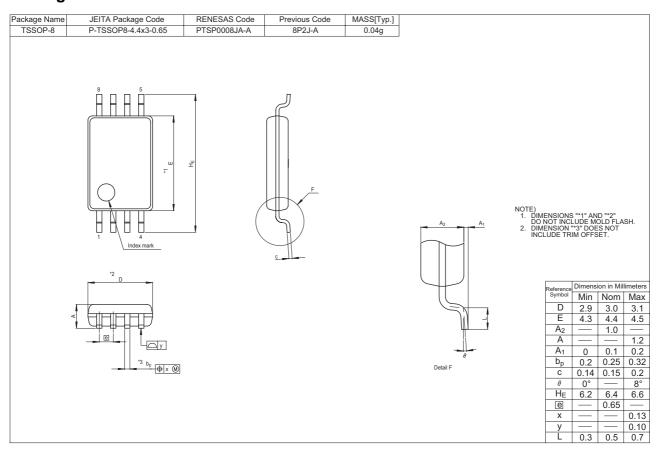
- 1. IGBT has MOS structure and its gate is insulated by thin silicon oxide. So please handle carefully to protect the device from electrostatic charge.
- 2. Gate drive voltage during on-period must be applied to satisfy the rating of maximum pulse collector current. And peak reverse gate current during turn-off must become less than 25 mA. (In general, when $R_{G \text{ (off)}} = 68\Omega$, it is satisfied.)
- 3. The ground of the drive signal must be connected to pin 7 only. If the emitter terminal pins 5 and 6 in which a large currents flow are given to the device as the drive signal emitter, the device may be damaged due to large currents since the specified gate voltage is not applied to the IGBT within the device.
- 4. The operation life should be endured 5,000 shots under the charge current ($I_{Xe} \le 150 \text{ A}$: full luminescence condition) of main capacitor ($C_M = 400 \ \mu\text{F}$) which can endure repeated discharge of 5,000 times. Repetition period under full luminescence condition is over 3 seconds.
- 5. Total operation hours applied to the gate-emitter voltage must be within 5,000 hours when V_{GE} is driven at 4 V...

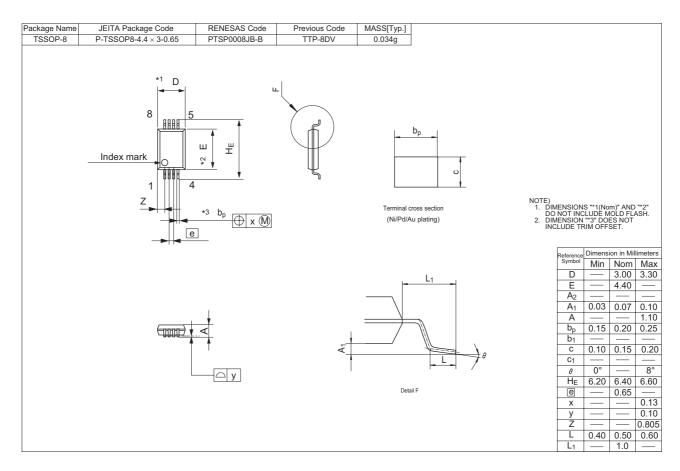
Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Surface-mounted type	Taping	3000	Type name – T +Direction (1 or 2) +3	CY25BAH-8F-T13

Note: Please confirm the specification about the shipping in detail.

Package Dimensions





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