To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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FS10VS-9A

High-Speed Switching Use
Nch Power MOS FET

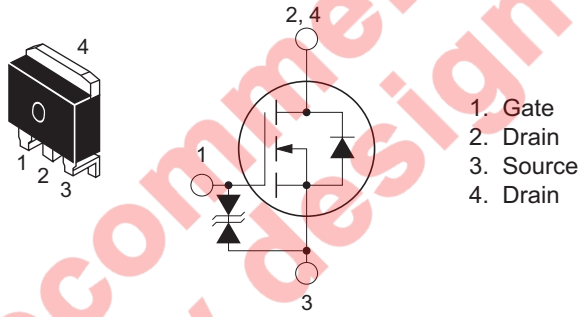
REJ03G0268-0100
Under development
Rev.1.00
Aug.20.2004

Features

- Drive voltage : 10 V
- V_{DSS} : 450 V
- $r_{DS(ON)\ (max)}$: 0.73 Ω
- I_D : 10 A

Outline

LDPAK(S)-1



Applications

DC-DC, PDP, lamp ballast, etc.

Maximum Ratings

(Tc = 25°C)

Parameter	Symbol	Ratings	Unit	Conditions
Drain-source voltage	V_{DSS}	450	V	V_{GS} = 0 V
Gate-source voltage	V_{GSS}	±30	V	V_{DS} = 0 V
Drain current	I_D	10	A	
Drain current (Pulsed)	I_{DM}	30	A	
Avalanche current	I_{DA}	10	A	L = 200 µH
Maximum power dissipation	P_D	100	W	
Channel temperature	Tch	– 55 to +150	°C	
Storage temperature	Tstg	– 55 to +150	°C	
Mass	—	1.2	g	Typical value

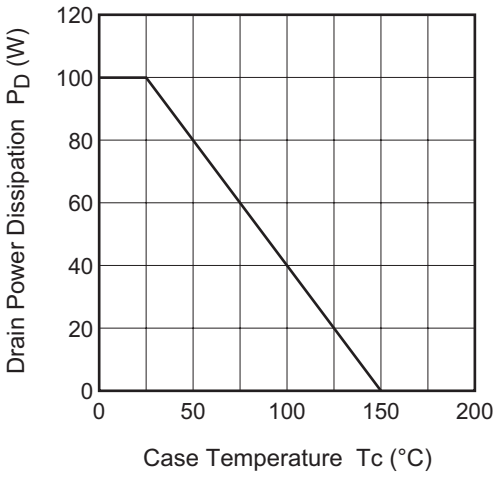
Electrical Characteristics

(Tch = 25°C)

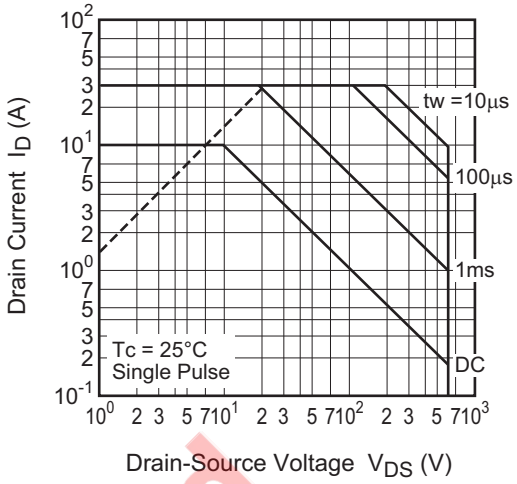
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test conditions
Drain-source breakdown voltage	$V_{(BR)DSS}$	450	—	—	V	I_D = 1 mA, V_{GS} = 0 V
Gate-source breakdown voltage	$V_{(BR)GSS}$	±30	—	—	V	I_G = ±100 µA, V_{DS} = 0 V
Gate-source leakage current	I_{GSS}	—	—	±10	µA	V_{GS} = ±25 V, V_{DS} = 0 V
Drain-source leakage current	I_{DSS}	—	—	1.0	mA	V_{DS} = 450 V, V_{GS} = 0 V
Gate-source threshold voltage	$V_{GS(th)}$	2.5	3.0	3.5	V	I_D = 1 mA, V_{DS} = 10 V
Drain-source on-state resistance	$r_{DS(ON)}$	—	0.58	0.73	Ω	I_D = 5 A, V_{GS} = 10 V
Drain-source on-state voltage	$V_{DS(ON)}$	—	2.90	3.65	V	I_D = 5 A, V_{GS} = 10 V
Forward transfer admittance	$\lvert y_{fs} \rvert$	5.4	9.0	—	S	I_D = 5 A, V_{DS} = 10 V
Input capacitance	Ciss	—	1100	—	pF	V_{DS} = 25 V, V_{GS} = 0 V, f = 1MHz
Output capacitance	Coss	—	120	—	pF	
Reverse transfer capacitance	Crss	—	25	—	pF	
Turn-on delay time	$t_{d(on)}$	—	20	—	ns	V_{DD} = 200 V, I_D = 5 A, V_{GS} = 10 V, R_{GEN} = R_{GS} = 50 Ω
Rise time	t_r	—	30	—	ns	
Turn-off delay time	$t_{d(off)}$	—	140	—	ns	
Fall time	t_f	—	40	—	ns	
Source-drain voltage	V_{SD}	—	1.5	2.0	V	I_S = 5 A, V_{GS} = 0 V
Thermal resistance	Rth(ch-c)	—	—	1.25	°C/W	Channel to case

Performance Curves

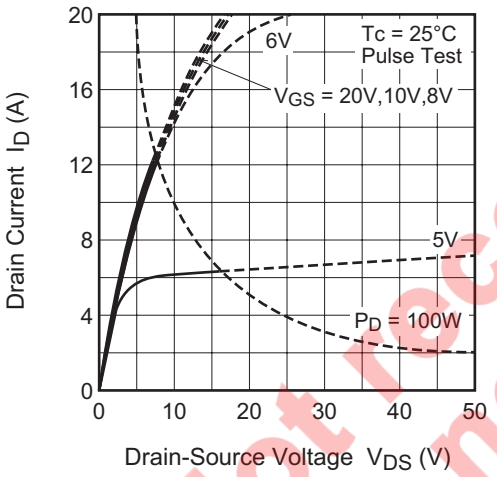
Drain Power Dissipation Derating Curve



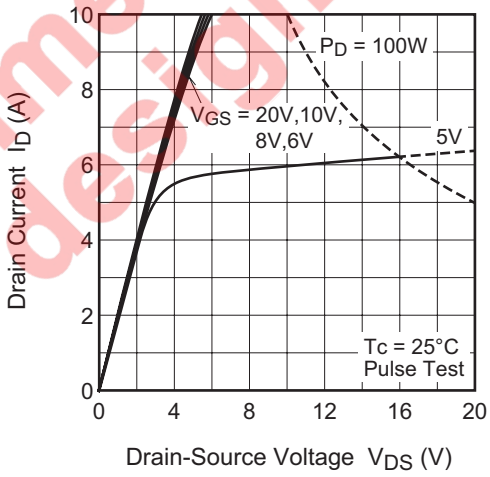
Maximum Safe Operating Area



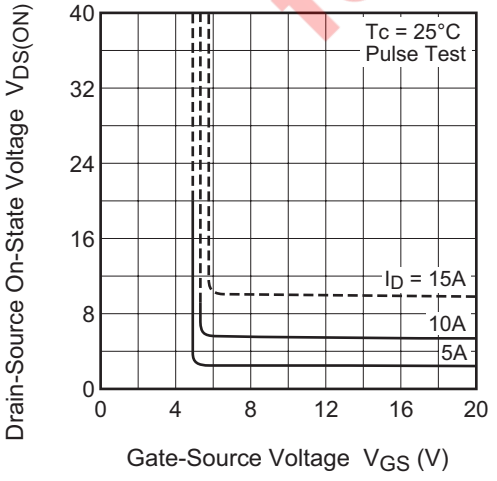
Output Characteristics (Typical)



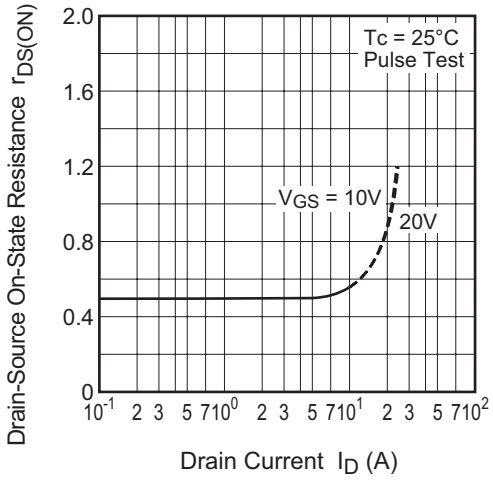
Output Characteristics (Typical)



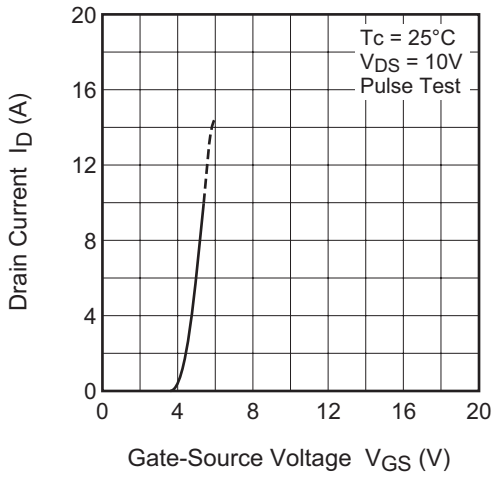
On-State Voltage vs. Gate-Source Voltage (Typical)



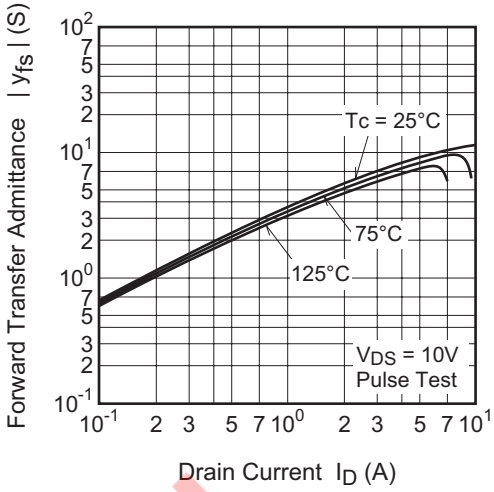
On-State Resistance vs. Drain Current (Typical)



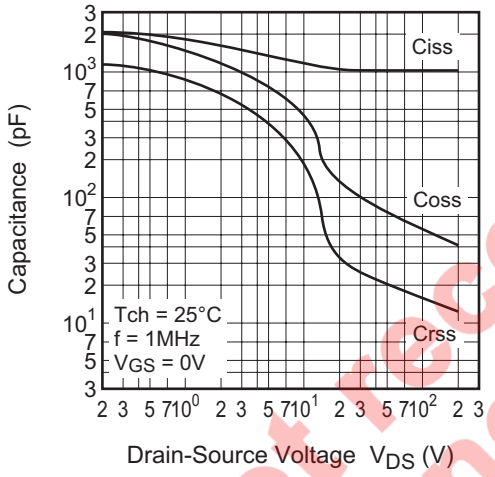
Transfer Characteristics (Typical)



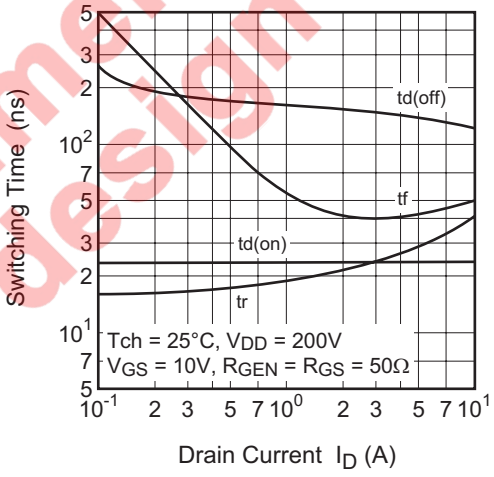
Forward Transfer Admittance vs. Drain Current (Typical)



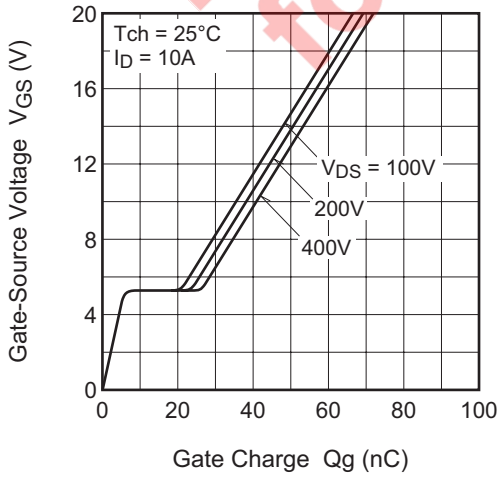
Capacitance vs. Drain-Source Voltage (Typical)



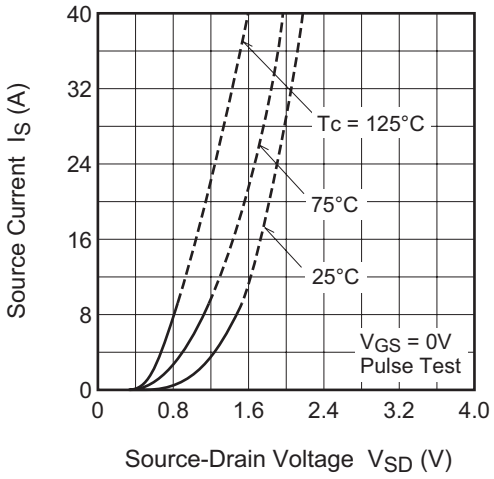
Switching Characteristics (Typical)



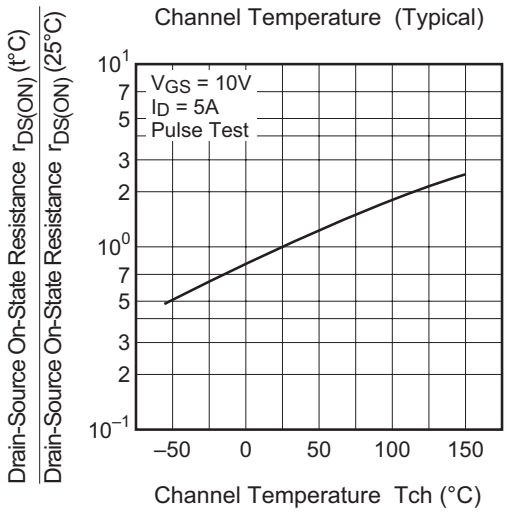
Gate-Source Voltage vs. Gate Charge (Typical)



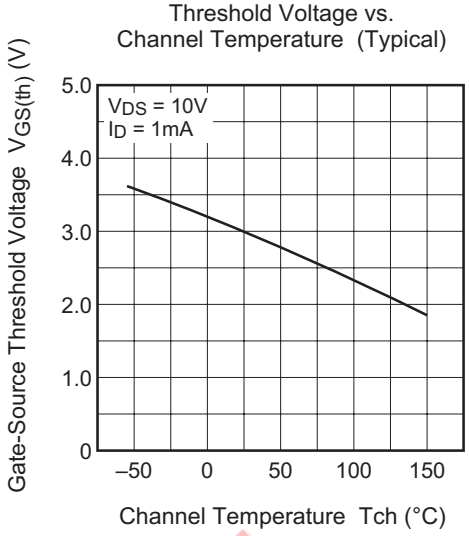
Source-Drain Diode Forward Characteristics (Typical)



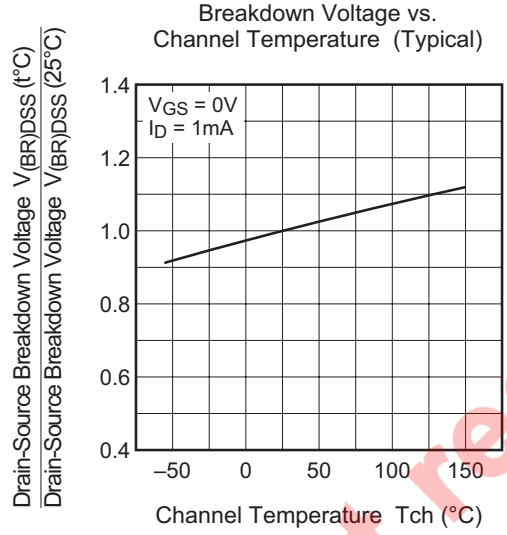
On-State Resistance vs. Channel Temperature (Typical)



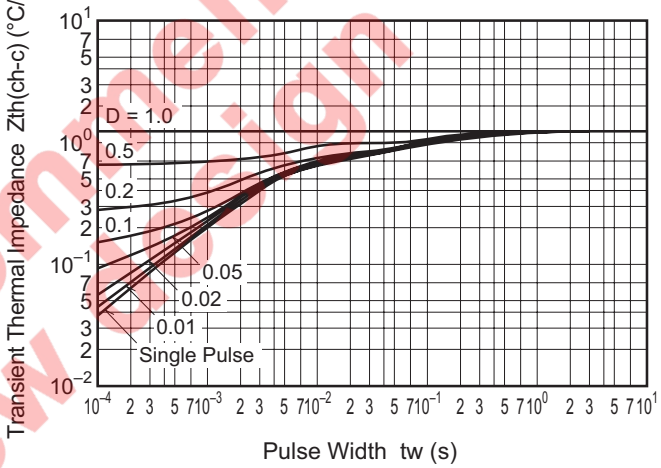
Threshold Voltage vs. Channel Temperature (Typical)



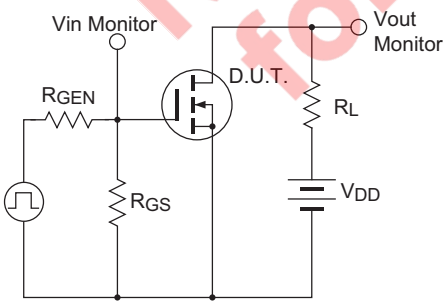
Breakdown Voltage vs. Channel Temperature (Typical)



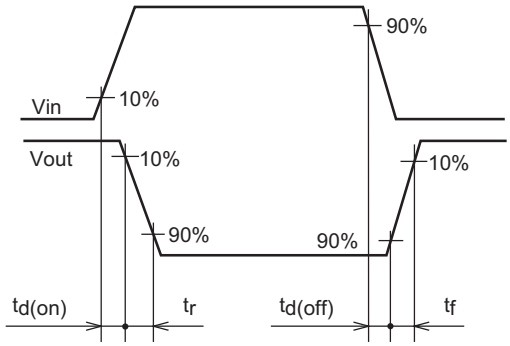
Transient Thermal Impedance Characteristics



Switching Time Measurement Circuit

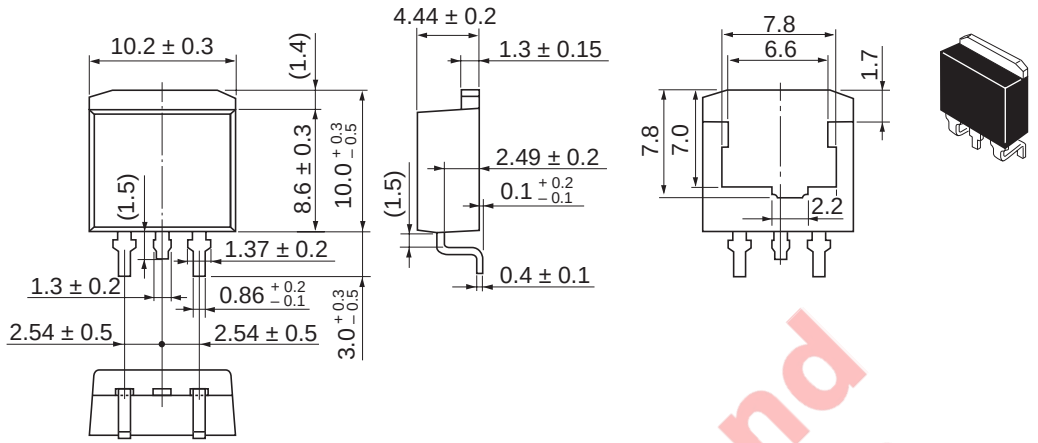


Switching Waveform



Package Dimensions

As of January, 2003
Unit: mm



Package Code	LDPAK (S)-(1)
JEDEC	—
JEITA	—
Mass (reference value)	1.30 g

Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Surface-mounted type	Taping	1000	Type name – T +Direction (1 or 2) +1	FS10VS-9A-T11

Note : Please confirm the specification about the shipping in detail.

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Colophon .1.0