

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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H5N5007P

Silicon N Channel MOS FET
High Speed Power Switching

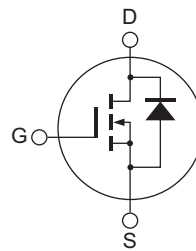
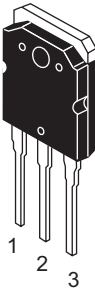
REJ03G1116-0400
(Previous: ADE-208-1404B)
Rev.4.00
Sep 07, 2005

Features

- Low on-resistance
- Low leakage current
- High speed switching
- Low gate charge

Outline

RENESAS Package code: PRSS0004ZE-A
(Package name: TO-3P)



1. Gate
2. Drain (Flange)
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	500	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	25	A
Drain peak current	I _{D (pulse)} ^{Note 1}	100	A
Body-drain diode reverse drain current	I _{DR}	25	A
Avalanche current	I _{AP} ^{Note 3}	7	A
Channel dissipation	P _{ch} ^{Note 2}	150	W
Channel to case thermal Impedance	θ ch-c	0.833	°C/W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

- Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%
 2. Value at Tc = 25°C
 3. T_{ch} ≤ 150°C

Electrical Characteristics

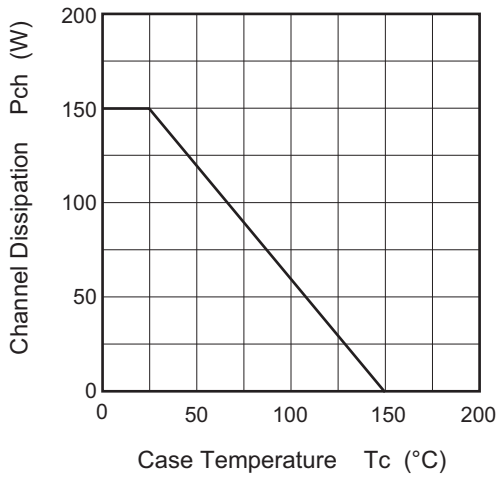
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	500	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to source leak current	I _{GSS}	—	—	±0.1	μA	V _{GS} = ±30 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	1	μA	V _{DS} = 500 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS (off)}	3.0	—	4.0	V	I _D = 1 mA, V _{DS} = 10 V
Static drain to source on state resistance	R _{DS (on)}	—	0.18	0.225	Ω	I _D = 12.5 A, V _{GS} = 10 V ^{Note 4}
Forward transfer admittance	y _{fs}	14	24	—	S	I _D = 12.5 A, V _{DS} = 10 V ^{Note 4}
Input capacitance	C _{iss}	—	3900	—	pF	V _{DS} = 25 V
Output capacitance	C _{oss}	—	375	—	pF	V _{GS} = 0
Reverse transfer capacitance	C _{rss}	—	78	—	pF	f = 1 MHz
Turn-on delay time	t _{d (on)}	—	55	—	ns	I _D = 12.5 A
Rise time	t _r	—	110	—	ns	V _{GS} = 10 V
Turn-off delay time	t _{d (off)}	—	225	—	ns	R _L = 20 Ω
Fall time	t _f	—	110	—	ns	R _g = 10 Ω
Total gate charge	Q _g	—	135	—	nC	V _{DD} = 400 V
Gate to source charge	Q _{gs}	—	22	—	nC	V _{GS} = 10 V
Gate to drain charge	Q _{gd}	—	74	—	nC	I _D = 25 A
Body-drain diode forward voltage	V _{DF}	—	0.9	1.4	V	I _F = 25 A, V _{GS} = 0
Body-drain diode reverse recovery time	t _{rr}	—	390	—	ns	I _F = 25 A, V _{GS} = 0
Body-drain diode reverse recovery charge	Q _{rr}	—	5	—	μC	di _F /dt = 100 A/μs

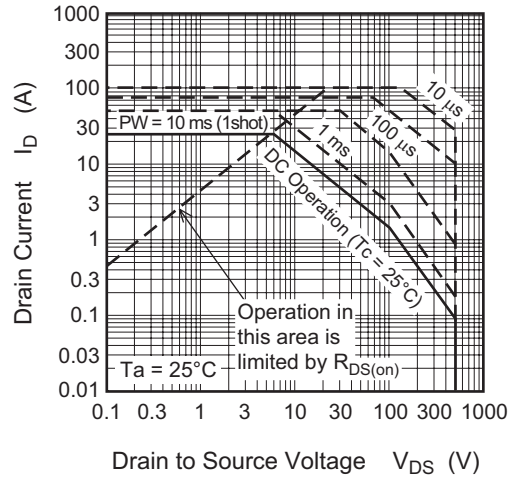
Note: 4. Pulse test

Main Characteristics

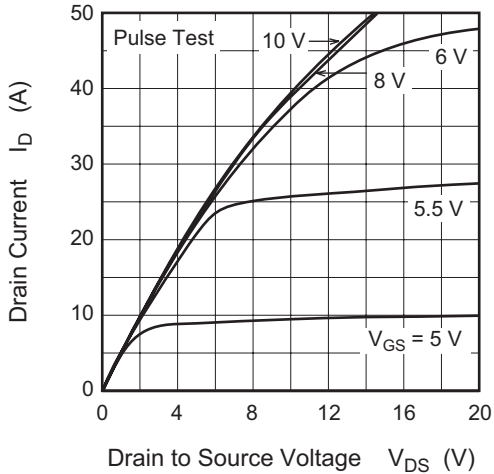
Power vs. Temperature Derating



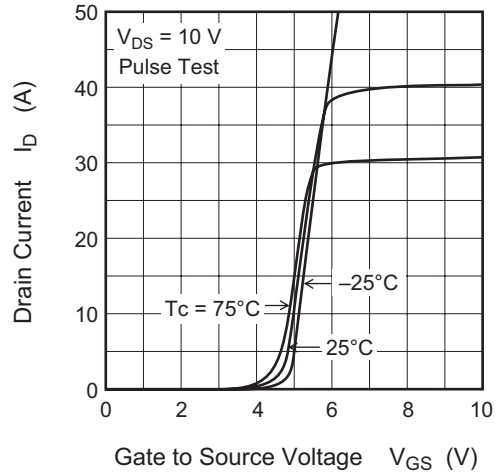
Maximum Safe Operation Area



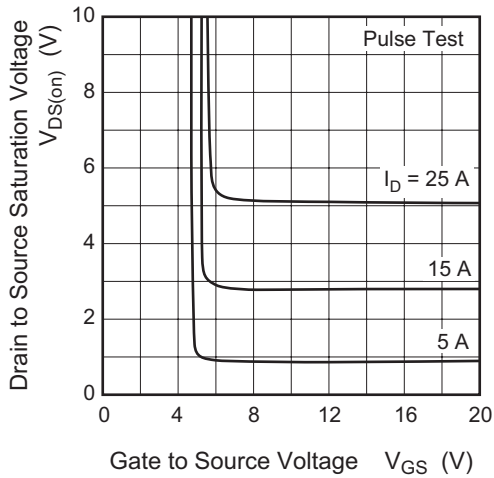
Typical Output Characteristics



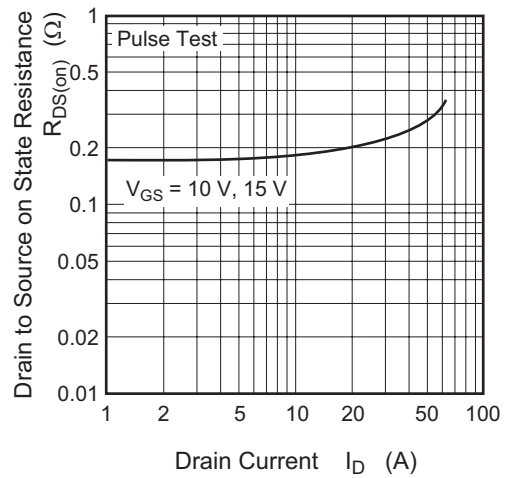
Typical Transfer Characteristics



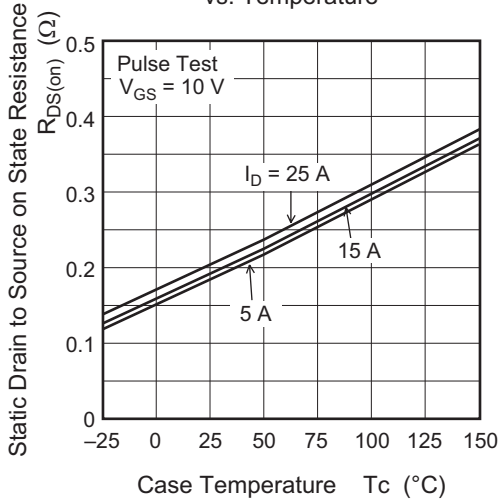
Drain to Source Saturation Voltage vs. Gate to Source Voltage



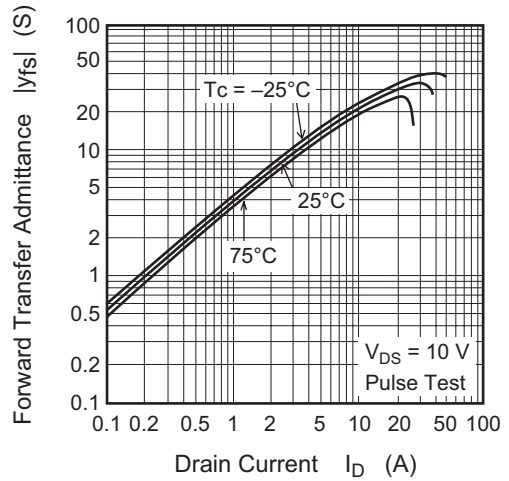
Static Drain to Source on State Resistance vs. Drain Current



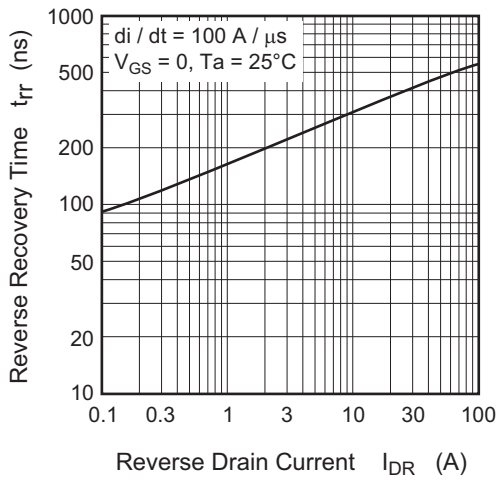
Static Drain to Source on State Resistance vs. Temperature



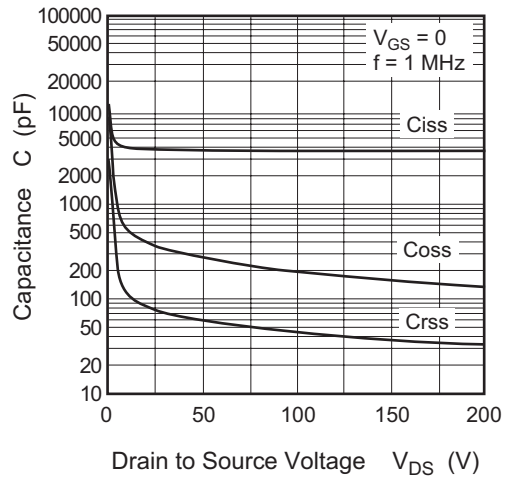
Forward Transfer Admittance vs. Drain Current



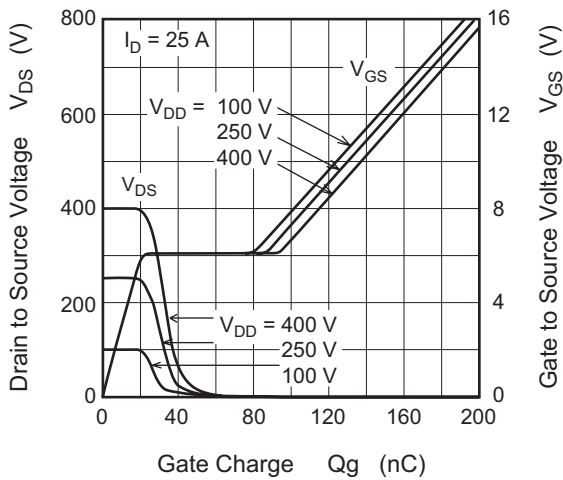
Body-Drain Diode Reverse Recovery Time



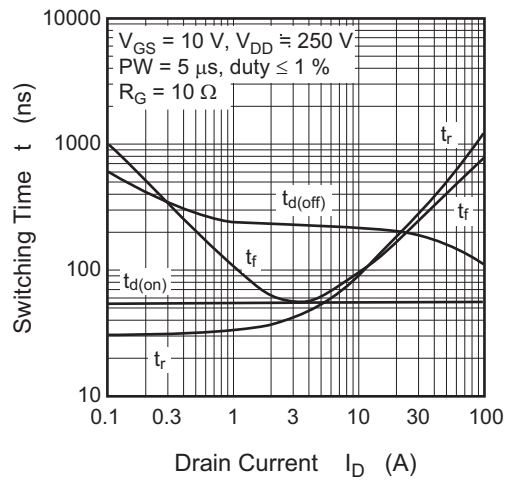
Typical Capacitance vs. Drain to Source Voltage



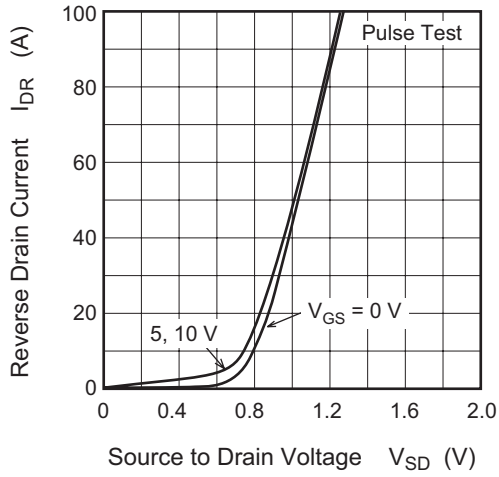
Dynamic Input Characteristics



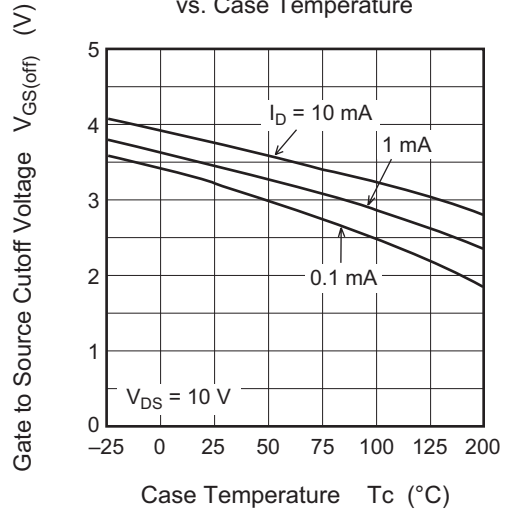
Switching Characteristics



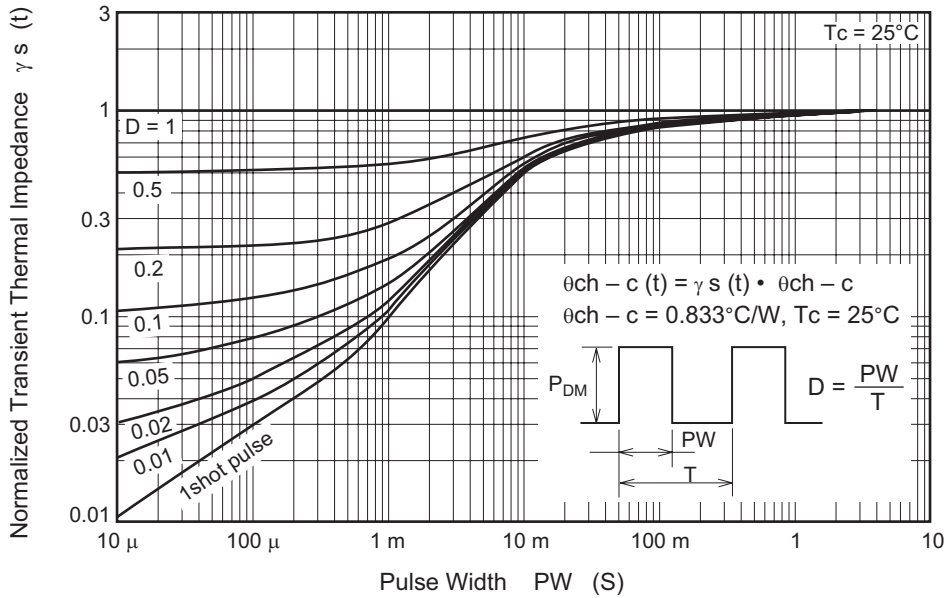
Reverse Drain Current vs. Source to Drain Voltage



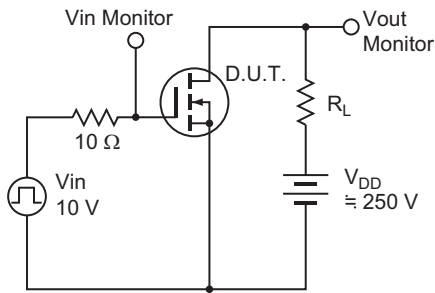
Gate to Source Cutoff Voltage vs. Case Temperature



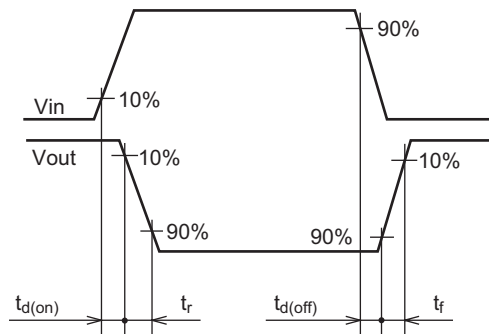
Normalized Transient Thermal Impedance vs. Pulse Width



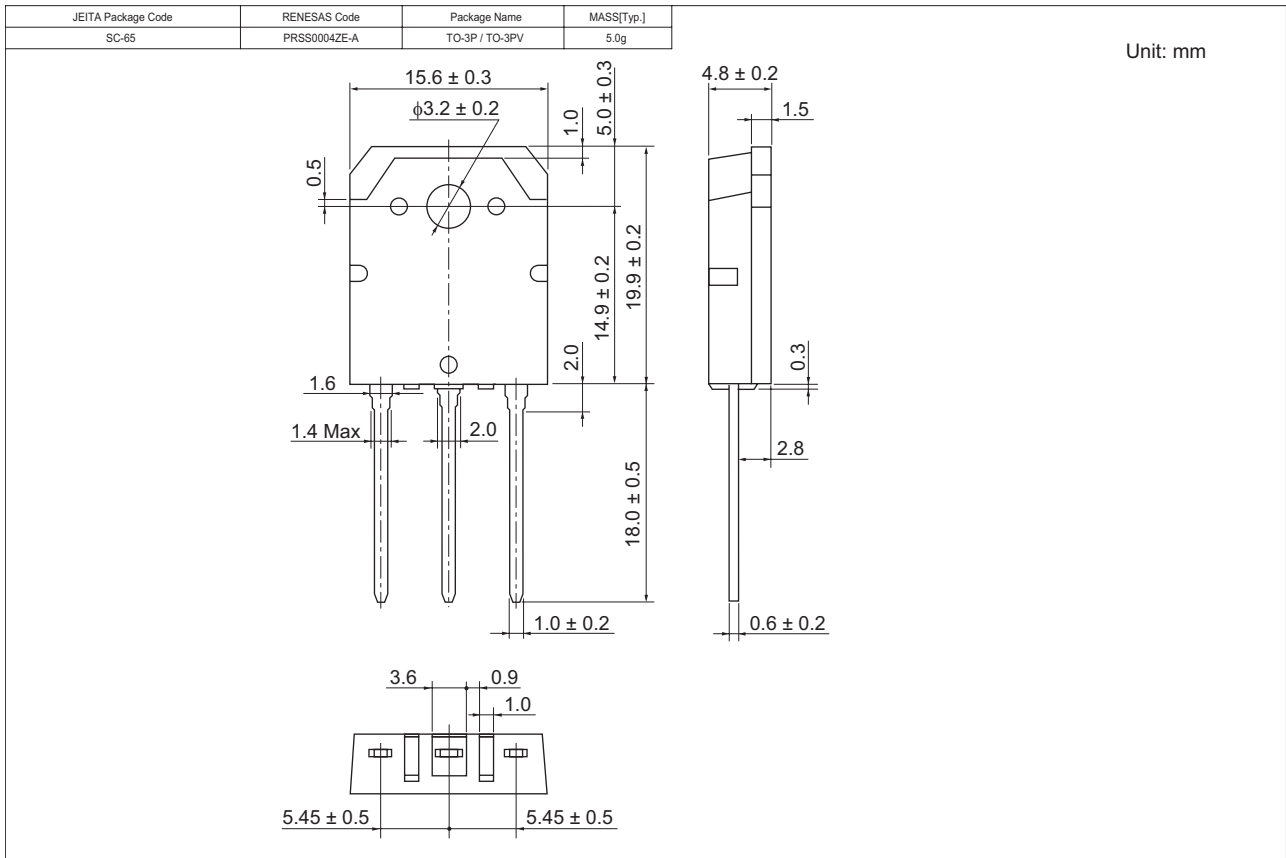
Switching Time Test Circuit



Waveform



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
H5N5007P-E	360 pcs	Box (Tube)

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