

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

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## H7N0312AB

Silicon N Channel MOS FET  
High Speed Power Switching

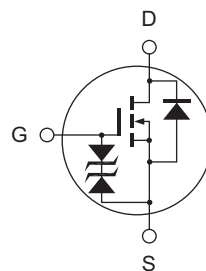
REJ03G1127-0400  
(Previous: ADE-208-1571B)  
Rev.4.00  
Sep 07, 2005

### Features

- Low on-resistance  
 $R_{DS(on)} = 2.6 \text{ m}\Omega$  typ.
- Low drive current
- 4.5 V gate drive device can be driven from 5 V source

### Outline

RENESAS Package code: PRSS0004AC-A  
(Package name: TO-220AB)



1. Gate
2. Drain (Flange)
3. Source

## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	85	A
Drain peak current	I <sub>D (pulse)</sub> <sup>Note 1</sup>	340	A
Body-drain diode reverse drain current	I <sub>DR</sub>	85	A
Channel dissipation	P <sub>ch</sub> <sup>Note 2</sup>	125	W
Channel to case thermal impedance	θ <sub>ch-c</sub>	1.0	°C/W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%

2. Value at Tc = 25°C

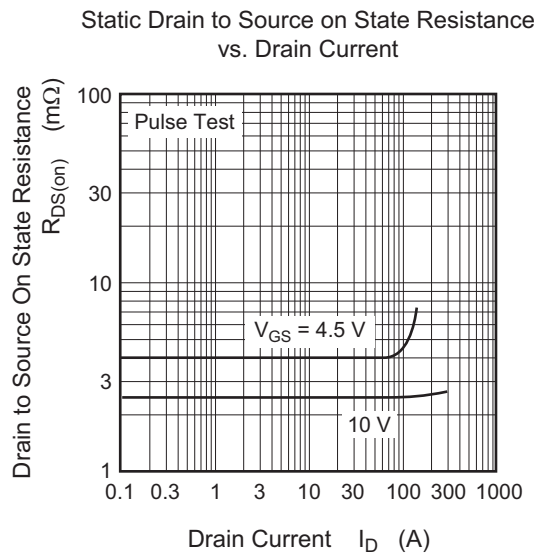
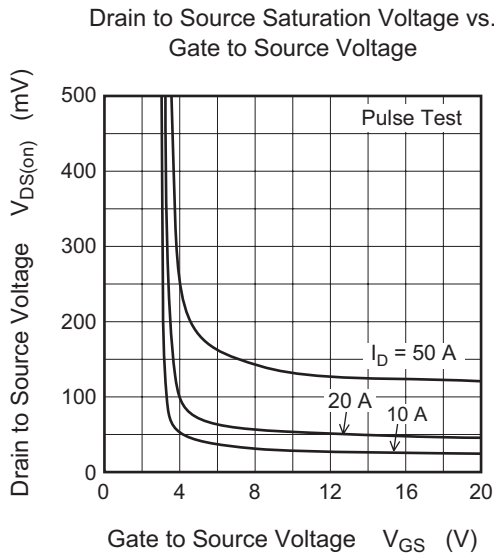
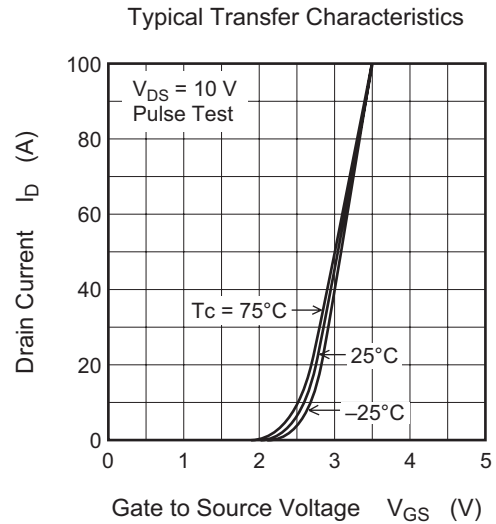
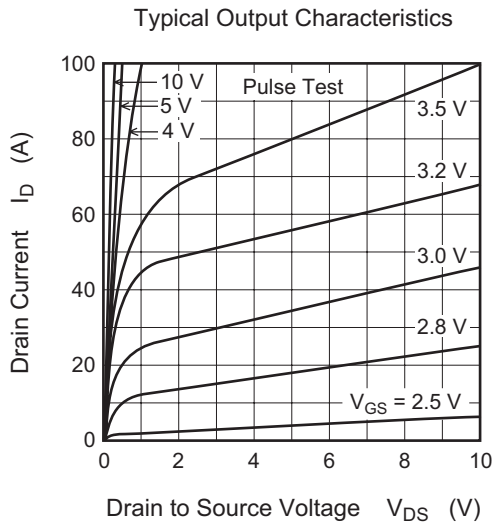
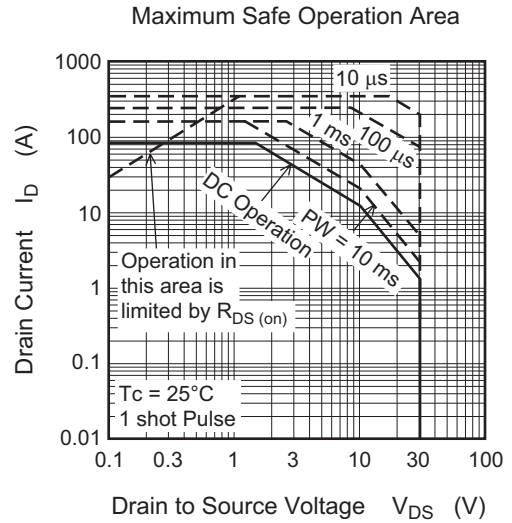
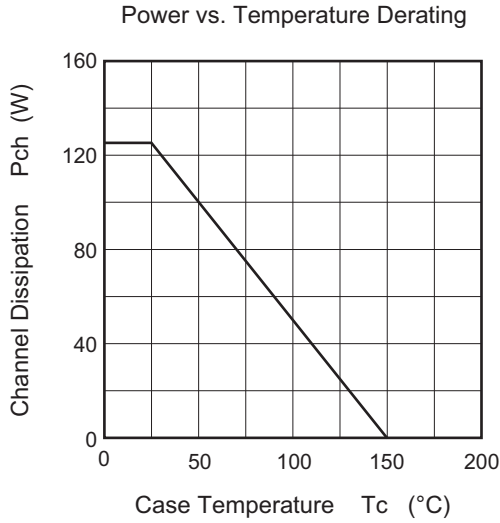
## Electrical Characteristics

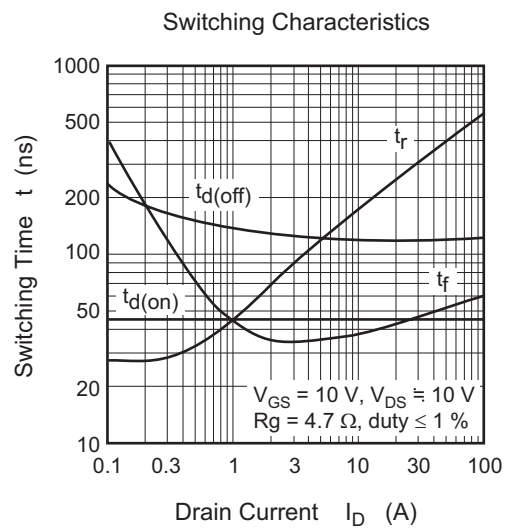
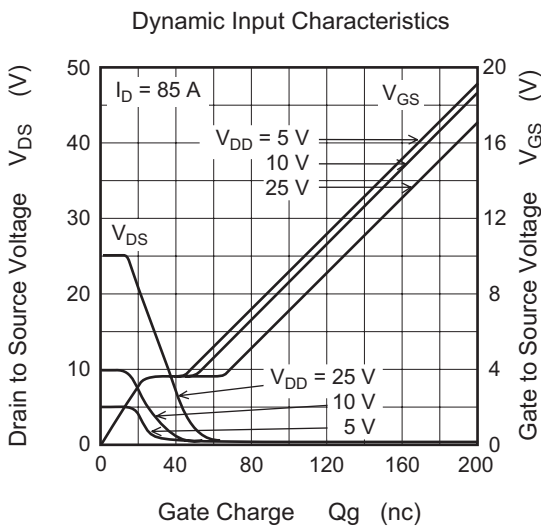
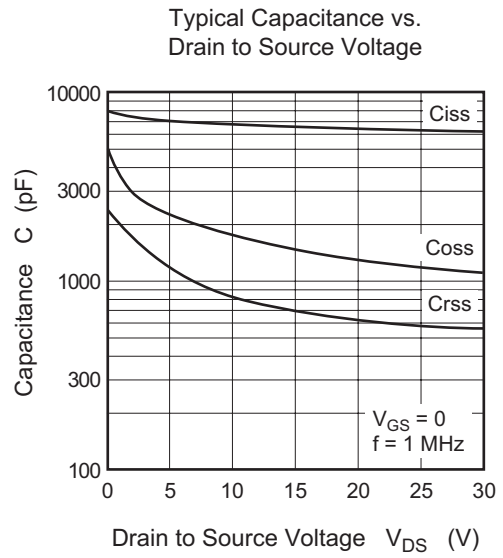
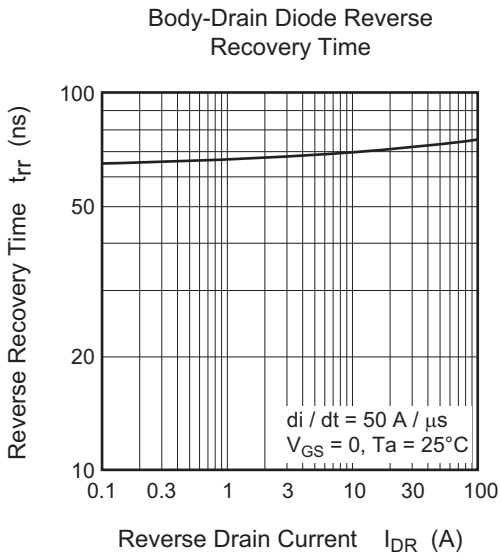
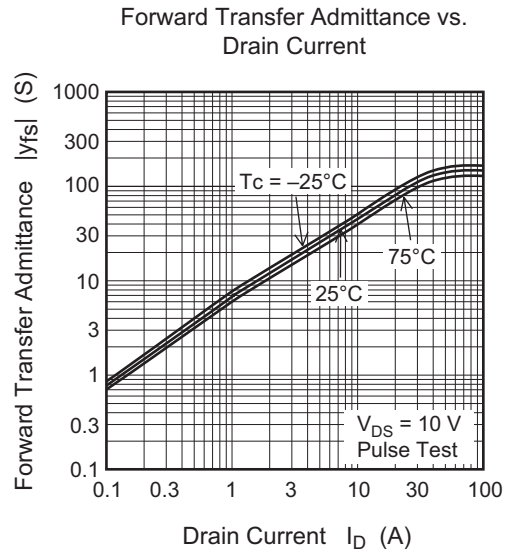
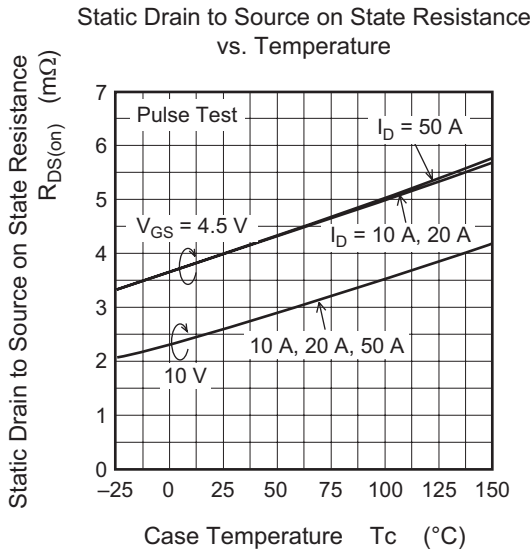
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR) DSS</sub>	30	—	—	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source breakdown voltage	V <sub>(BR) GSS</sub>	±20	—	—	V	I <sub>G</sub> = ±100 μA, V <sub>DS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μA	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	10	μA	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	V <sub>GS (off)</sub>	1.0	—	2.5	V	I <sub>D</sub> = 1 mA, V <sub>DS</sub> = 10 V <sup>Note 3</sup>
Static drain to source on state resistance	R <sub>DS (on)</sub>	—	2.6	3.3	mΩ	I <sub>D</sub> = 42.5 A, V <sub>GS</sub> = 10 V <sup>Note 3</sup>
		—	4.0	5.8	mΩ	I <sub>D</sub> = 42.5 A, V <sub>GS</sub> = 4.5 V <sup>Note 3</sup>
Forward transfer admittance	y <sub>fs</sub>	75	125	—	S	I <sub>D</sub> = 42.5 A, V <sub>DS</sub> = 10 V <sup>Note 3</sup>
Input capacitance	C <sub>iss</sub>	—	6900	—	pF	V <sub>DS</sub> = 10 V
Output capacitance	C <sub>oss</sub>	—	1750	—	pF	V <sub>GS</sub> = 0
Reverse transfer capacitance	C <sub>rss</sub>	—	820	—	pF	f = 1 MHz
Total gate charge	Q <sub>g</sub>	—	115	—	nC	V <sub>DD</sub> = 10 V
Gate to source charge	Q <sub>gs</sub>	—	24	—	nC	V <sub>GS</sub> = 10 V
Gate to drain charge	Q <sub>gd</sub>	—	24	—	nC	I <sub>D</sub> = 85 A
Turn-on delay time	t <sub>d (on)</sub>	—	45	—	ns	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 42.5 A
Rise time	t <sub>r</sub>	—	380	—	ns	R <sub>L</sub> = 0.24 Ω
Turn-off delay time	t <sub>d (off)</sub>	—	125	—	ns	R <sub>g</sub> = 4.7 Ω
Fall time	t <sub>f</sub>	—	50	—	ns	
Body-drain diode forward voltage	V <sub>DF</sub>	—	0.92	—	V	I <sub>F</sub> = 85 A, V <sub>GS</sub> = 0
Body-drain diode reverse recovery time	t <sub>rr</sub>	—	75	—	ns	I <sub>F</sub> = 85 A, V <sub>GS</sub> = 0 di <sub>F</sub> /dt = 50 A/μs

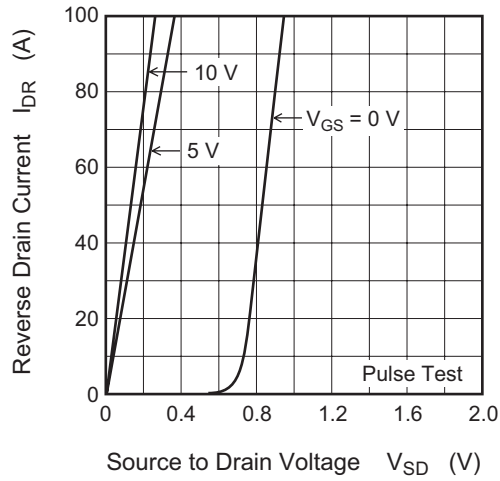
Note: 3. Pulse test

### Main Characteristics

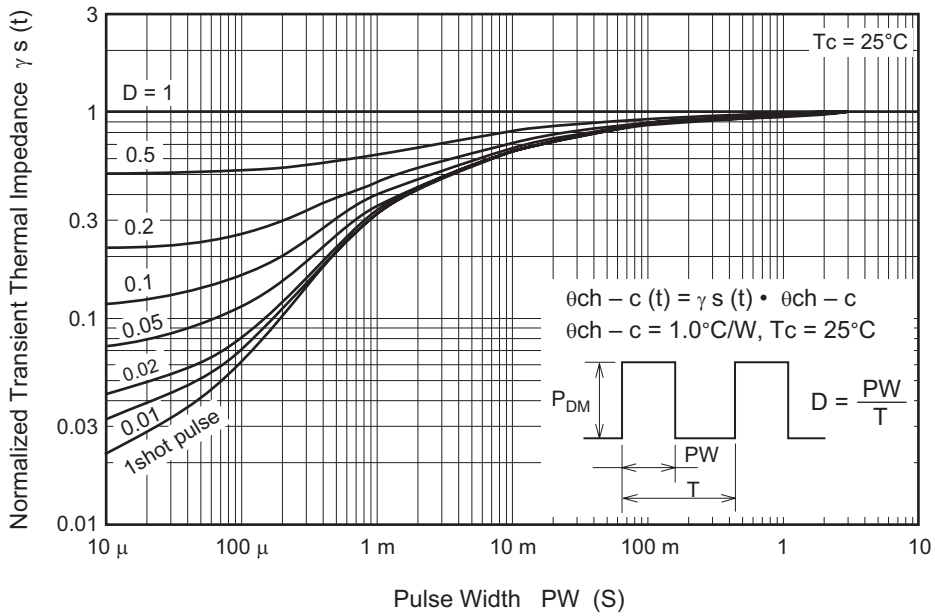




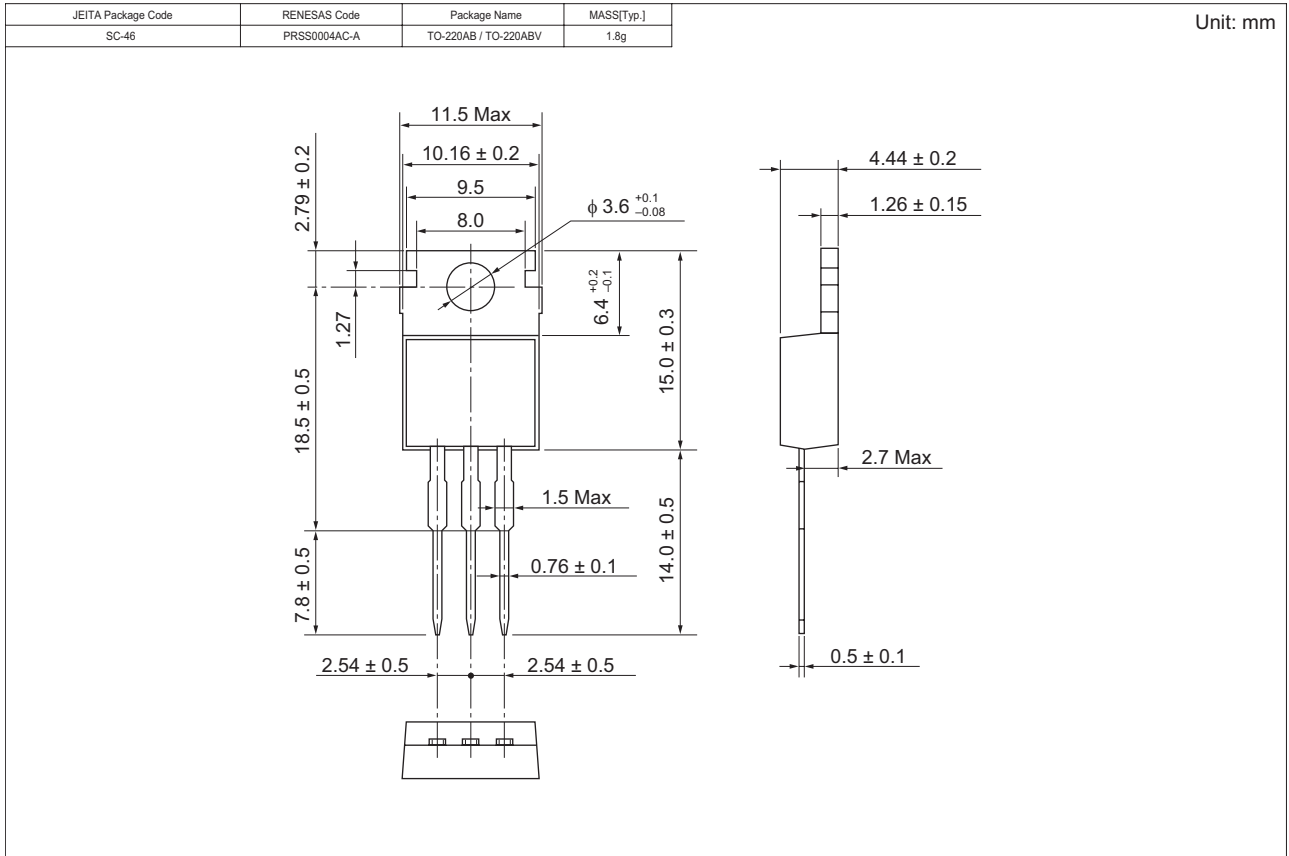
Reverse Drain Current vs. Source to Drain Voltage



Normalized Transient Thermal Impedance vs. Pulse Width



### Package Dimensions



### Ordering Information

Part Name	Quantity	Shipping Container
H7N0312AB-E	500 pcs	Box (Sack)

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