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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HAT2035R

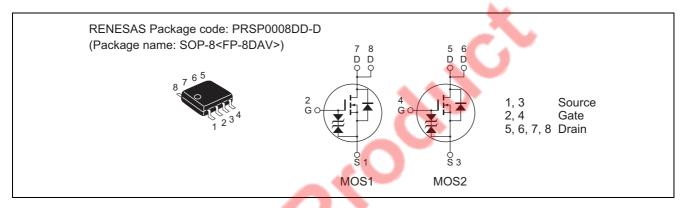
Silicon N Channel Power MOS FET High Speed Power Switching

REJ03G1242-0100 Rev.1.00 Jun. 09, 2005

Features

- Low on-resistance
- Capable of 4 V gate drive
- Low drive current
- High density mounting

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

| Item | Symbol | Ratings | Unit |
|--|-----------------------------|-------------|------|
| Drain to Source voltage | V _{DSS} | 150 | V |
| Gate to Source voltage | V_{GSS} | ±15 | V |
| Drain current | I _D | 0.5 | А |
| Drain peak current | I _{D(pulse)} Note1 | 2 | А |
| Body-Drain diode reverse Drain current | I _{DR} | 0.5 | А |
| Channel dissipation | P _{ch} Note2 | 1 | W |
| Channel dissipation | P _{ch} Note3 | 1.5 | W |
| Channel temperature | Tch | 150 | °C |
| Storage temperature | Tstg | −55 to +150 | °C |

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1 %

2. 1 Drive operation : When using the glass epoxy board (FR4 40 x 40 x 1.6 mm)

3. 2 Drive operation: When using the glass epoxy board (FR4 40 x 40 x 1.6 mm)

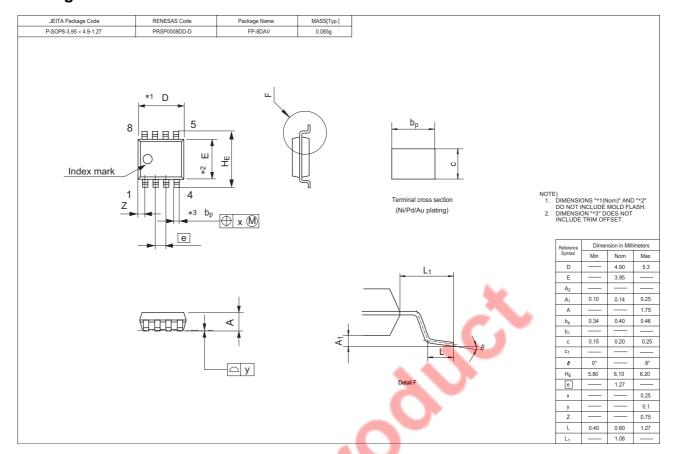
Electrical Characteristics

 $(Ta = 25^{\circ}C)$

| Item | Symbol | Min | Тур | Max | Unit | Test Conditions |
|----------------------------------|---------------------|------|------|-----|------|---|
| Drain to Source breakdown | $V_{(BR)DSS}$ | 150 | _ | _ | V | $I_D = 10 \text{ mA}, V_{GS} = 0$ |
| voltage | | | | | | |
| Gate to Source breakdown voltage | $V_{(BR)GSS}$ | ±15 | _ | _ | V | $I_G = \pm 100 \ \mu A, \ V_{DS} = 0$ |
| Gate to Source leak current | I_{GSS} | _ | _ | ±10 | μΑ | $V_{GS} = \pm 12 \text{ V}, V_{DS} = 0$ |
| Zero Gate voltage Drain current | I _{DSS} | _ | _ | 5 | μΑ | $V_{DS} = 150 \text{ V}, V_{GS} = 0$ |
| Gate to Source cutoff voltage | $V_{GS(off)}$ | 1.0 | _ | 2.1 | V | $V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$ |
| Static Drain to Source on state | R _{DS(on)} | _ | 1.6 | 2.2 | Ω | $I_D = 0.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$ |
| resistance | R _{DS(on)} | _ | 1.9 | 2.7 | Ω | $I_D = 0.5 \text{ A}, V_{GS} = 4 \text{ V}^{Note4}$ |
| | R _{DS(on)} | _ | 2.4 | 5.5 | Ω | $I_D = 2 A$, $V_{GS} = 5 V^{Note4}$ |
| Forward transfer admittance | y _{fs} | 0.56 | 0.86 | _ | S | $I_D = 0.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$ |
| Input capacitance | Ciss | _ | 95 | _ | pF | V _{DS} = 10 V |
| Output capacitance | Coss | _ | 42 | _ | pF | $V_{GS} = 0$ |
| Reverse transfer capacitance | Crss | _ | 11 | _ | pF | f = 1 MHz |
| Turn-on delay time | t _{d(on)} | _ | 9 | _ | ns | $V_{GS} = 5 \text{ V}, I_D = 0.5 \text{ A},$ |
| Rise time | t _r | _ | 16 | _ | ns | $V_{DD} \cong 30 \text{ V}$ |
| Turn-off delay time | $t_{d(off)}$ | _ | 18 | _ | ns | |
| Fall time | t _f | _ | 14 | _ | ns | 3 |
| Body-Drain diode forward voltage | V_{DF} | _ | 0.9 | 1.4 | V | $IF = 0.5 \text{ A}, V_{GS} = 0^{\text{Note4}}$ |
| Body-Drain diode reverse | t _{rr} | _ | 90 | - | ns | IF = 0.5 A, V _{GS} = 0 |
| recovery time | | | | | | $diF/dt = 50 A/\mu s$ |

Notes: 4. Pulse test

Package Dimensions



Ordering Information

| Part Name | Quantity | ~ | Shipping Container |
|---------------|-----------|---|--------------------|
| HAT2035R-EL-E | 2500 pcs. | | Taping |

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