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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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HAT2050T

Silicon N Channel Power MOS FET
High Speed Power Switching

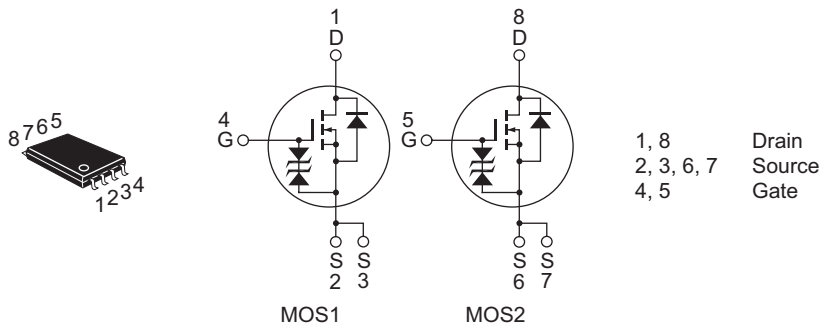
REJ03G1171-0300
(Previous: ADE-208-660A)
Rev.3.00
Sep 07, 2005

Features

- Low on-resistance
- Capable of 4 V gate drive
- Low drive current
- High density mounting

Outline

RENESAS Package code: PTSP0008JB-A
(Package name: TSSOP-8 <TTP-8D>)



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	100	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	1	A
Drain peak current	I _{D (pulse)} ^{Note 1}	4	A
Body-drain diode reverse drain current	I _{DR}	1	A
Channel dissipation	P _{ch} ^{Note 2}	1.0	W
Channel dissipation	P _{ch} ^{Note 3}	1.5	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%

2. 1 Drive operation: When using the glass epoxy board (FR4 40 × 40 × 1.6 mm), PW ≤ 10 s

3. 2 Drive operation: When using the glass epoxy board (FR4 40 × 40 × 1.6 mm), PW ≤ 10 s

Electrical Characteristics

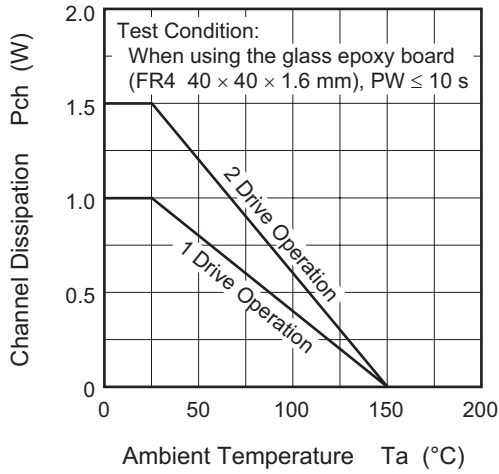
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR) DSS}	100	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR) GSS}	±20	—	—	V	I _G = ±100 μA, V _{DS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	μA	V _{GS} = ±16 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	1	μA	V _{DS} = 100 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS (off)}	1.3	—	2.3	V	V _{DS} = 10 V, I _D = 1 mA
Static drain to source on state resistance	R _{DS (on)}	—	0.56	0.75	Ω	I _D = 0.5 A, V _{GS} = 10 V ^{Note 4}
	R _{DS (on)}	—	0.72	1.0	Ω	I _D = 0.5 A, V _{GS} = 4 V ^{Note 4}
Forward transfer admittance	y _{fs}	0.7	1.1	—	S	I _D = 0.5 A, V _{DS} = 10 V ^{Note 4}
Input capacitance	C _{iss}	—	90	—	pF	V _{DS} = 10 V V _{GS} = 0 f = 1 MHz
Output capacitance	C _{oss}	—	42	—	pF	
Reverse transfer capacitance	C _{rss}	—	20	—	pF	
Turn-on delay time	t _{d (on)}	—	11	—	ns	
Rise time	t _r	—	24	—	ns	V _{DD} ≅ 10 V
Turn-off delay time	t _{d (off)}	—	14	—	ns	
Fall time	t _f	—	11	—	ns	
Body-drain diode forward voltage	V _{DF}	—	0.84	1.1	V	I _F = 1 A, V _{GS} = 0 ^{Note 4}
Body-drain diode reverse recovery time	t _{rr}	—	85	—	ns	I _F = 1 A, V _{GS} = 0 di _F /dt = 20 A/μs

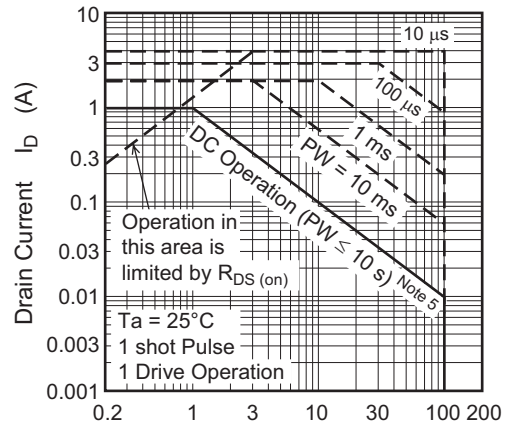
Note: 4. Pulse test

Main Characteristics

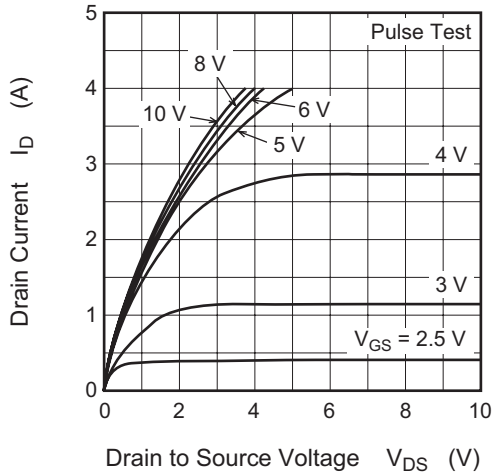
Power vs. Temperature Derating



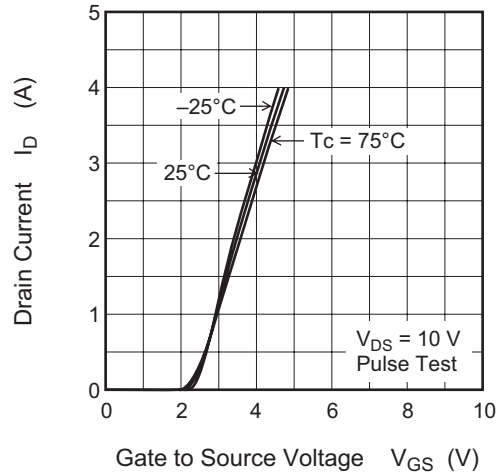
Maximum Safe Operation Area



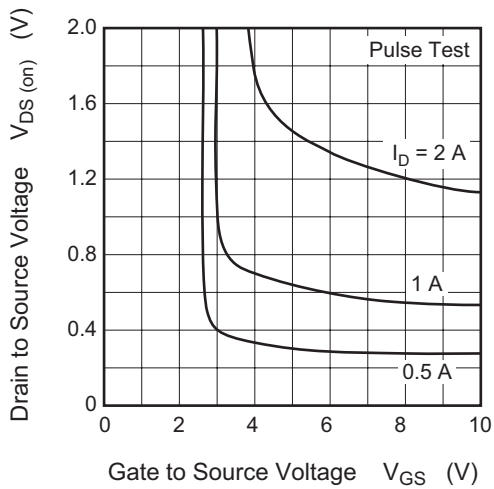
Typical Output Characteristics



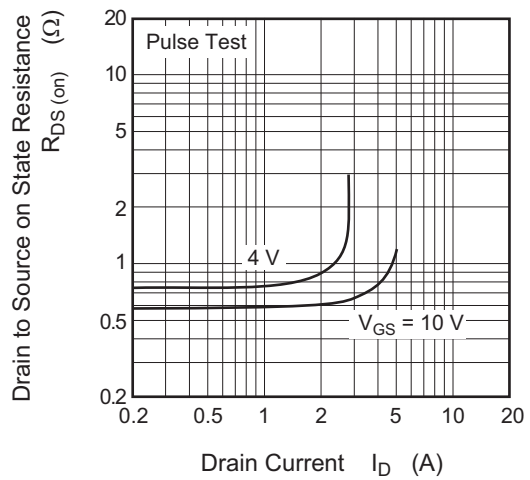
Typical Transfer Characteristics

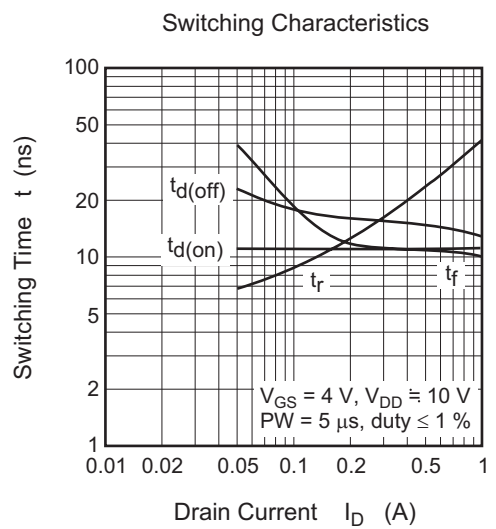
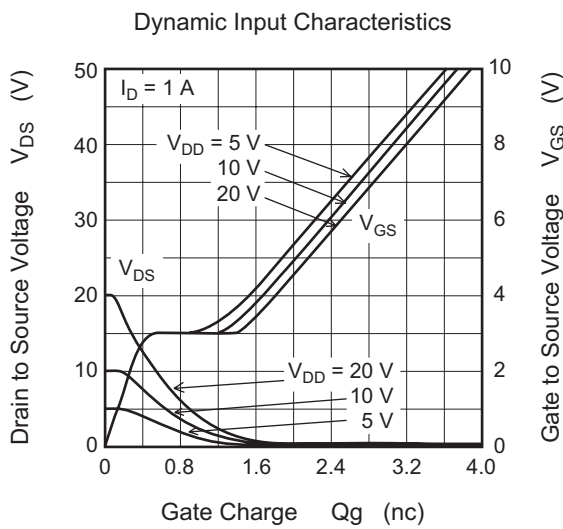
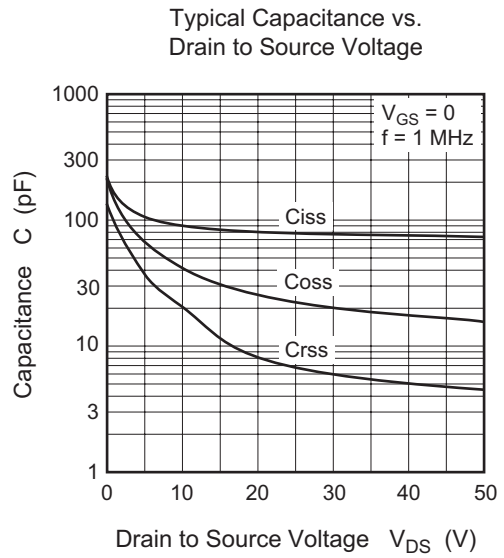
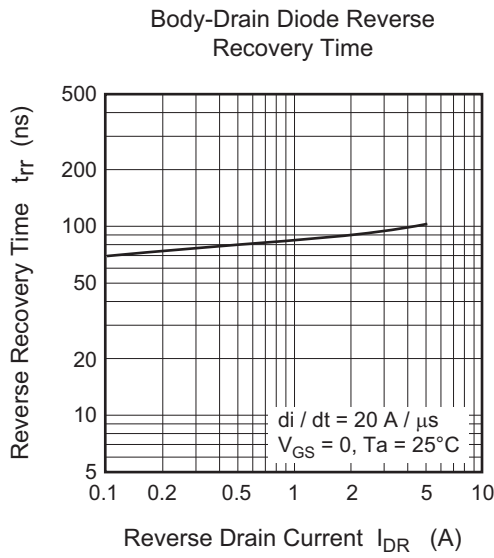
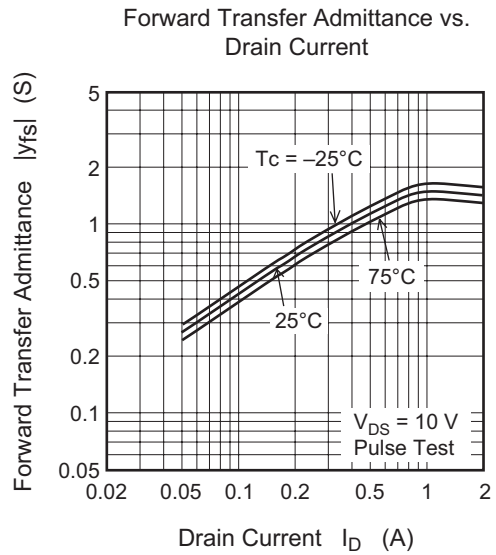
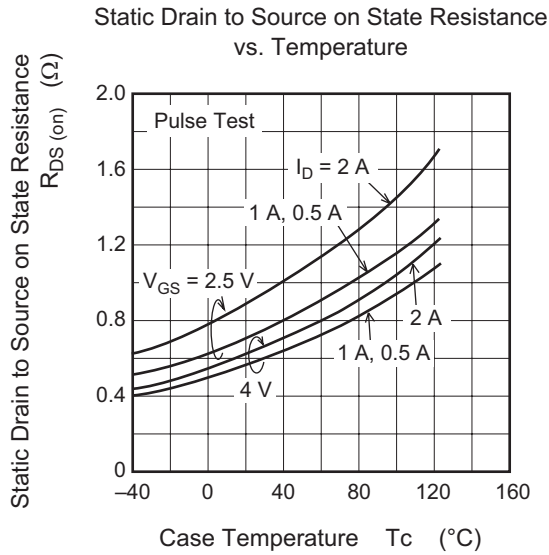


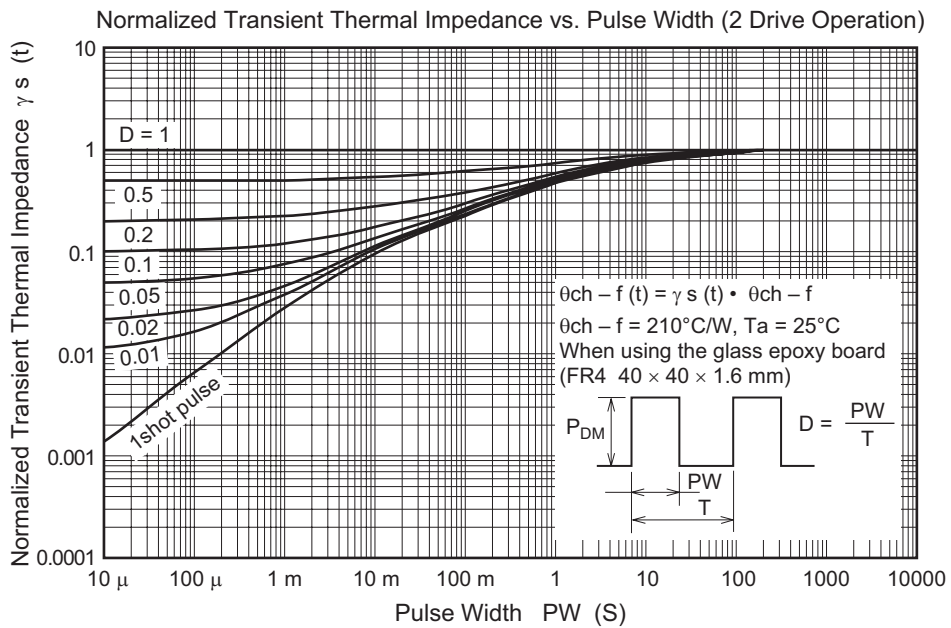
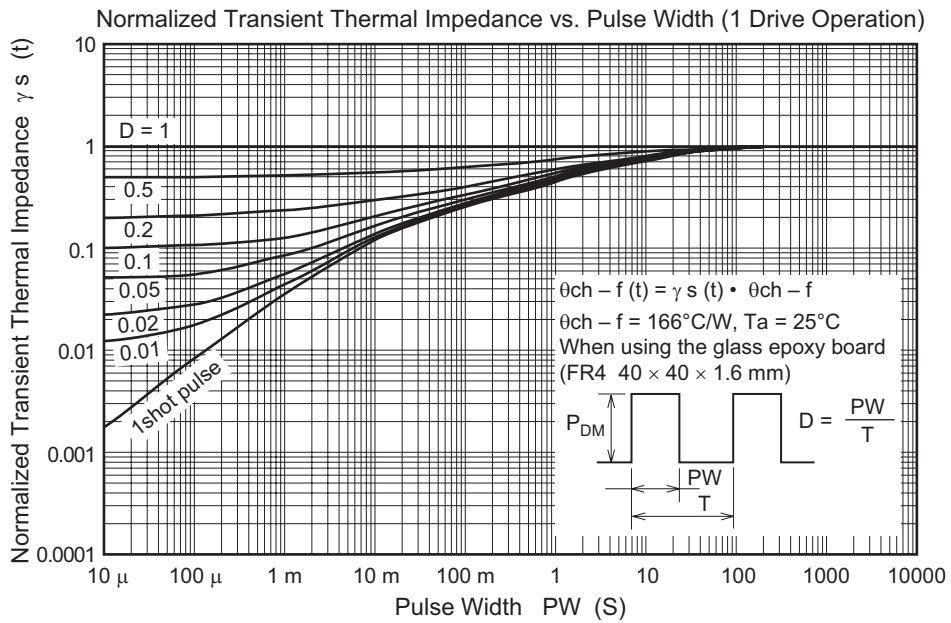
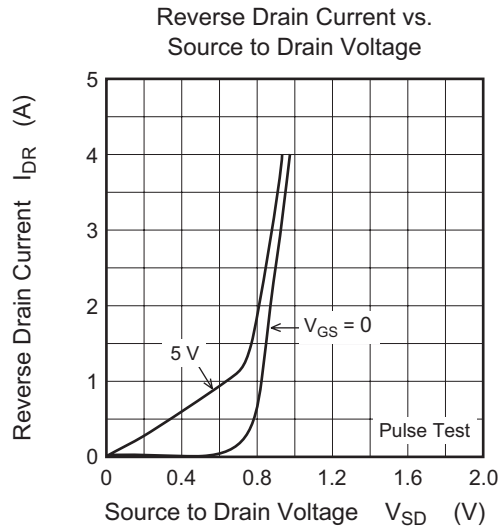
Drain to Source Saturation Voltage vs. Gate to Source Voltage

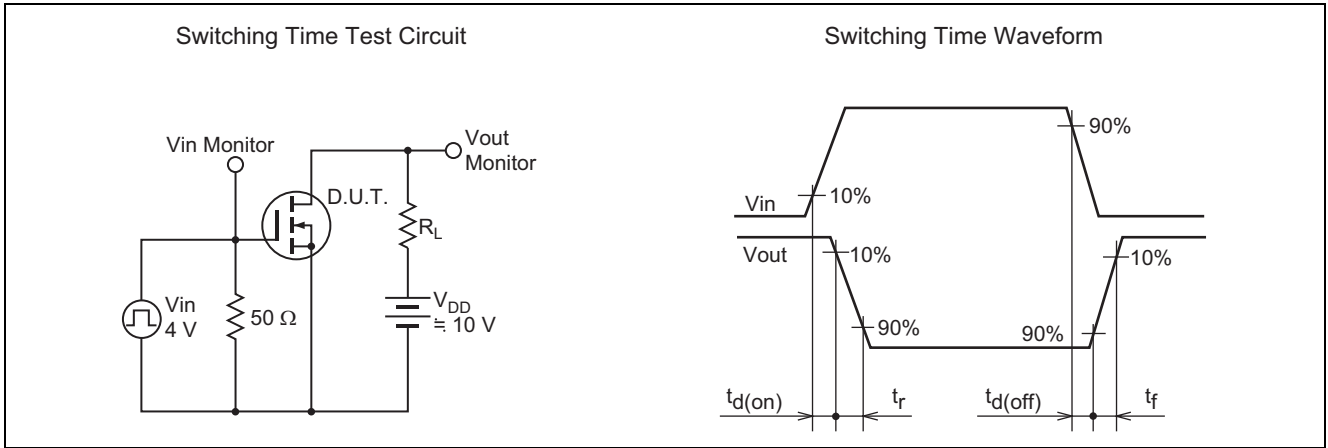


Static Drain to Source on State Resistance vs. Drain Current

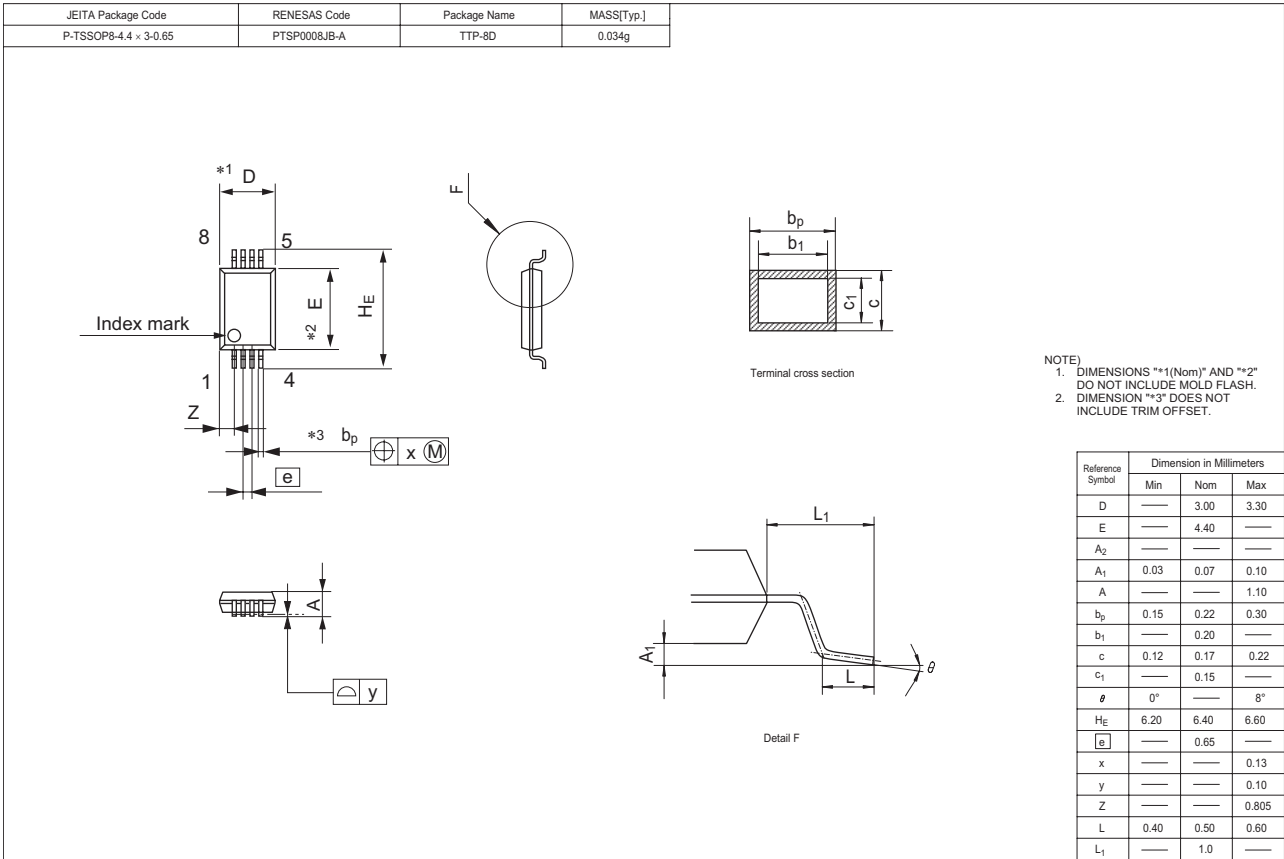








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HAT2050T-EL-E	3000 pcs	Taping

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Renesas Technology America, Inc.

450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.

7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.

10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.

Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.

Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> 2-796-3115, Fax: <82> 2-796-2145

Renesas Technology Malaysia Sdn. Bhd.

Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510