

HZM6.2ZMWA

Silicon Planar Zener Diode for Surge Absorb

REJ03G1208-0100
 (Previous: ADE-208-1514)
 Rev.1.00
 Jun 08, 2005

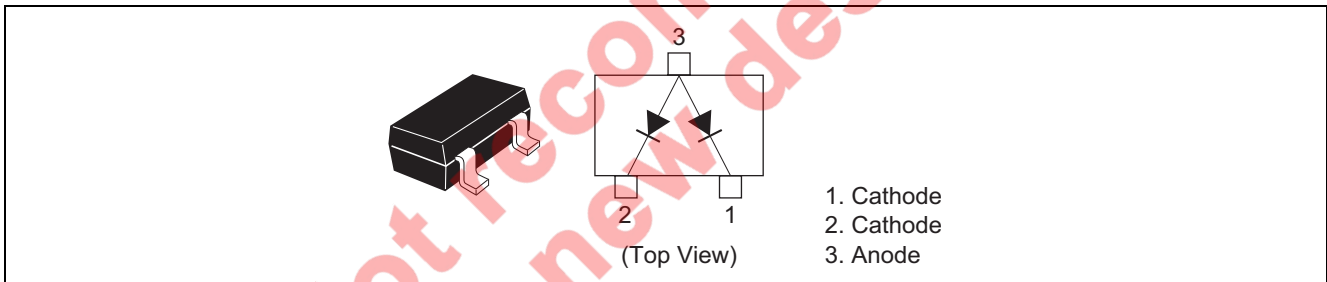
Features

- HZM6.2ZMWA has two devices in a monolithic, and can absorb surge.
- Low capacitance ($C = 8.5 \text{ pF max}$) and can protect ESD of signal line.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
HZM6.2ZMWA	62N	MPAK	PLSP0003ZC-A (MPAK)

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd * ¹	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: 1. Two device total, See Fig.2.

Electrical Characteristics *¹

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	V _Z	5.90	—	6.50	V	I _Z = 5 mA, 40 ms pulse
Reverse current	I _R	—	—	3	μA	V _R = 5.5 V
Capacitance	C	—	—	8.5	pF	V _R = 0 V, f = 1 MHz
Dynamic resistance	r _d	—	—	60	Ω	I _Z = 5 mA
ESD-Capability * ²	—	13	—	—	kV	C = 150 pF, R = 330 Ω, Both forward and reverse direction 10 pulse

Notes: 1. Per one device

2. Failure criterion ; I_R > 3 μA at V_R = 5.5 V.

Not recommended for new design

Main Characteristic

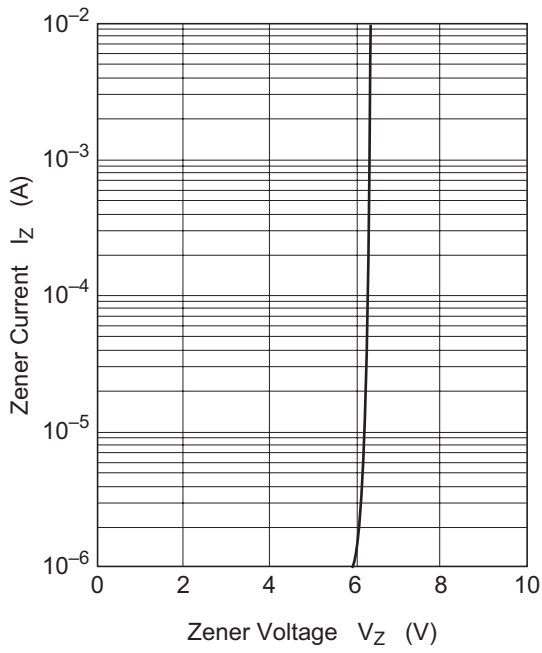


Fig.1 Zener current vs. Zener voltage

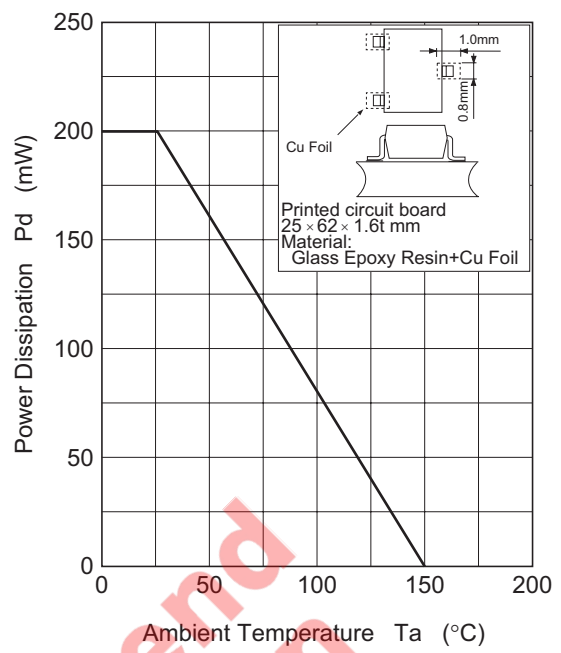


Fig.2 Power Dissipation vs. Ambient Temperature

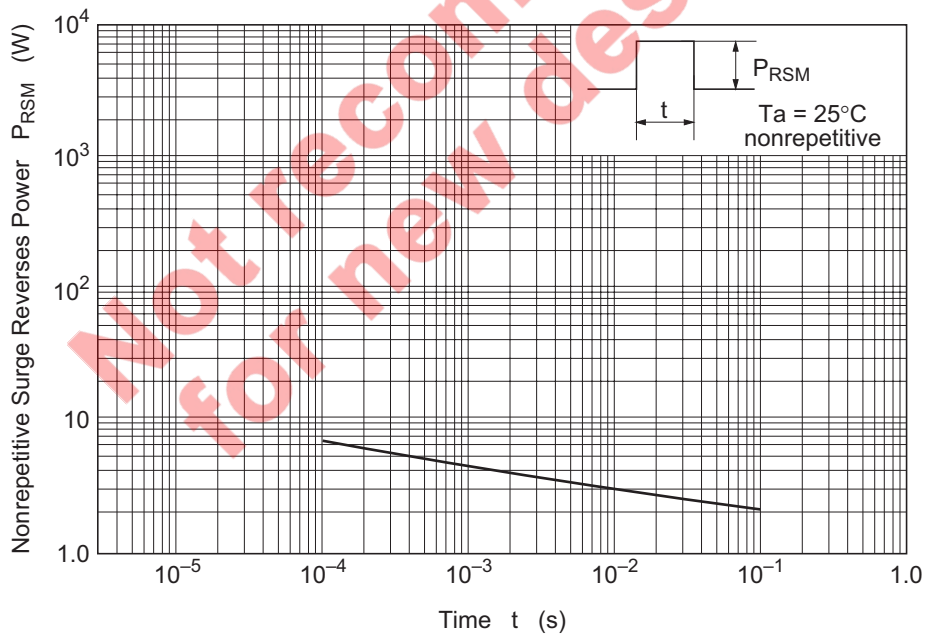


Fig.3 Surge Reverse Power Ratings

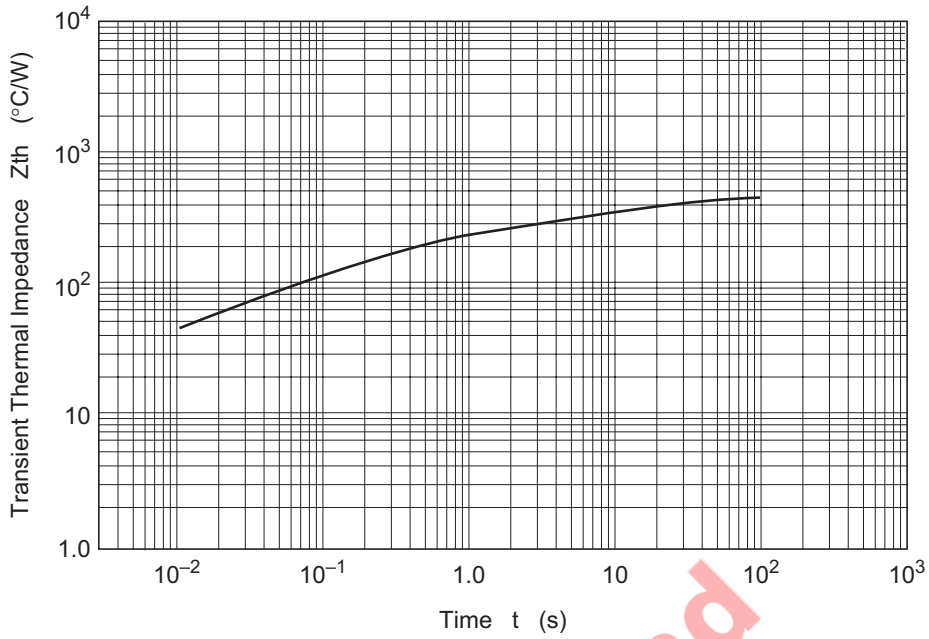
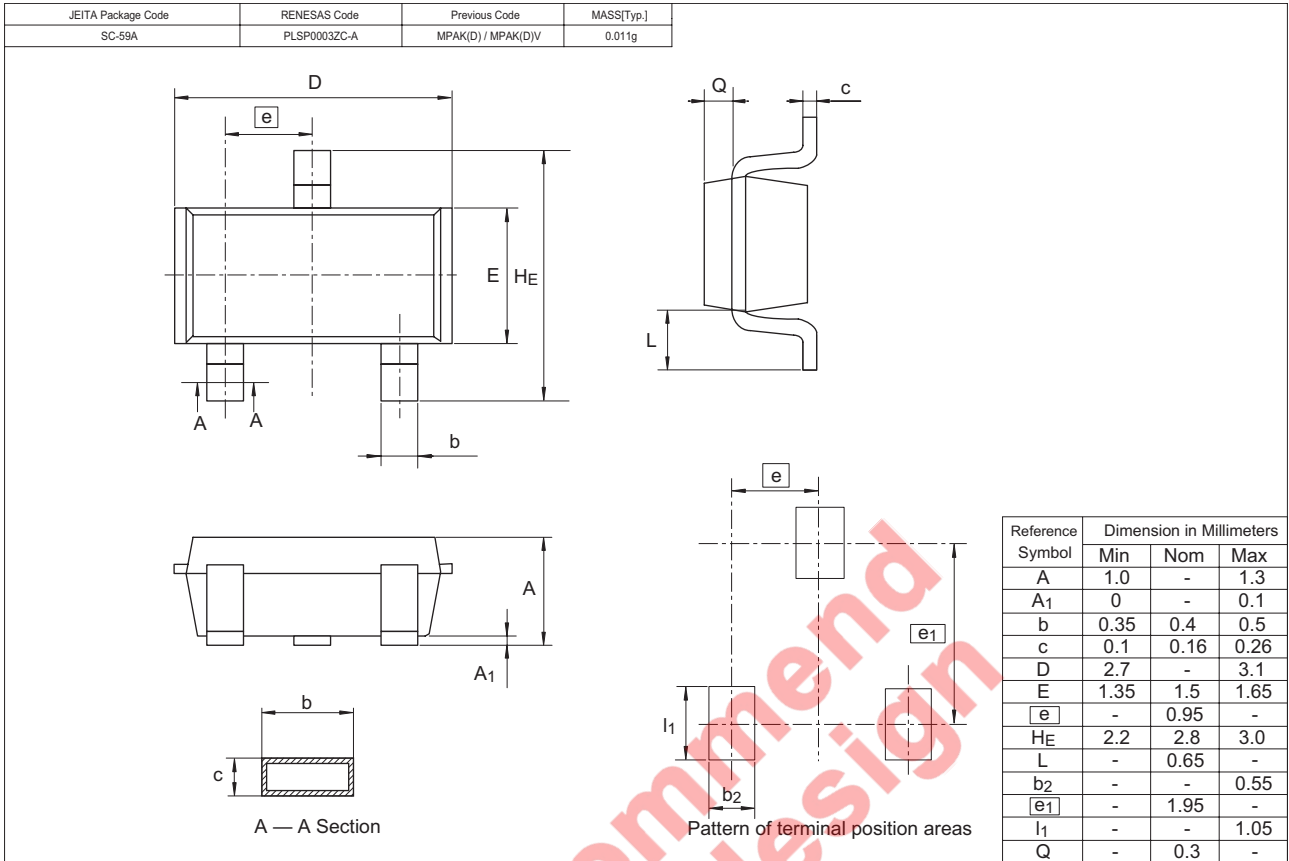


Fig.4 Transient Thermal Impedance *

Note: Measurement value by forward bias.

Not recommend
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Package Dimensions



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April 1st, 2010
Renesas Electronics Corporation

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