

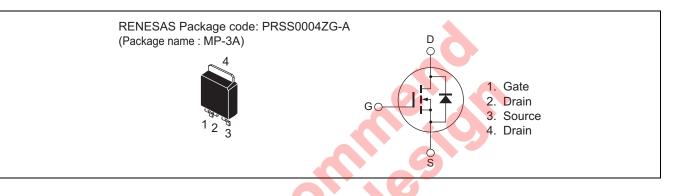
RJK60S1DPD

600V - 8A - SJ MOS FET High Speed Power Switching R07DS0853EJ0004 Rev.0.04 Nov 30, 2012

Features

- Superjunction MOSFET
- Low on-resistance
- $R_{DS(on)} = 0.84 \ \Omega$ typ. (at $I_D = 2.2$ A, $V_{GS} = 10$ V, $Ta = 25^{\circ}$ C)
- High speed switching $t_f = 61$ ns typ. (at $I_D = 2.2$ A, $V_{GS} = 10$ V, $R_L = 136 \Omega$, $Rg = 10 \Omega$, $Ta = 25^{\circ}C$)

Outline



Absolute Maximum Ratings

	.0			$(Ta = 25^{\circ}C)$
Item		Symbol	Ratings	Unit
Drain to source voltage		V _{DSS}	600	V
Gate to source voltage		V _{GSS}	+30, -20	V
Drain current	Tc = 25°C	ID Note1,2	8	А
	Tc = 100°C	ID Note1,2	5	А
Drain peak current		Note1 D (pulse)	16	А
Body-drain diode rever	se drain cu <mark>rren</mark> t	I _{DR} ^{Note1}	8	А
Body-drain diode rever	se drain peak current	I _{DR (pulse)} Note1	16	А
Avalanche current		I _{AP} ^{Note3} 1.1		A
Avalanche energy		E _{AR} ^{Note3}		
Channel dissipation		Pch Note4	31.2	W
Channel to cse thermal impedance		θch-c	4.0	°C/W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

Notes: 1. Limited by Tch max.

- 2. Maximum duty cycle D = 0.5
- 3. STch = 25° C, Tch $\leq 150^{\circ}$ C
- 4. Value at $Tc = 25^{\circ}C$

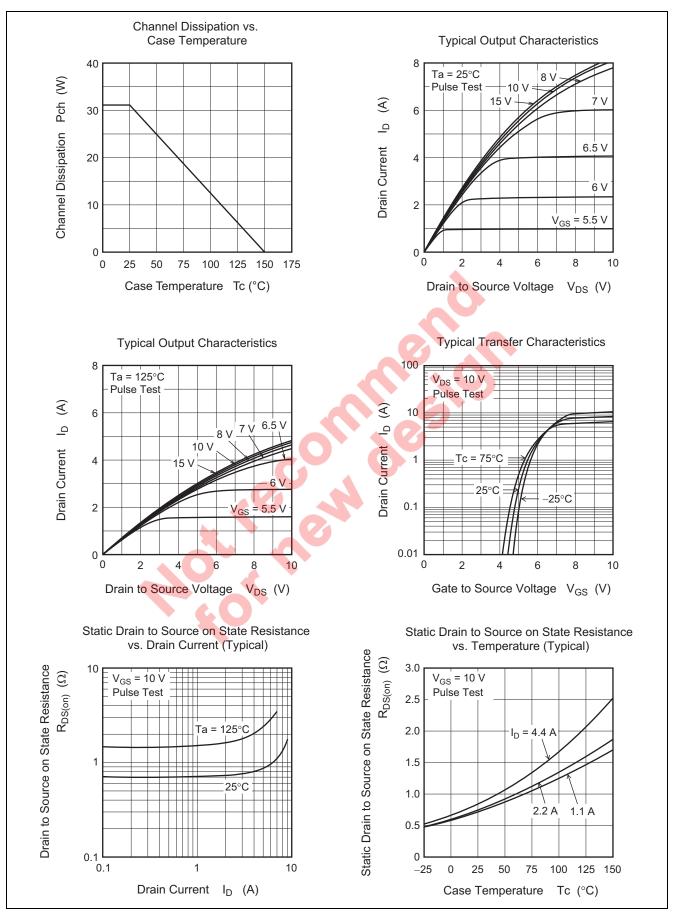


Electrical Characteristics

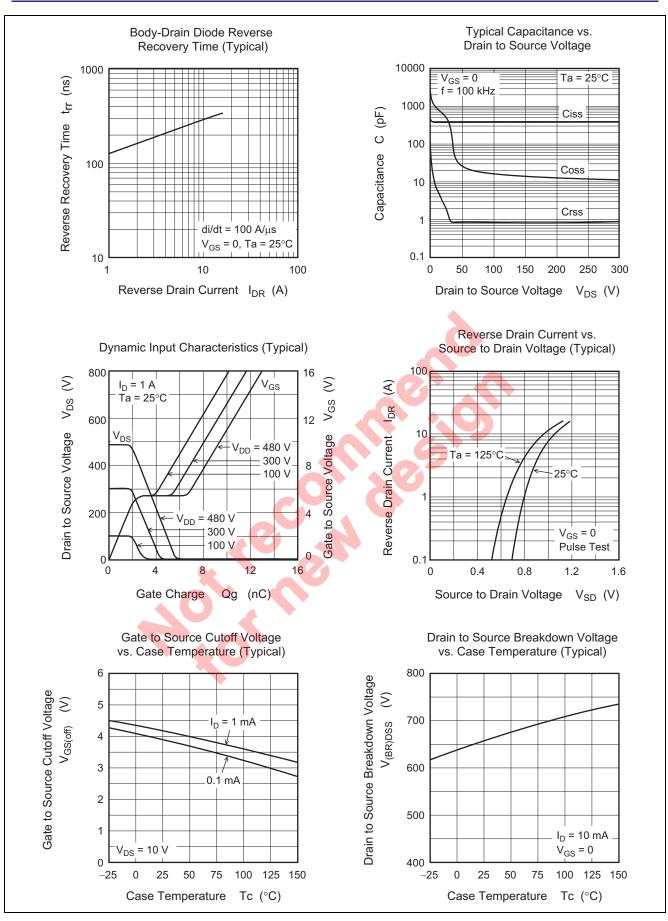
Drain to source breakdown voltage Zero gate voltage drain current Gate to source leak current	Symbol V _{(BR)DSS}	Min	Тур	Max	Unit	Test conditions
Zero gate voltage drain current Gate to source leak current	• (BR)D35	600			V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I _{DSS}		_	1	mA	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0$
	I _{GSS}	_		±0.1	μA	$V_{GS} = +30V, -20V, V_{DS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	3		5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}		0.84	1.05	Ω	$I_D = 2.2 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note5}}$
resistance	R _{DS(on)}	_	2.10		Ω	$Ta = 150^{\circ}C$
	11 05(00)		2.10		22	$I_D = 2.2 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note5}}$
Gate resistance	Rg	_	3.4	_	Ω	f = 1 MHz
	- 3					$V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0$
Input capacitance	Ciss	_	380	—	pF	V _{DS} = 25 V
Output capacitance	Coss		500		pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		2.1		pF	f = 100 kHz
Turn-on delay time	t _{d(on)}	_	9		ns	I _D = 2.2 A
Rise time	tr	_	15	_	ns	V _{GS} = 10 V
Turn-off delay time	t _{d(off)}	_	22	—	ns	$R_L = 136 \Omega$
Fall time	t _f	_	61		ns	$Rg = 10 \Omega^{Note5}$
Total gate charge	Qg	_	9.3		nC	V _{DD} = 480 V
Gate to source charge	Qgs	_	2.1		nC	V _{GS} = 10 V
Gate to drain charge	Qgd	_	4.5	_	nC	$I_D = 1 A^{Note5}$
Body-drain diode forward voltage	V _{DF}	_	1.0	1.6	V	$I_F = 4.4 \text{ A}, V_{GS} = 0^{Note5}$
Body-drain diode reverse recovery time	t _{rr}	-	210	6	ns	I _F = 4.4 A
Body-drain diode reverse recovery	I _{rr}	_	14.5		Α	$V_{GS} = 0$
current						$di_F/dt = 100 \text{ A}/\mu \text{s}^{\text{Note5}}$
Body-drain diode reverse recovery	Qrr		1.7	—	μC	
Notes: 5. Pulse test						



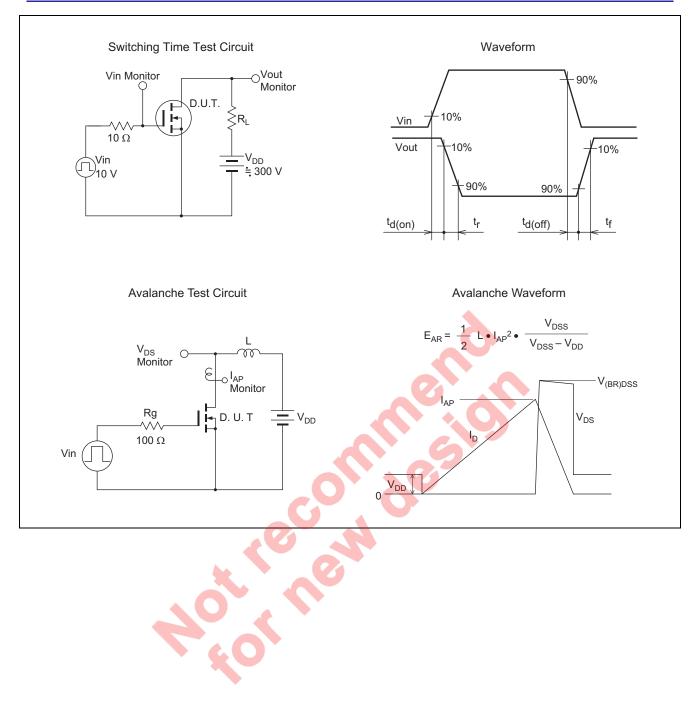
Main Characteristics





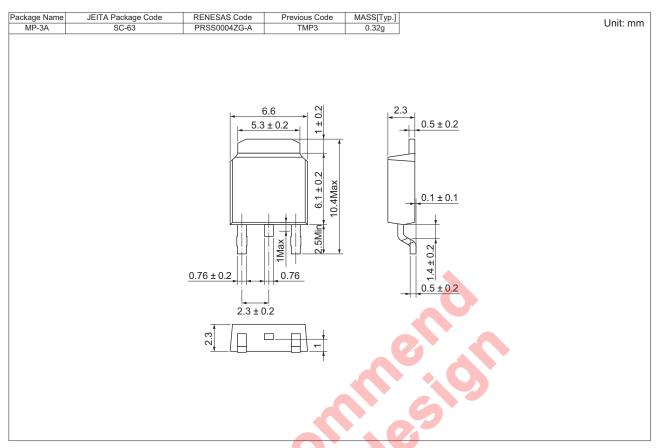








Package Dimension



Ordering Information

4°51

Orderable Part No.	Quantity	Shipping Container
RJK60S1DPD-00#J2	3000 pcs	Taping



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