Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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MOS Field Effect Power Transistors



 μ PA1710

SWITCHING P-CHANNEL POWER MOS FFT INDUSTRIAL USE

DESCRIPTION

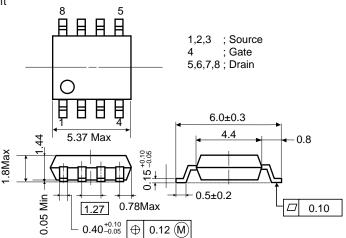
This product is P-Channel MOS Field Effect Transistor designed for DC/DC converter and power management applications of notebook computers.

FEATURES

- Low On-Resistance
 - R_{DS(on)1} = 70 mΩ Max.(V_{GS} = -10V, I_D = -2.5A) R_{DS(on)2} = 0.16 Ω Max.(V_{GS} = -4V, I_D = -2.0A)
- Low Ciss Ciss = 980pF Typ.
- · Built-in G-S Protection Diode
- Small and Surface Mount Package (Power SOP8)

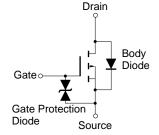
PACKAGE DIMENSIONS

(in millimeter)



ABSOLUTE MAXIMUM RATINGS (TA = 25 °C, All terminals are connected)

	-		
Drain to Source Voltage	VDSS	-30	V
Gate to Source Voltage	Vgss	- 20	V
Drain Current (DC)	I _{D(DC)}	- 5.0	Α
Drain Current (pulse)*	$I_{D(pulse)}$	- 20	Α
Total Power Dissipation (T _A = 25°C)**	Рт	2.0	W
Channel Temperature	Tch	150	С
Storage Temperature	T _{stg}	-50 to +150	С



The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if voltage exceeding the rated voltage may be applied to this device.

The information in this document is subject to change without notice.

^{*} PW \leq 10 μ s, Duty Cycle \leq 1%

^{**} Mounted on ceramic substrate of 1200 $\text{mm}^2 \times 0.7 \text{ mm}$



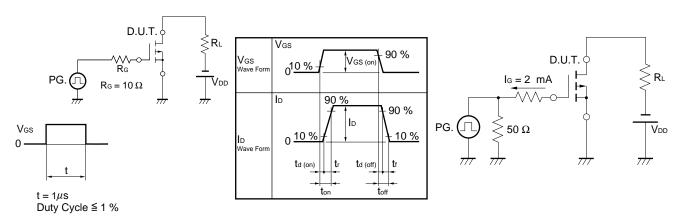


ELECTRICAL CHARACTERISTICS (TA = 25 °C, All terminals are connected)

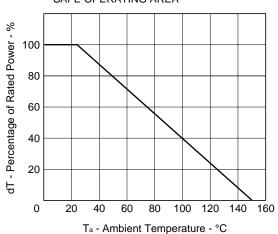
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain to Source On-state Resistance	RDS(on)1	Vgs = −10V, ID = −2.5A		50	70	mΩ
	R _{DS(on)2}	$V_{GS} = -4V, I_{D} = -2.0A$		110	160	mΩ
Gate to Source Cutoff Voltage	V _{GS(off)}	$V_{DS} = -10V, I_{D} = -1mA$	-1.0	-1.5	-2.5	V
Forward Transfer Admittance	yfs	V _{DS} = -10V, I _D = -2.5A	3.0	5.0		S
Drain Leakage Current	IDSS	$V_{DS} = -30V$, $V_{GS} = 0$			-10	μΑ
Gate to Source Leakage Current	Igss	$V_{GS} = -20V, V_{DS} = 0$			- 10	μΑ
Input Capacitance	Ciss	V _{DS} = -10V		980		pF
Output Capacitance	Coss	V _{GS} =0		780		pF
Reverse Transfer Capacitance	Crss	f = 1MHz		430		pF
Turn-On Delay Time	td(on)	ID = -2.5A		25		ns
Rise Time	tr	$V_{GS(on)} = -10V$		80		ns
Turn-Off Delay Time	td(off)	V _{DD} = -15V		100		ns
Fall Time	tr	$R_G = 10\Omega$		100		ns
Total Gate Charge	Q _G	ID = -5.0A		38		nC
Gate to Source Charge	Qgs	V _{DD} = -24V		3.6		nC
Gate to Drain Charge	Q _{GD}	Vgs = -10V		17		nC
Body Diode Forward Voltage	V _{F(S-D)}	IF = 5.0A, VGS = 0		0.8		V
Reverse Recovery Time	trr	IF = 5.0A, VGS = 0		85		ns
Reverse Recovery Charge	Qrr	$di/dt = 50A/\mu s$		200		nC

Test Circuit 1 Switching Time

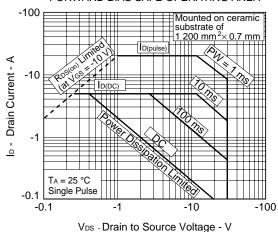
Test Circuit 2 Gate Charge

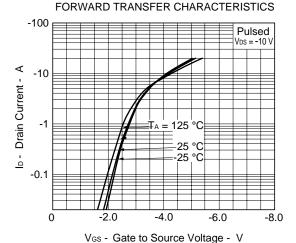


DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA

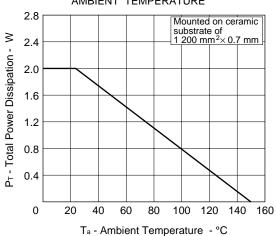


FORWARD BIAS SAFE OPERATING AREA

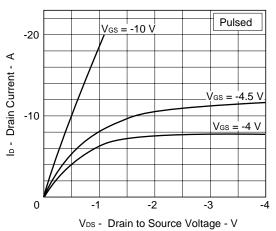




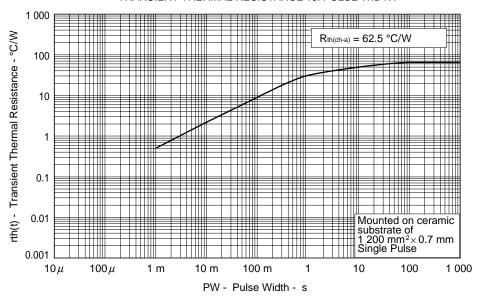
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



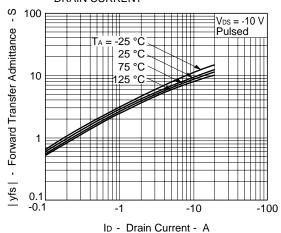
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



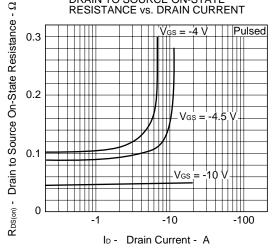
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



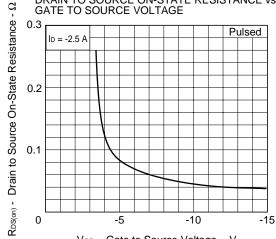
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE

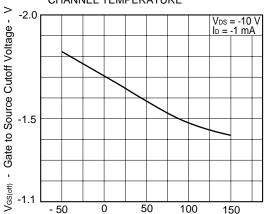


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



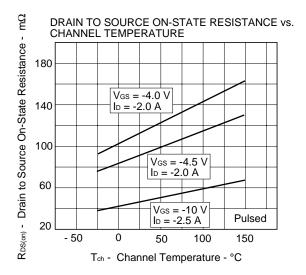
Vgs - Gate to Source Voltage - V

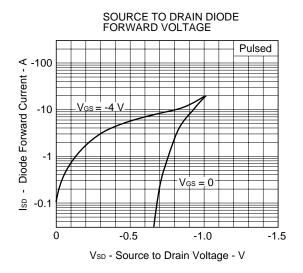
GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE

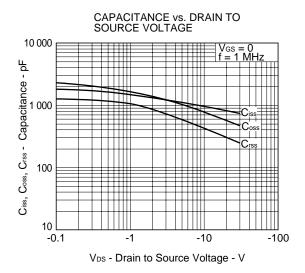


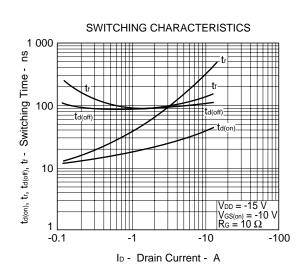
Tch - Channel Temperature - °C

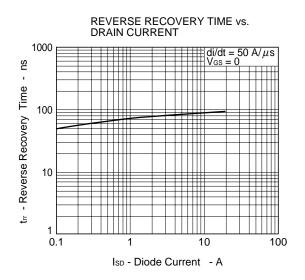


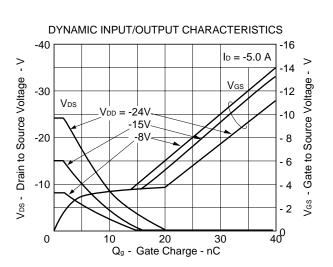
















REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system	TEI-1202
Quality grade on NEC semiconductor devices	IEI-1209
Semiconductor device mounting technology manual	C10535E
Semiconductor device package manual	C10943X
Guide to quality assurance for semiconductor devices	MEI-1202
Semiconductor selection guide	X10679E
Power MOS FET features and application switching power supply	TEA-1034
Application circuits using Power MOS FET	TEA-1035
Safe operating area of Power MOS FET	TEA-1037

[MEMO]



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Special: Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)

Specific: Aircrafts, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems or medical equipment for life support, etc.

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