

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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**P-CHANNEL MOSFET
FOR SWITCHING**

DESCRIPTION

The μPA1930 is a P-channel MOSFET designed for power switch of portable machine and so on.

FEATURES

- 4.5 V drive available
- $R_{DS(on)1} = 77 \text{ m}\Omega \text{ MAX. (} V_{GS} = -10 \text{ V, } I_D = -2.5 \text{ A)}$
- $R_{DS(on)2} = 100 \text{ m}\Omega \text{ MAX. (} V_{GS} = -4.5 \text{ V, } I_D = -2.5 \text{ A)}$

ORDERING INFORMATION

PART NUMBER	PACKAGE
μPA1930TE-T1-A	SC-95 (Mini Mold Thin Type)
μPA1930TE-T2-A	

Remark "-A" indicates Pb-free (This product does not contain Pb in external electrode and other parts).
 "-T1", "-T2" indicates the unit orientation (8 mm embossed carrier tape, 3,000 pcs/reel).

Marking : UA

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C)

Drain to Source Voltage (V _{GS} = 0V)	V _{DSS}	-30	V
Gate to Source Voltage (V _{DS} = 0V)	V _{GSS}	±20	V
Drain Current (DC) ^{Note1}	I _{D(DC)}	±4.5	A
Drain Current (pulse) ^{Note2}	I _{D(pulse)}	±18	A
Total Power Dissipation	P _{T1}	0.2	W
Total Power Dissipation ^{Note1}	P _{T2}	2.0	W
Channel Temperature	T _{ch}	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

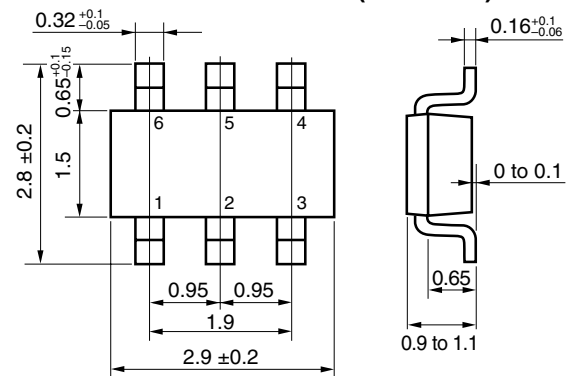
- Notes**
- Mounted on FR-4 Board 2500 mm² × 1.6 mm, t ≤ 5 sec
 - PW ≤ 10 μs, Duty Cycle ≤ 1%

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

Caution This product is electrostatic-sensitive device due to low ESD capability and should be handled with caution for electrostatic discharge. V_{ESD} ± 150 V TYP. (C = 200 pF, R = 0 Ω, Single pulse)

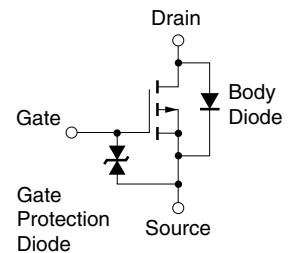
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PACKAGE DRAWING (Unit : mm)



- 1, 2, 5, 6 : Drain
- 3 : Gate
- 4 : Source

EQUIVALENT CIRCUIT

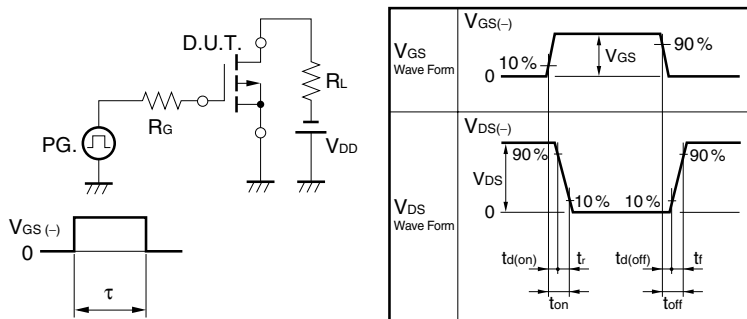


ELECTRICAL CHARACTERISTICS (T_A = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -30 V, V _{GS} = 0 V			-1	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V			±10	μA
Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = -10 V, I _D = -1.0 mA	-1.0		-2.5	V
Forward Transfer Admittance ^{Note}	y _{fs}	V _{DS} = -10 V, I _D = -2.5 A	1			S
Drain to Source On-state Resistance ^{Note}	R _{DS(on)1}	V _{GS} = -10 V, I _D = -2.5 A		58	77	mΩ
	R _{DS(on)2}	V _{GS} = -4.5 V, I _D = -2.5 A		77	100	mΩ
Input Capacitance	C _{iss}	V _{DS} = -10 V		325		pF
Output Capacitance	C _{oss}	V _{GS} = 0 V		78		pF
Reverse Transfer Capacitance	C _{rss}	f = 1.0 MHz		65		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = -15 V, I _D = -2.5 A,		8.5		ns
Rise Time	t _r	V _{GS} = -10 V,		3.5		ns
Turn-off Delay Time	t _{d(off)}	R _G = 6 Ω		33		ns
Fall Time	t _f			19.5		ns
Total Gate Charge	Q _G	V _{DD} = -24 V,		7.5		nC
Gate to Source Charge	Q _{GS}	V _{GS} = -10 V,		1.1		nC
Gate to Drain Charge	Q _{GD}	I _D = -4.5 A		2.3		nC
Diode Forward Voltage ^{Note}	V _{F(S-D)}	I _F = 4.5 A, V _{GS} = 0 V		0.93		V

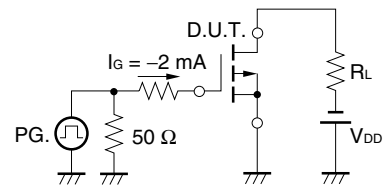
Note Pulsed

TEST CIRCUIT 1 SWITCHING TIME



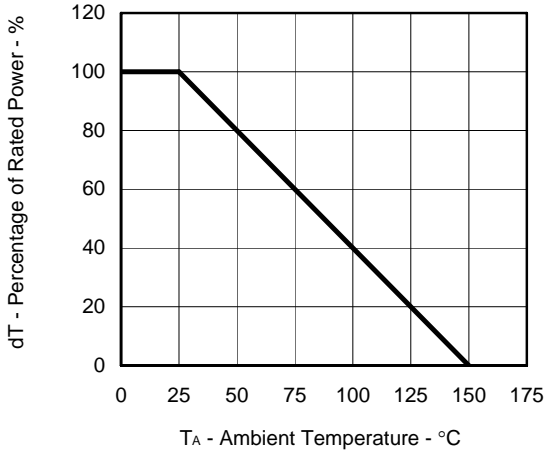
τ = 1 μs
Duty Cycle ≤ 1%

TEST CIRCUIT 2 GATE CHARGE

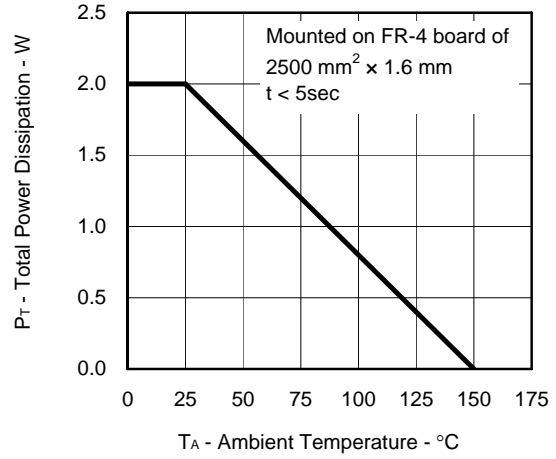


TYPICAL CHARACTERISTICS (T_A = 25°C)

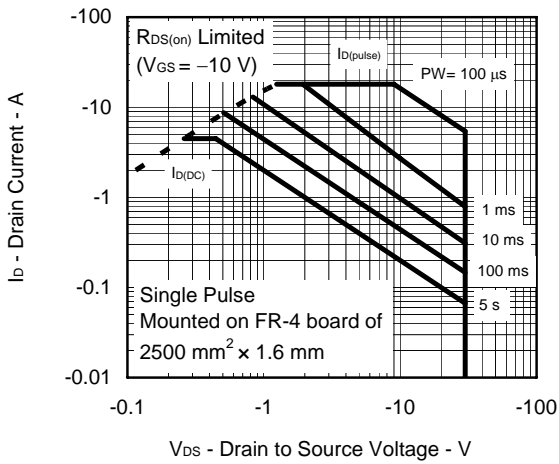
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



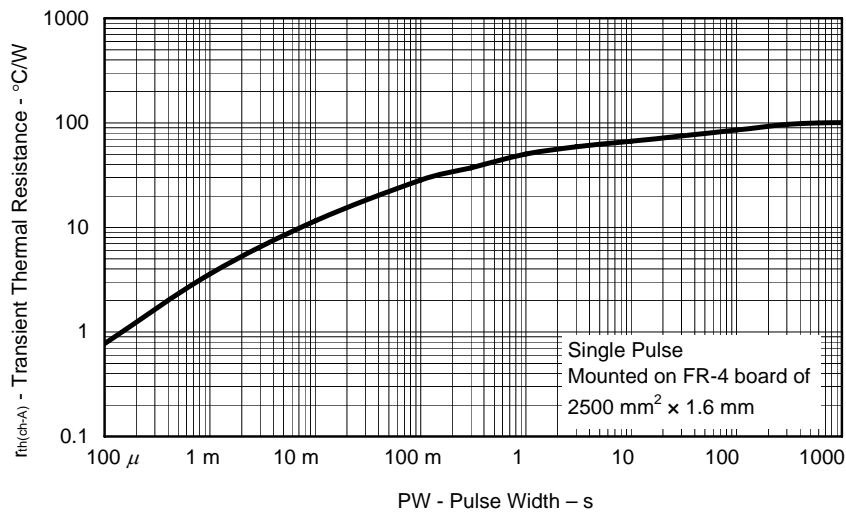
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



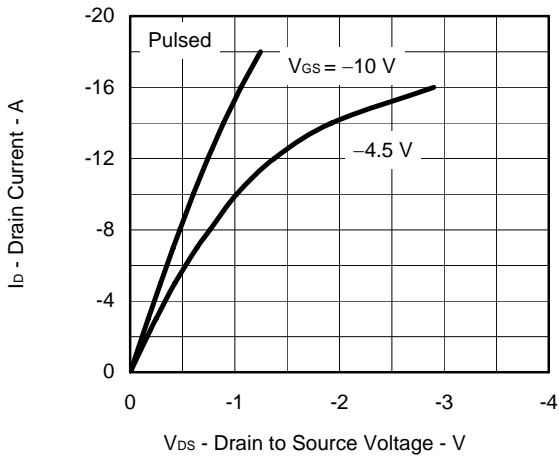
FORWARD BIAS SAFE OPERATING AREA



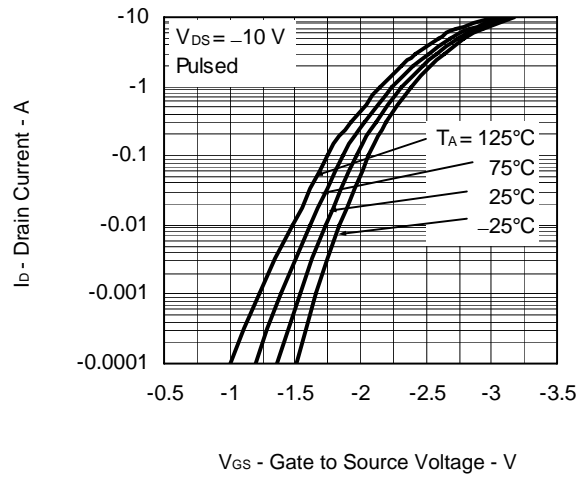
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



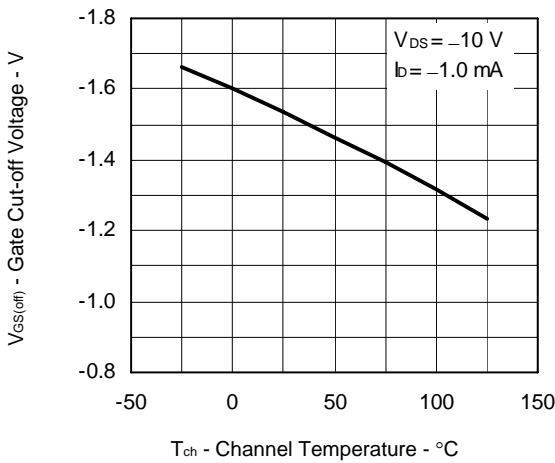
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



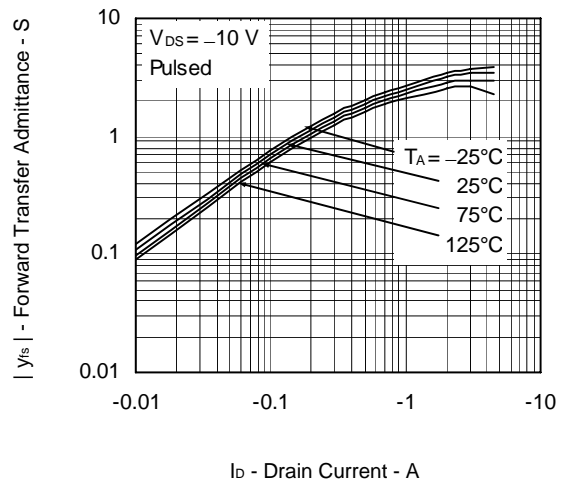
FORWARD TRANSFER CHARACTERISTICS



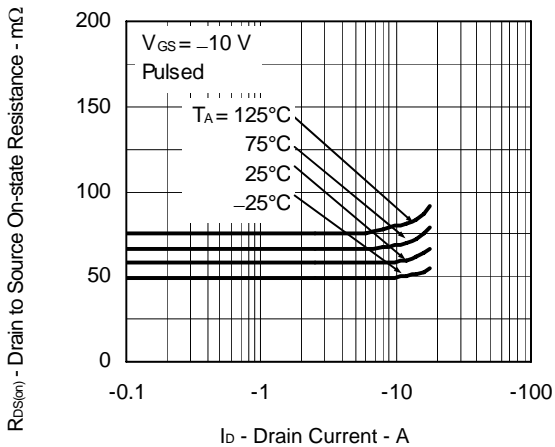
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



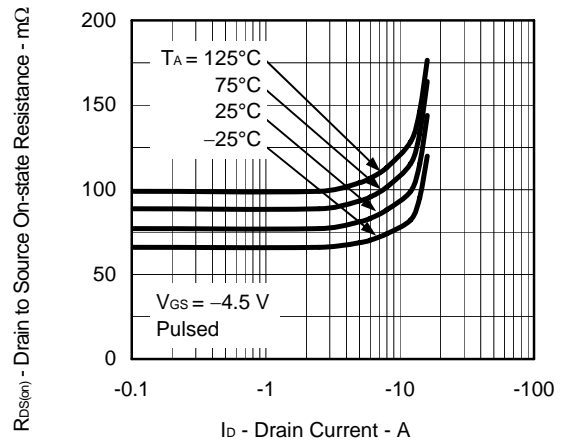
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



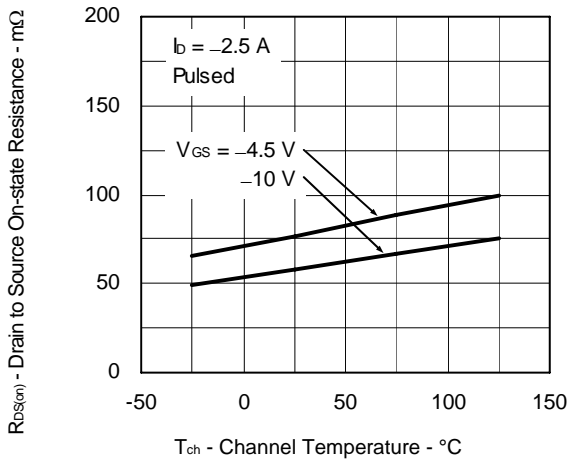
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



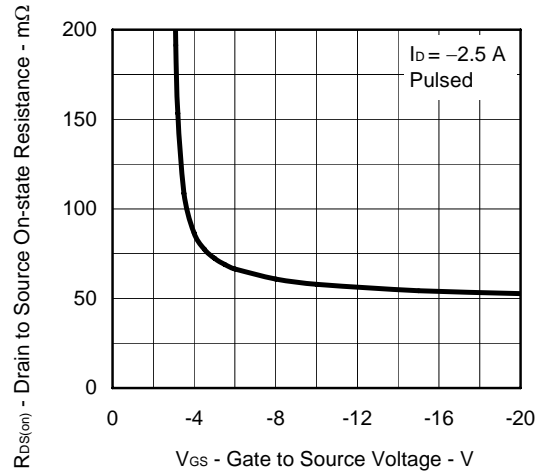
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



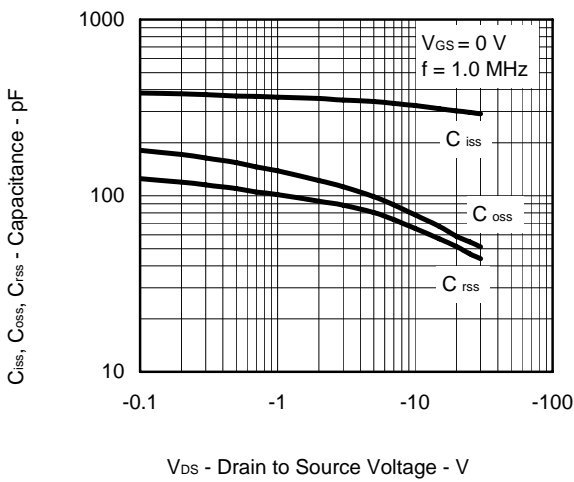
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



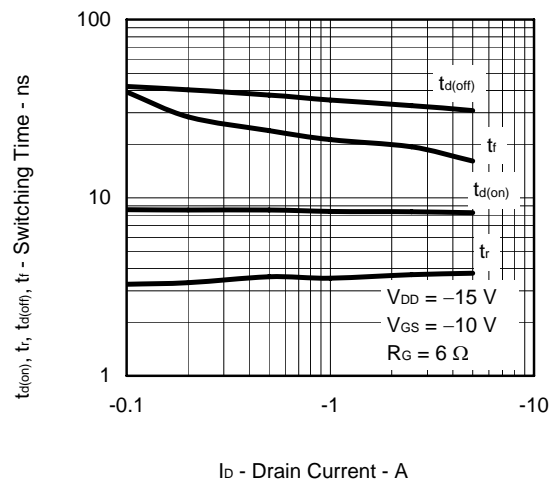
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



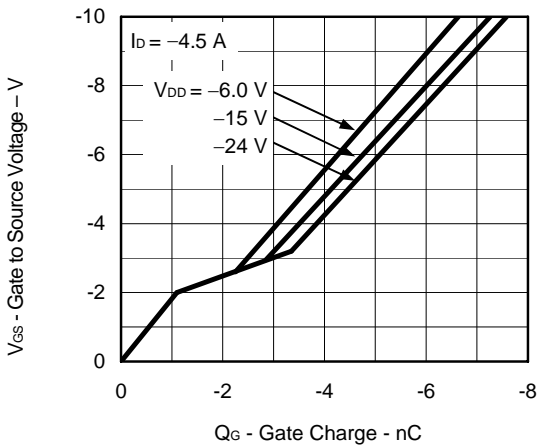
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



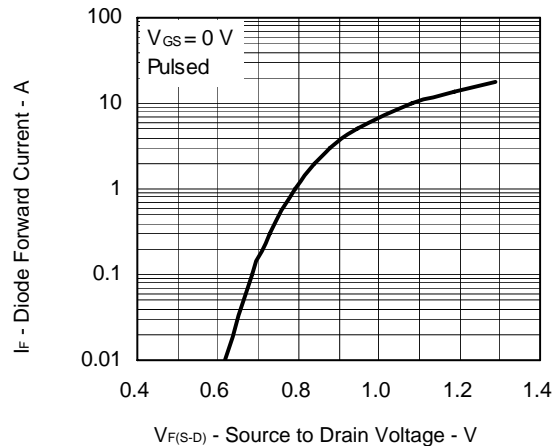
SWITCHING CHARACTERISTICS



DYNAMIC INPUT CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



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