

# μ PA2593

R07DS0012EJ0200

Rev.2.00

Sep 10, 2010

## MOS FIELD EFFECT TRANSISTOR

### Description

The μPA2593 is N- and P-channel MOSFETs designed for DC/DC converters and power management applications of portable equipments.

N- and P-channel MOSFETs are assembled in one package, to contribute minimize the equipments.

### Features

- 4.5 V drive available
- Low on-state resistance
  - N-channel
    - $R_{DS(on)1} = 58 \text{ m}\Omega \text{ MAX.}$  ( $V_{GS} = 10 \text{ V}$ ,  $I_D = 2 \text{ A}$ )
    - $R_{DS(on)2} = 103 \text{ m}\Omega \text{ MAX.}$  ( $V_{GS} = 4.5 \text{ V}$ ,  $I_D = 2 \text{ A}$ )
  - P-channel
    - $R_{DS(on)1} = 140 \text{ m}\Omega \text{ MAX.}$  ( $V_{GS} = -10 \text{ V}$ ,  $I_D = -2 \text{ A}$ )
    - $R_{DS(on)2} = 195 \text{ m}\Omega \text{ MAX.}$  ( $V_{GS} = -4.5 \text{ V}$ ,  $I_D = -2 \text{ A}$ )
- Built-in gate protection diode
- Small and surface mount package (8-pin VSOF (2429))

### Ordering Information

Part No.	LEAD PLATING	PACKING	Package
μ PA2593T1H-T1-AT *1	Pure Sn (Tin)	8 mm embossed taping	8-pin VSOF (2429)
μ PA2593T1H-T2-AT *1		3000 p/reel	

Note: \*1. Pb-free (This product does not contain Pb in the external electrode and other parts.)

Marking: 2593

### Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$ )

Item	Symbol	N-CHANNEL	P-CHANNEL	Unit
Drain to Source Voltage ( $V_{GS} = 0 \text{ V}$ )	$V_{DSS}$	40	-40	V
Gate to Source Voltage ( $V_{DS} = 0 \text{ V}$ )	$V_{GSS}$	$\pm 18$	$\mp 18$	V
Drain Current (DC)	$I_{D(DC)}$	$\pm 4.5$	$\mp 3.5$	A
Drain Current (pulse) *1	$I_{D(pulse)}$	$\pm 18$	$\mp 18$	A
Total Power Dissipation (1 unit, 5 s) *2	$P_{T1}$	1.5		W
Total Power Dissipation (2 unit, 5 s) *2	$P_{T2}$	1.24		W
Channel Temperature	$T_{ch}$	150		$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55 to +150		$^\circ\text{C}$

Notes: \*1.  $PW \leq 10 \mu\text{s}$ , Duty Cycle  $\leq 1\%$

\*2. Mounted on FR-4 board of 25.4 mm x 25.4 mm x 0.8 mm

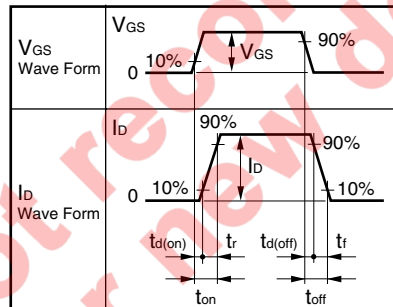
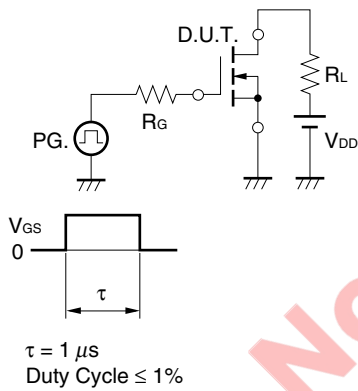
**Electrical Characteristics (T<sub>A</sub> = 25°C)**

**N-channel MOSFET**

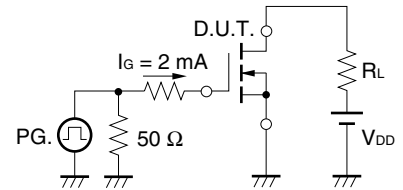
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			1	μA	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V
Gate Leakage Current	I <sub>GSS</sub>			±10	μA	V <sub>GS</sub> = ±14 V, V <sub>DS</sub> = 0 V
Gate Cut-off Voltage	V <sub>GS(off)</sub>	1.0		2.5	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Forward Transfer Admittance *1	y <sub>fs</sub>	2.0			S	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 2 A
Drain to Source On-state Resistance *1	R <sub>DS(on)1</sub>		50	58	mΩ	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2 A
	R <sub>DS(on)2</sub>		70	103	mΩ	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 2 A
Input Capacitance	C <sub>iss</sub>		315		pF	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz
Output Capacitance	C <sub>oss</sub>		70		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>		38		pF	
Turn-on Delay Time	t <sub>d(on)</sub>		5		ns	V <sub>DD</sub> = 20 V, I <sub>D</sub> = 2 A, V <sub>GS</sub> = 10 V, R <sub>G</sub> = 6 Ω
Rise Time	t <sub>r</sub>		3		ns	
Turn-off Delay Time	t <sub>d(off)</sub>		21		ns	
Fall Time	t <sub>f</sub>		3		ns	
Total Gate Charge	Q <sub>G</sub>		7		nC	V <sub>DD</sub> = 32 V, V <sub>GS</sub> = 10 V I <sub>D</sub> = 4.5 A
Body Diode Forward Voltage *1	V <sub>F(S-D)</sub>		0.9		V	I <sub>F</sub> = 4.5 A, V <sub>GS</sub> = 0 V

Note: \*1. Pulsed

**TEST CIRCUIT 1 SWITCHING TIME**



**TEST CIRCUIT 2 GATE CHARGE**

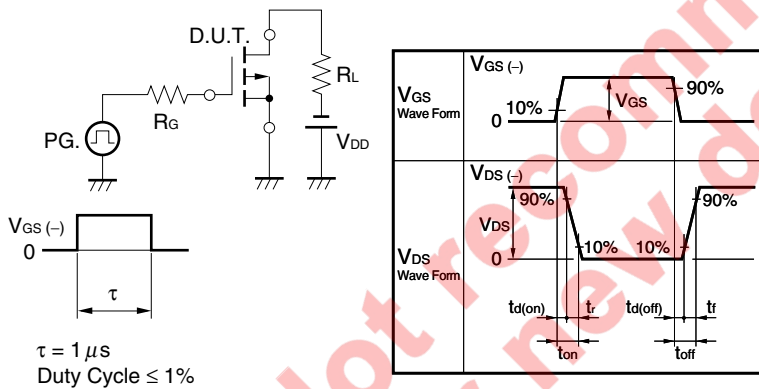


**P-channel MOSFET**

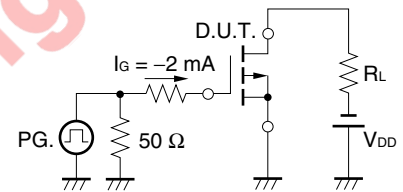
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Zero Gate Voltage Drain Current	$I_{DSS}$			-1	μA	$V_{DS} = -40\text{ V}, V_{GS} = 0\text{ V}$
Gate Leakage Current	$I_{GSS}$			±10	μA	$V_{GS} = \mp 14\text{ V}, V_{DS} = 0\text{ V}$
Gate Cut-off Voltage	$V_{GS(off)}$	-1.0		-2.5	V	$V_{DS} = -10\text{ V}, I_D = -1\text{ mA}$
Forward Transfer Admittance *1	$ y_{fs} $	2.0			S	$V_{DS} = -5\text{ V}, I_D = -2\text{ A}$
Drain to Source On-state Resistance *1	$R_{DS(on)1}$		115	140	mΩ	$V_{GS} = -10\text{ V}, I_D = -2\text{ A}$
	$R_{DS(on)2}$		135	195	mΩ	$V_{GS} = -4.5\text{ V}, I_D = -2\text{ A}$
Input Capacitance	$C_{iss}$		350		pF	$V_{DS} = -10\text{ V},$ $V_{GS} = 0\text{ V},$ $f = 1\text{ MHz}$
Output Capacitance	$C_{oss}$		60		pF	
Reverse Transfer Capacitance	$C_{rss}$		47		pF	
Turn-on Delay Time	$t_{d(on)}$		5.5		ns	$V_{DD} = -20\text{ V}, I_D = -2\text{ A},$ $V_{GS} = -10\text{ V},$ $R_G = 6\ \Omega$
Rise Time	$t_r$		3.5		ns	
Turn-off Delay Time	$t_{d(off)}$		40		ns	
Fall Time	$t_f$		8		ns	
Total Gate Charge	$Q_G$		8		nC	$V_{DD} = -32\text{ V}, V_{GS} = -10\text{ V}$ $I_D = -3.5\text{ A}$
Body Diode Forward Voltage *1	$V_{F(S-D)}$		0.95		V	$I_F = -3.5\text{ A}, V_{GS} = 0\text{ V}$

Note: \*1. Pulsed

**TEST CIRCUIT 1 SWITCHING TIME**



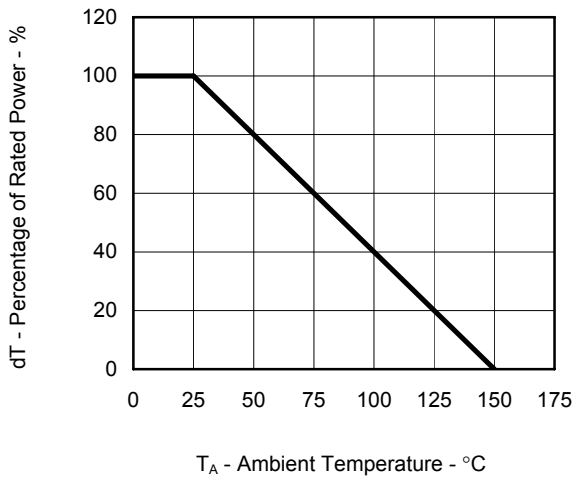
**TEST CIRCUIT 2 GATE CHARGE**



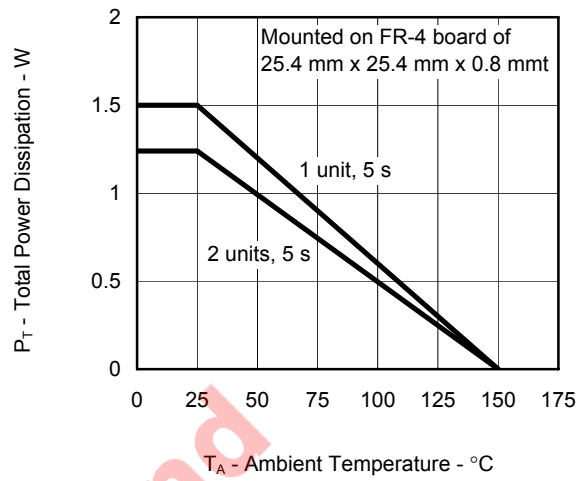
Typical Characteristics (T<sub>A</sub> = 25°C)

(1) N-channel MOSFET

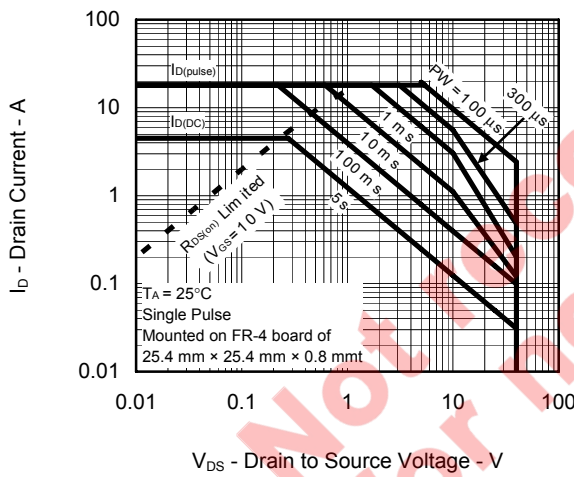
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



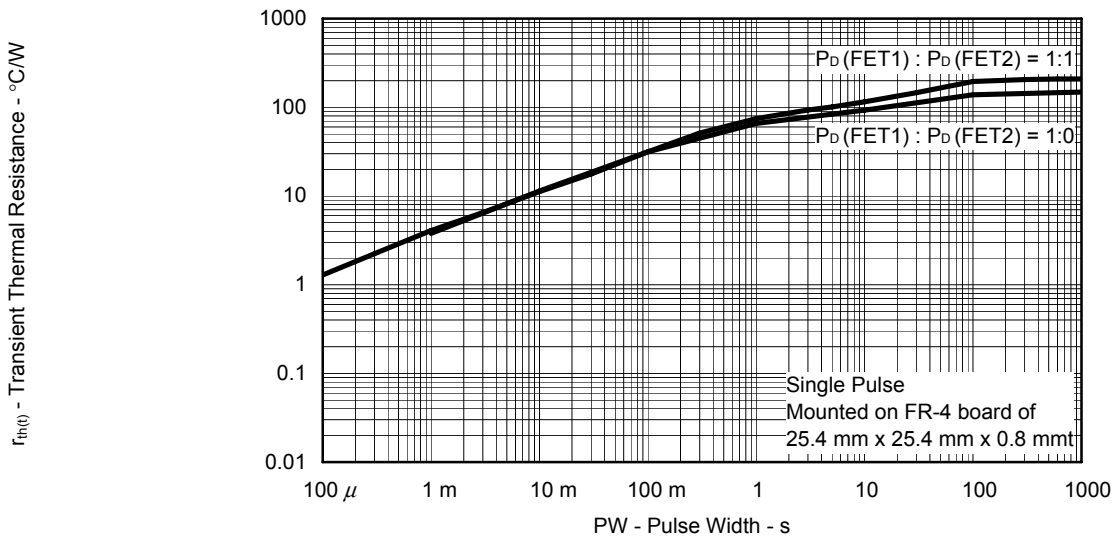
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



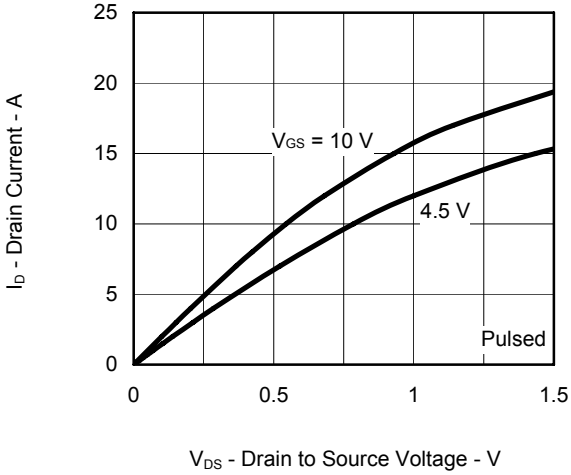
FORWARD BIAS SAFE OPERATING AREA



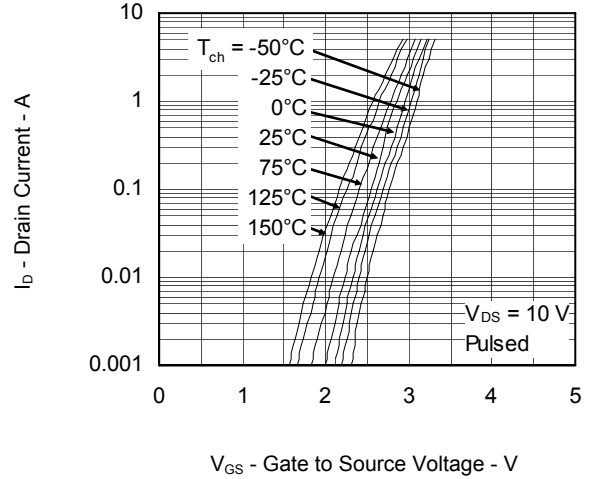
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



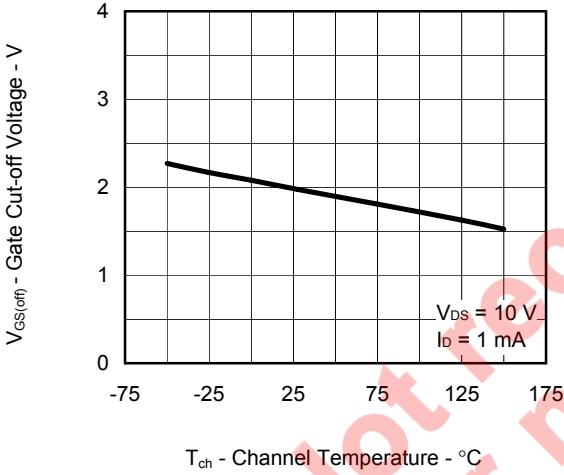
DRAIN CURRENT vs.  
DRAIN TO SOURCE VOLTAGE



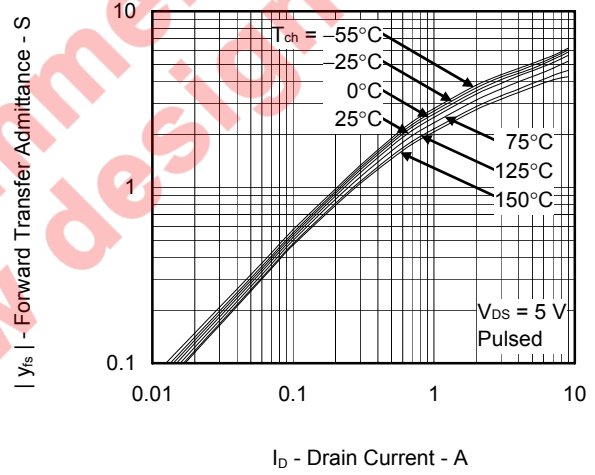
FORWARD TRANSFER CHARACTERISTICS



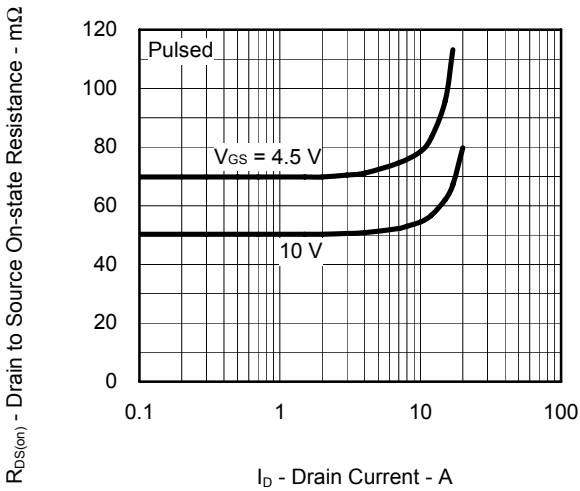
GATE CUT-OFF VOLTAGE vs.  
CHANNEL TEMPERATURE



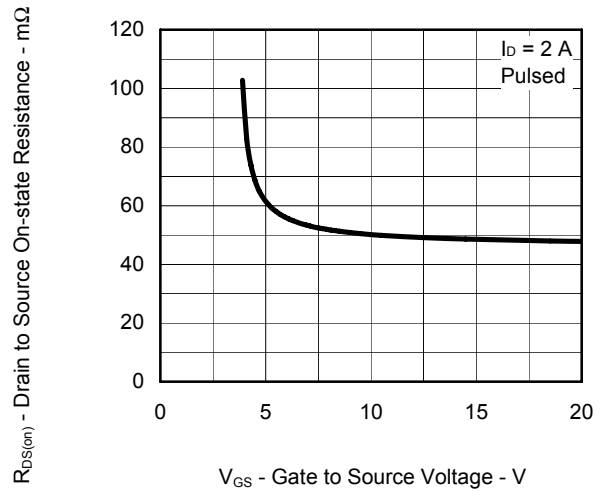
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



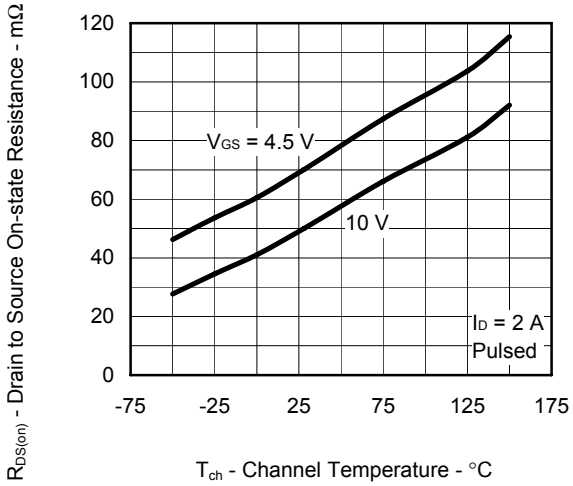
DRAIN TO SOURCE ON-STATE RESISTANCE vs.  
DRAIN CURRENT



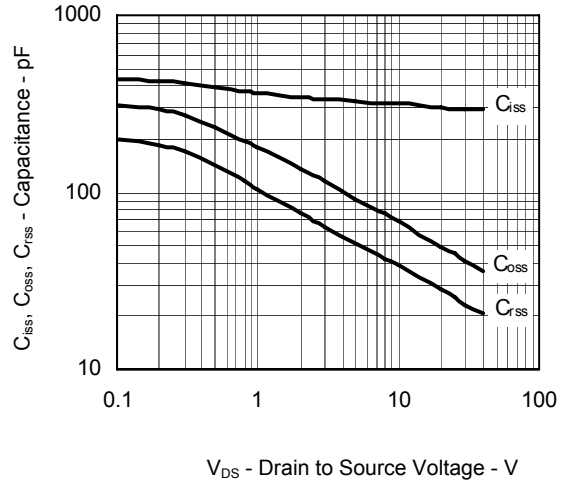
DRAIN TO SOURCE ON-STATE RESISTANCE vs.  
GATE TO SOURCE VOLTAGE



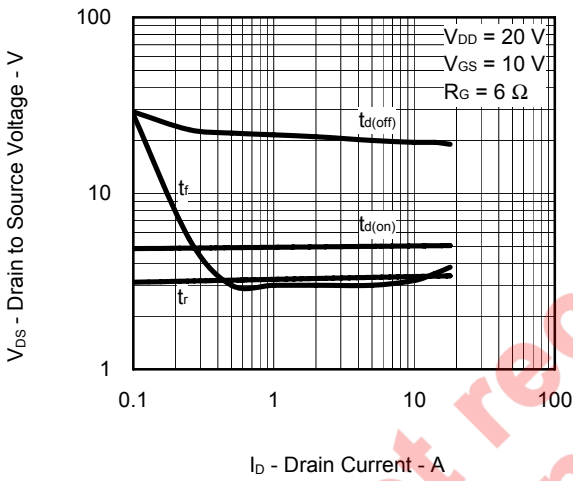
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



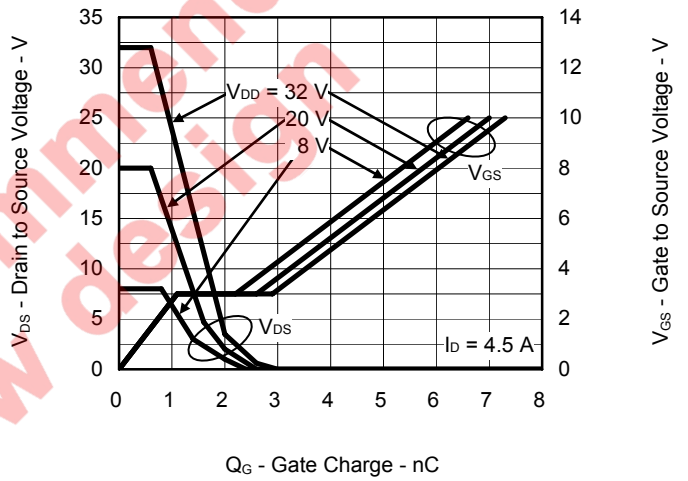
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



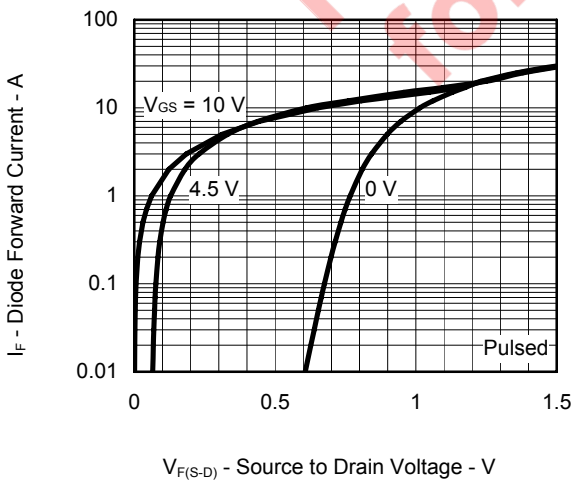
SWITCHING CHARACTERISTICS



DYNAMIC INPUT/OUTPUT CHARACTERISTICS

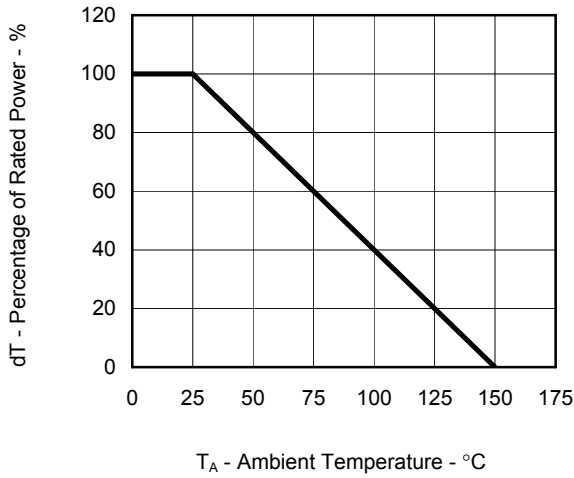


SOURCE TO DRAIN DIODE FORWARD VOLTAGE

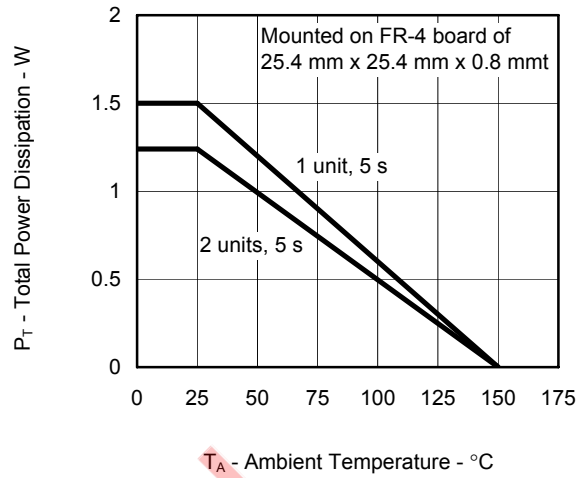


(2) P-channel MOSFET

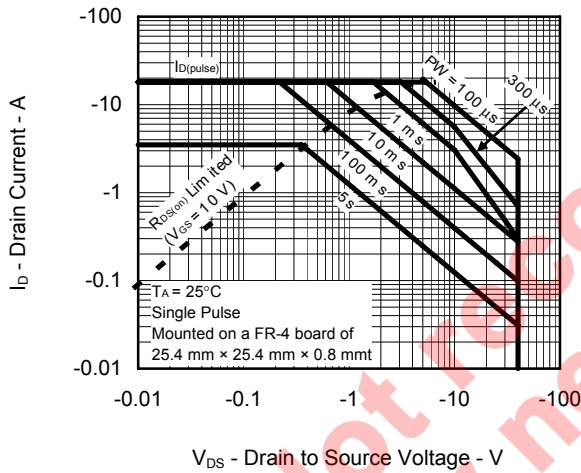
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



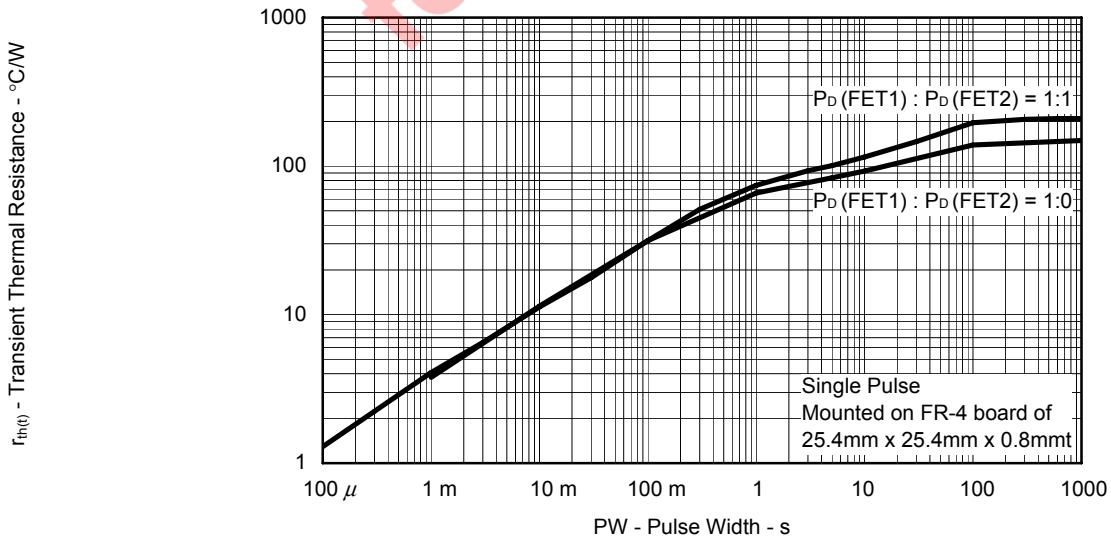
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



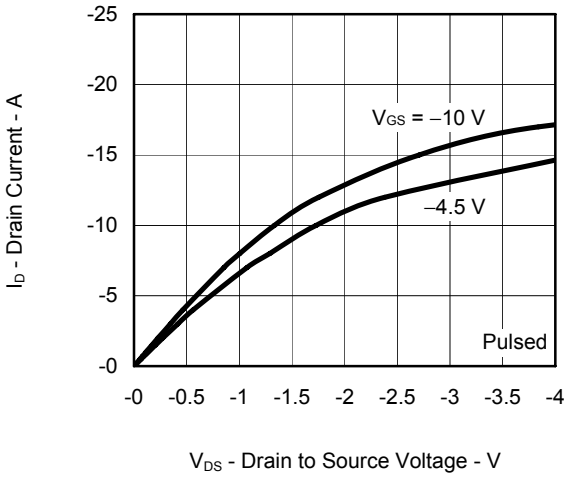
FORWARD BIAS SAFE OPERATING AREA



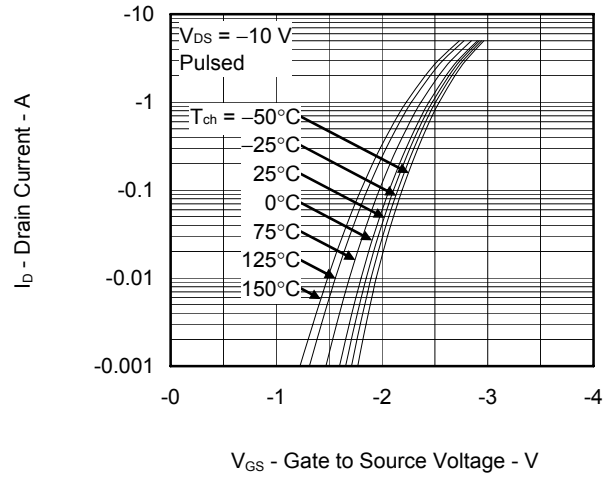
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



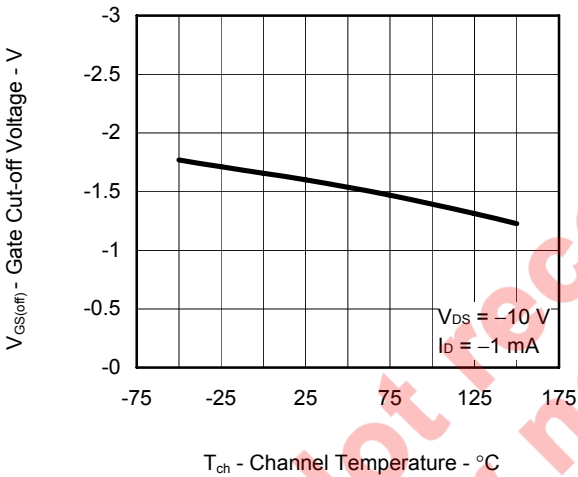
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



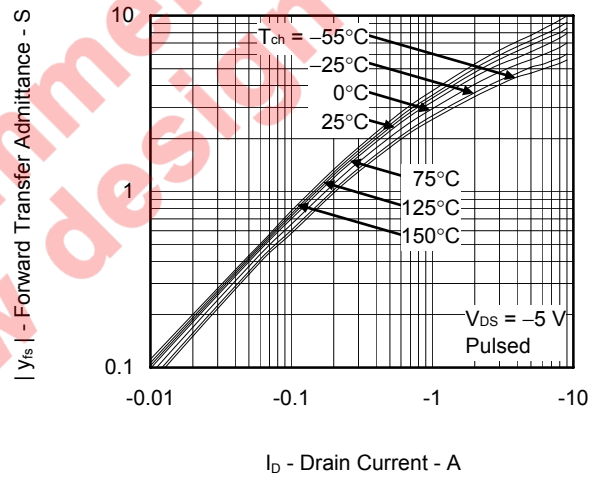
FORWARD TRANSFER CHARACTERISTICS



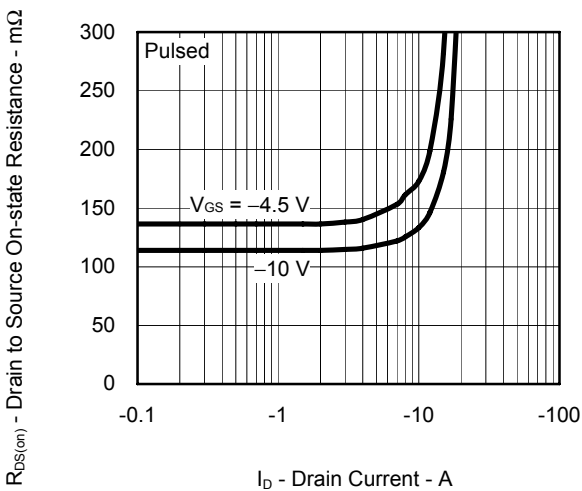
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



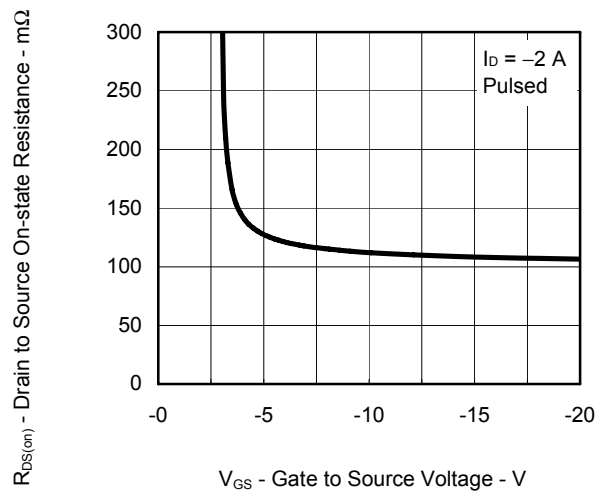
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

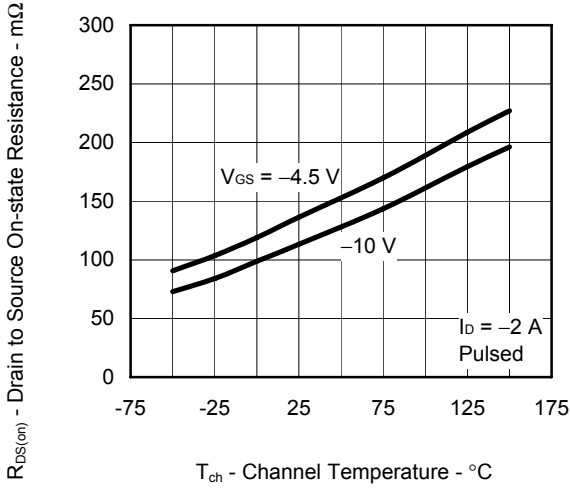


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

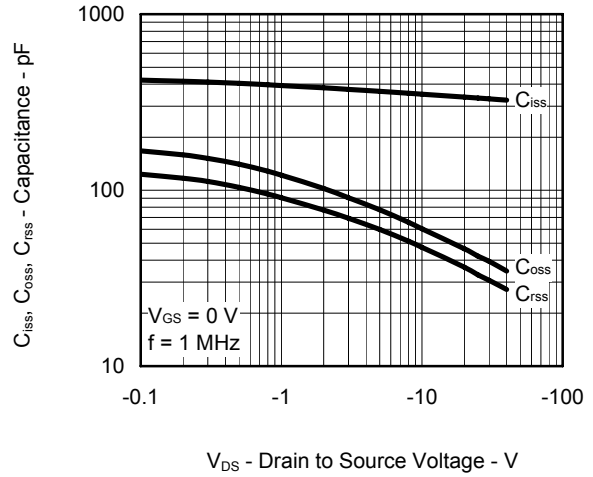




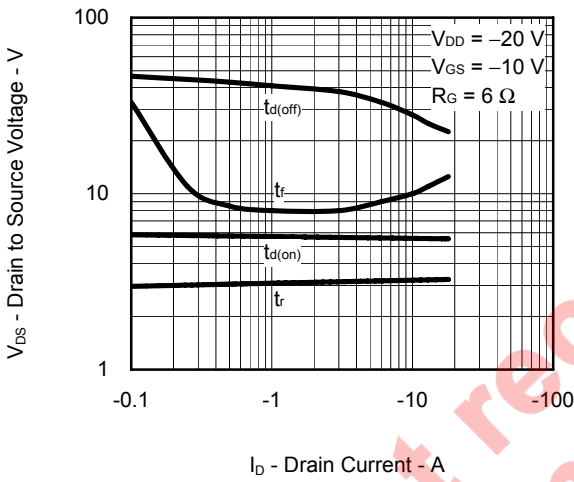
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



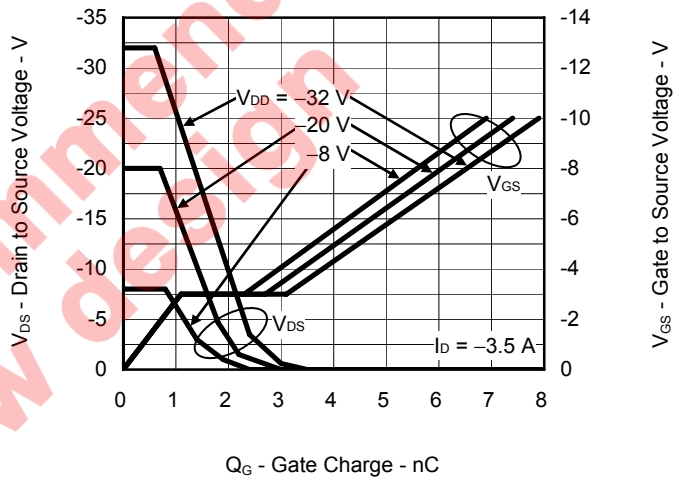
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



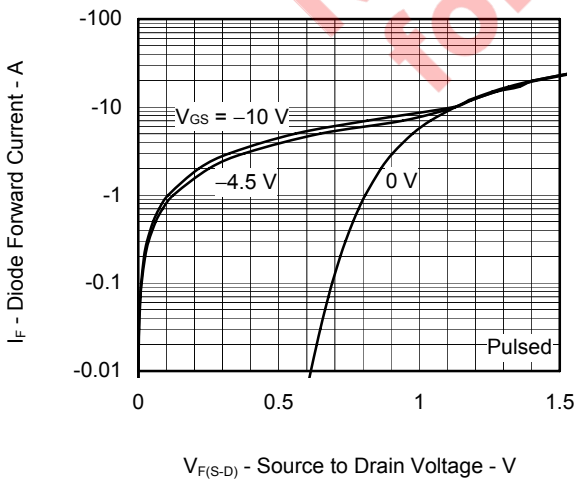
SWITCHING CHARACTERISTICS



DYNAMIC INPUT/OUTPUT CHARACTERISTICS

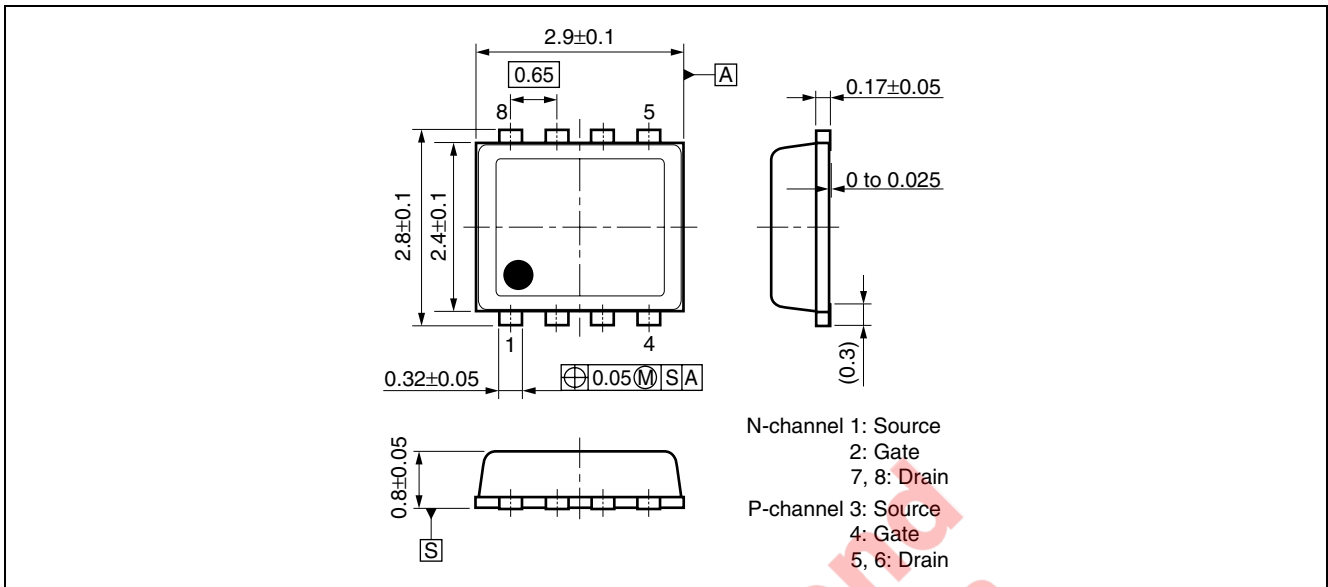


SOURCE TO DRAIN DIODE FORWARD VOLTAGE

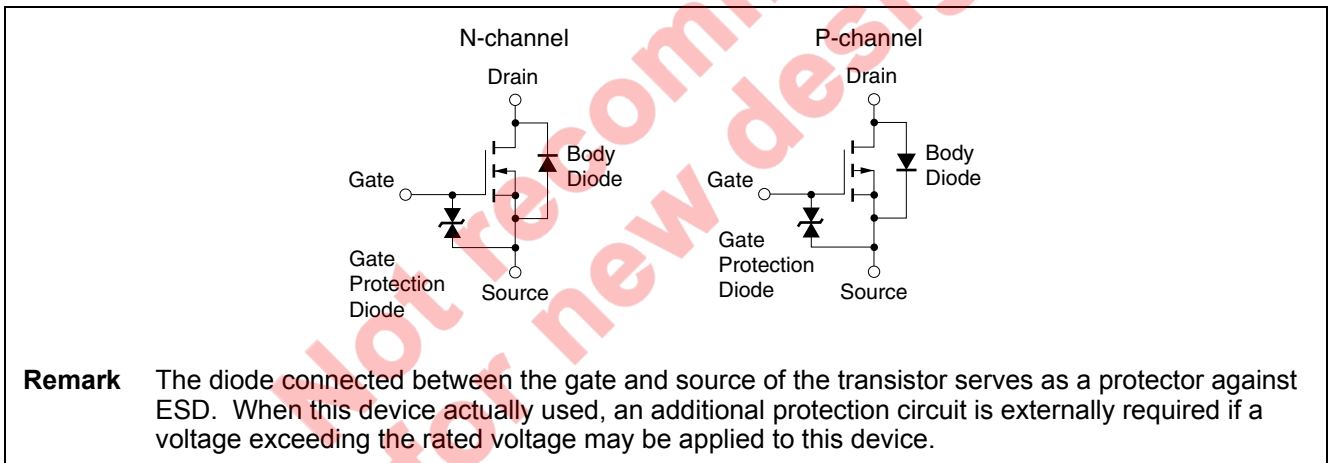


Package Drawings (Unit: mm)

8-pin VSOF (2429)



Equivalent Circuit



<b>Revision History</b>	<b>μPA2593</b>
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Rev.	Date	Description	
		Page	Summary
1.00	Jul 01, 2010	–	First Eddition Issued
2.00	Sep 10,2010	5, 6	Change of graphs

**Not recommend  
for new design**

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