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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# MOS FIELD EFFECT TRANSISTOR $\mu$ PA2730TP

# SWITCHING P-CHANNEL POWER MOS FET

#### **DESCRIPTION**

The  $\mu$ PA2730TP which has a heat spreader is P-Channel MOS Field Effect Transistor designed for power management applications of notebook computers and Li-ion battery protection circuit.

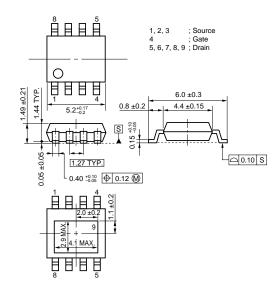
#### **FEATURES**

- · Low on-state resistance
  - $R_{DS(on)1} = 7.0 \text{ m}\Omega$  MAX. (Vgs = -10 V, ID = -7.5 A)
  - $R_{DS(on)2} = 10.5 \text{ m}\Omega$  MAX. (Vgs = -4.5 V, ID = -7.5 A)
  - $R_{DS(on)3} = 12.0 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = -4.0 \text{ V, ID} = -7.5 \text{ A)}$
- Low Ciss: Ciss = 4670 pF TYP.
- Small and surface mount package (Power HSOP8)

#### ORDERING INFORMATION

PART NUMBER	PACKAGE
μPA2730TP	Power HSOP8

#### **PACKAGE DRAWING (Unit: mm)**



#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C, Unless otherwise noted, All terminals are connected.)

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- **Notes 1.** Mounted on a glass epoxy board (1 inch x 1 inch x 0.8 mm), PW = 10 sec
  - **2.** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%
  - 3. Starting Tch = 25°C, VdD = -15 V, Rg = 25  $\Omega$ , L = 100  $\mu$ H, Vgs = -20  $\rightarrow$  0 V

# **Remark** Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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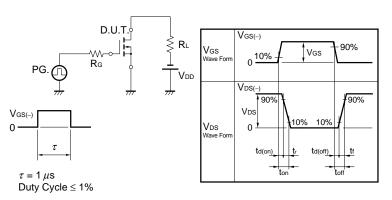
#### ELECTRICAL CHARACTERISTICS (TA = 25°C, Unless otherwise noted, All terminals are connected.)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	Ipss	V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V			-1	μΑ
Gate Leakage Current	Igss	V <sub>GS</sub> = ∓20 V, V <sub>DS</sub> = 0 V			∓100	nA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -1 mA	-1.0		-2.5	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -7.5 A	14	30		S
Drain to Source On-state Resistance	RDS(on)1	Ves = -10 V, Ib = -7.5 A		5.7	7.0	mΩ
	RDS(on)2	Ves = -4.5 V, I <sub>D</sub> = -7.5 A		7.7	10.5	mΩ
	R <sub>DS(on)3</sub>	V <sub>G</sub> S = -4.0 V, I <sub>D</sub> = -7.5 A		8.8	12.0	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = −10 V		4670		pF
Output Capacitance	Coss	Ves = 0 V		1220		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		760		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = -15 V, I <sub>D</sub> = -7.5 A		20		ns
Rise Time	tr	V <sub>GS</sub> = −10 V		28		ns
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 10 \Omega$		190		ns
Fall Time	tf			110		ns
Total Gate Charge	Q <sub>G</sub>	V <sub>DD</sub> = −24 V		97		nC
Gate to Source Charge	Qgs	V <sub>G</sub> S = −10 V		10		nC
Gate to Drain Charge	Q <sub>GD</sub>	ID = 15 A		32		nC
Body Diode Forward Voltage	V <sub>F</sub> (S-D)	IF = 15 A, VGS = 0 V		0.81		V
Reverse Recovery Time	trr	IF = 15 A, VGS = 0 V		65		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ μs		62		nC

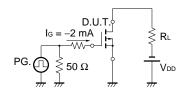
#### **TEST CIRCUIT 1 AVALANCHE CAPABILITY**

# $\begin{array}{c} \text{D.U.T.} \\ \text{PG.} \\ \text{Vgs} = -20 \rightarrow 0 \text{ V} \\ \end{array} \begin{array}{c} \text{D.U.T.} \\ \text{Starting Tch} \\ \end{array}$

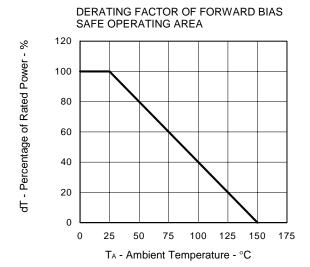
#### **TEST CIRCUIT 2 SWITCHING TIME**



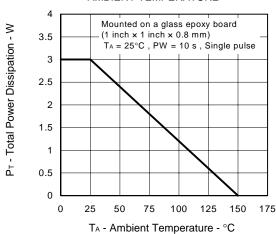
#### **TEST CIRCUIT 3 GATE CHARGE**



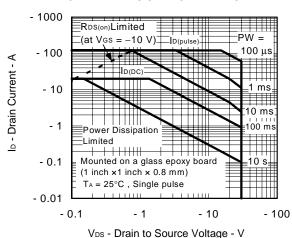
#### TYPICAL CHARACTERISTICS (TA = 25°C)



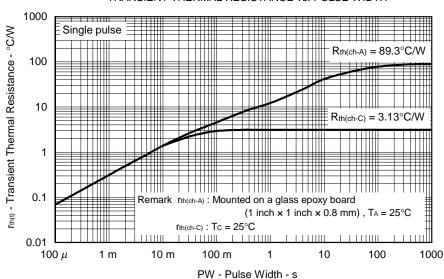
## TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



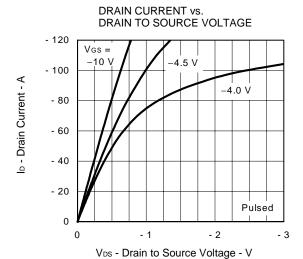
#### FORWARD BIAS SAFE OPERATING AREA



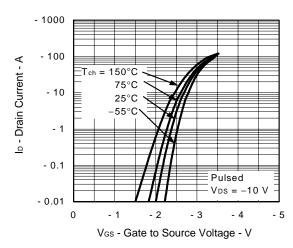
#### TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

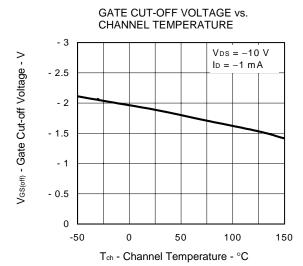


3

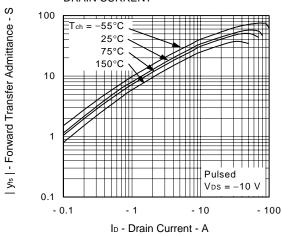


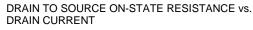
#### FORWARD TRANSFER CHARACTERISTICS

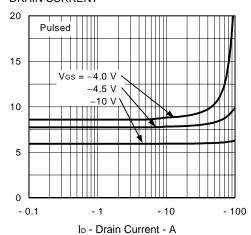




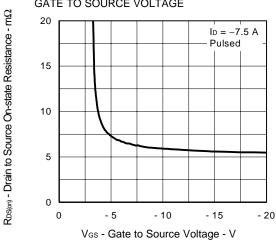
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT







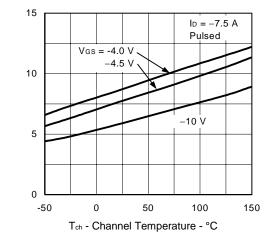
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



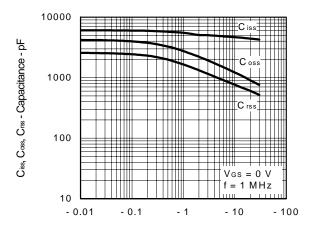
Rps(on) - Drain to Source On-state Resistance - mΩ

RDS(m) - Drain to Source On-state Resistance - mΩ

## DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE

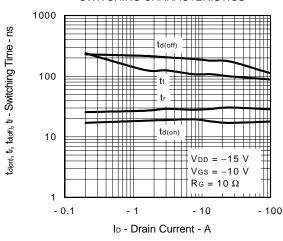


#### CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

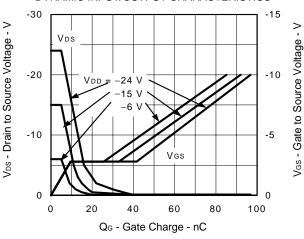


V<sub>DS</sub> - Drain to Source Voltage - V

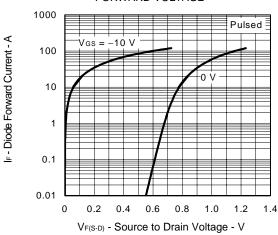
#### **SWITCHING CHARACTERISTICS**



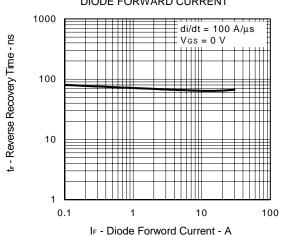
DYNAMIC INPUT/OUTPUT CHARACTERISTICS

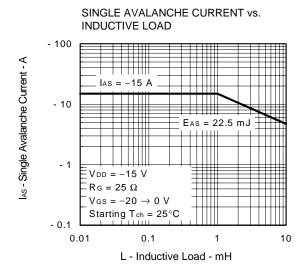


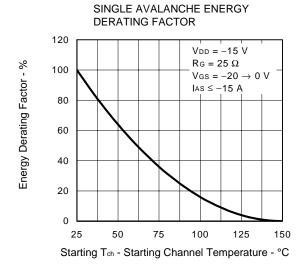
# SOURCE TO DRAIN DIODE FORWARD VOLTAGE



REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT







NEC  $\mu$ PA2730TP

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