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April 1st, 2010 Renesas Electronics Corporation

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MOS FIELD EFFECT TRANSISTOR

μ PA653TT

P-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

DESCRIPTION

The $\mu PA653TT$ is a switching device, which can be driven directly by a 4.0 V power source.

This device features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- 4.0 V drive available
- Low on-state resistance

 $R_{DS(on)1} = 165 \text{ m}\Omega \text{ MAX.}$ (Vgs = -10 V, ID = -1.5 A)

 $R_{DS(on)2} = 267 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = -4.5 \text{ V, ID} = -1.5 \text{ A)}$

RDS(on)3 = 304 m Ω MAX. (VGS = -4.0 V, ID = -1.5 A)

ORDERING INFORMATION

PART NUMBER	PACKAGE			
μPA653TT	6pinWSOF (1620)			

Marking: WG

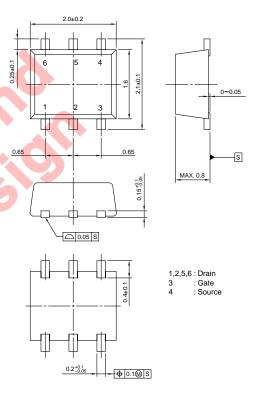
ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	-30	V
Gate to Source Voltage (Vbs = 0 V)	Vgss	∓20	V
Drain Current (DC)	ID(DC)	∓2.5	Α
Drain Current (pulse) Note1	ID(pulse)	∓10	Α
Total Power Dissipation	P _{T1}	0.2	W
Total Power Dissipation Note2	P _{T2}	1.3	W
Channel Temperature	T_ch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

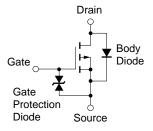
Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Mounted on FR-4 board of 5000 mm² x 1.1 mm, $t \le 5$ sec.

PACKAGE DRAWING (Unit: mm)



EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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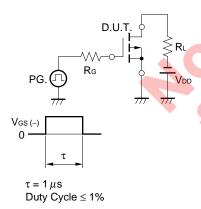
Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

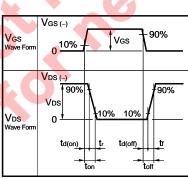


ELECTRICAL CHARACTERISTICS (TA = 25°C)

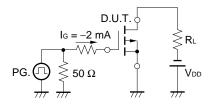
	1					
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	Vps = -30 V, Vgs = 0 V			-10	μΑ
Gate Leakage Current	lgss	$V_{GS} = \mp 20 \text{ V}, V_{DS} = 0 \text{ V}$			∓10	μΑ
Gate Cut-off Voltage	V _{GS(off)}	$V_{DS} = -10 \text{ V}, I_{D} = -1.0 \text{ mA}$	-1.5	-1.8	-2.5	V
Forward Transfer Admittance	y fs	$V_{DS} = -10 \text{ V}, I_{D} = -1.5 \text{ A}$	1.0	2.9		S
Drain to Source On-state Resistance	RDS(on)1	V _G S = −10 V, I _D = −1.5 A		132	165	mΩ
	RDS(on)2	V _G S = −4.5 V, I _D = −1.5 A		200	267	mΩ
	RDS(on)3	$V_{GS} = -4.0 \text{ V}, I_{D} = -1.5 \text{ A}$		228	304	mΩ
Input Capacitance	Ciss	V _{DS} = −10 V		175		pF
Output Capacitance	Coss	Vgs = 0 V		56		pF
Reverse Transfer Capacitance	Crss	f = 1.0 MHz		25		pF
Turn-on Delay Time	td(on)	V _{DD} = −15 V, I _D = −1.5 A		12		ns
Rise Time	t r	Vgs = −10 V		40		ns
Turn-off Delay Time	td(off)	R _G = 10 Ω		128		ns
Fall Time	t f			82		ns
Total Gate Charge	Q _G	V _{DD} = −24 V	ク	3.4		nC
Gate to Source Charge	Qgs	V _G s = −10 V		0.6		nC
Gate to Drain Charge	Q _{GD}	I _D = -2.5 A		1.0		nC
Body Diode Forward Voltage	V _{F(S-D)}	IF = 2.5 A, VGS = 0 V		0.90		٧

TEST CIRCUIT 1 SWITCHING TIME

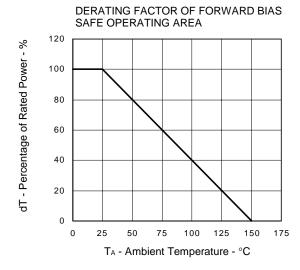


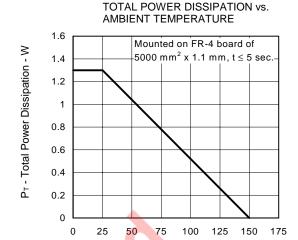


TEST CIRCUIT 2 GATE CHARGE



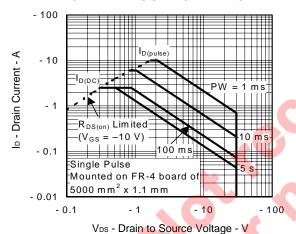
TYPICAL CHARACTERISTICS (TA = 25°C)





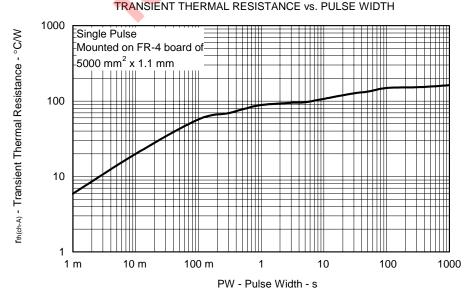
TA - Ambient Temperature - °C

FORWARD BIAS SAFE OPERATING AREA





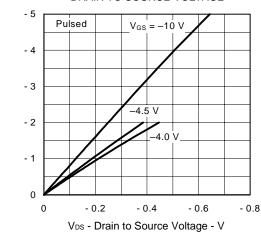
M. Heel



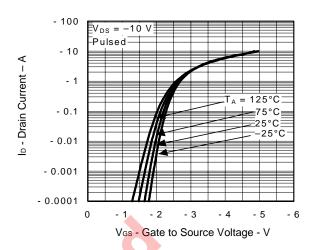
3

lo - Drain Current - A

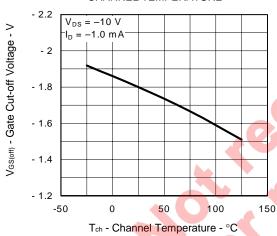
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



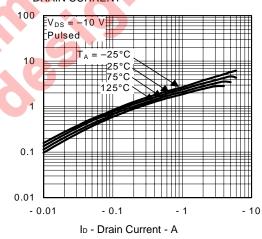
FORWARD TRANSFER CHARACTERISTICS



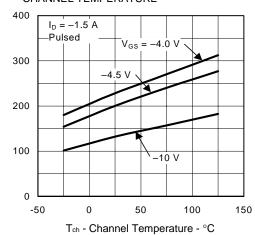
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



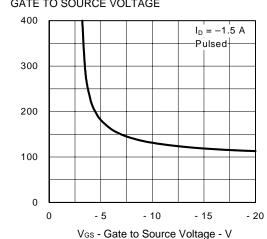
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



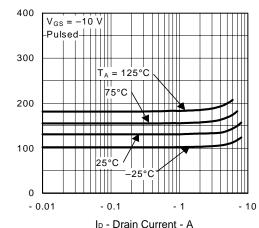
R_{DS(σ1)} - Drain to Source On-state Resistance - mΩ

R_{DS(m)} - Drain to Source On-state Resistance - mΩ

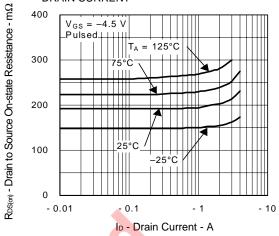
| yfs | - Forward Transfer Admittance - S

 $\mathsf{R}_{\mathsf{DS}(\mathsf{cn})}$ - Drain to Source On-state Resistance - $m\Omega$

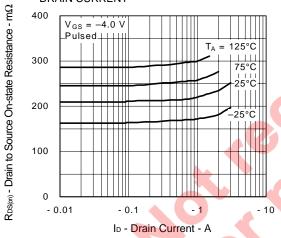
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



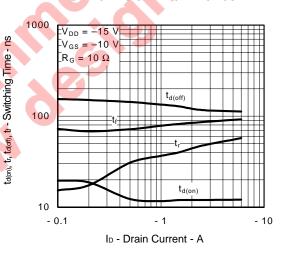
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



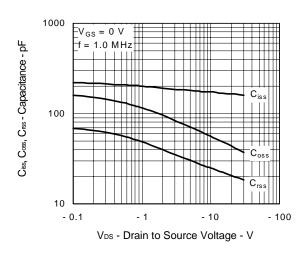
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



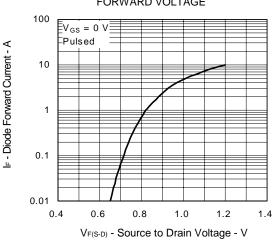
SWITCHING CHARACTERISTICS



CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



DYNAMIC INPUT/OUTPUT CHARACTERISTICS



[MEMO]



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