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# BIPOLAR ANALOG INTEGRATED CIRCUIT $\mu$ PC29S00 Series

# LOW DROPOUT VOLTAGE REGULATOR WITH ON/OFF FUNCTION

# **DESCRIPTION**

The  $\mu$  PC29S00 series is a low dropout regulator, which has 100 mA capable for the output current. This series features ON/OFF function to control output voltage.

The  $\mu$  PC29S00 series is suitable for NEC Electronics' single chip microcomputers, which have on-chip flash memory. The  $\mu$  PC29S00 series is use of erasing and writing data on its flash memory.

### **FEATURES**

- ON/OFF control function (active high)
- Output current excess of 100 mA
- ★ Surface mount device package: 8-pin plastic SOP (5.72 mm (225)) (7.8 V output, 10 V output)
  - High accuracy output voltage: ±2% (7.8 V output)

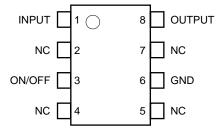
-2 to +1% (10 V output)

• On-chip all kinds of protection circuit

#### **★** ORDERING INFORMATION

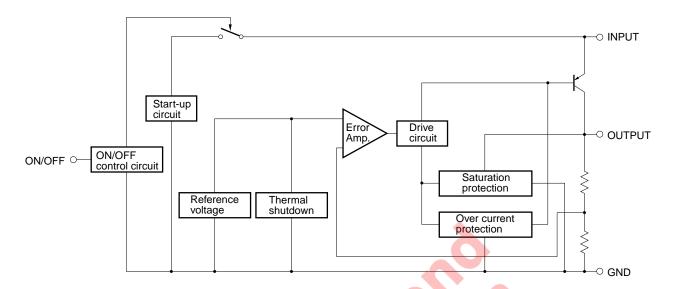
Part Number	Package	Output Voltage
μPC29S78GR	8-pin plastic SOP (5.72 mm (225))	7.8 V
μPC29S10GR	8-pin plastic SOP (5.72 mm (225))	10 V

#### PIN CONFIGURATION (Marking Side)



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#### **BLOCK DIAGRAM**



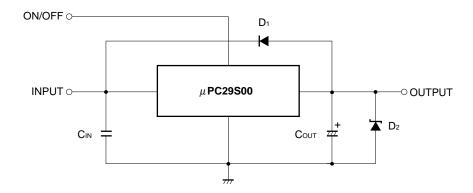
ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Input Voltage	Vin	20	V
Internal Power Dissipation	PT Note	0.48	W
Operating Ambient Temperature	TA	−30 to +85	°C
Operating Junction Temperature	Tu	-30 to +150	°C
Storage Temperature	Tstg	−55 to +150	°C
Thermal Resistance (Junction to Ambient)	Rth(J-A)	260	°C/W

Note T<sub>A</sub> ≤ 25°C, internally limited. When the operating junction temperature rises up to 150°C, the internal circuit shuts down the output voltage.

Caution Product quality may suffer if the absolute maximum rating is exceeded even momentarily for any parameter. That is, the absolute maximum ratings are rated values at which the product is on the verge of suffering physical damage, and therefore the product must be used under conditions that ensure that the absolute maximum ratings are not exceeded.

# **★ TYPICAL CONNECTION**



C<sub>IN</sub>: 0.1 to 0.47  $\mu$ F. Be sure to connect to prevent abnormal oscillation. For using capacitors, film capacitors whose voltage and temperature characteristics are excellent are recommended. Take care that some monolithic ceramic capacitor is inferior in the temperature and voltage characteristics. When using the monolithic capacitor, the C<sub>IN</sub> needs to be held these capacities in voltage and temperature used.

Cout: 10  $\mu$ F or higher. Be sure to connect to prevent oscillation and to improve the transient load stabilization. Connect the CIN and Cout as close as possible to the IC pins (within 2 cm).

D1: Need for Vo > VIN

D<sub>2</sub>: Need a shottky barrier diode for Vo < GND

Cautions 1. When output is off (VoN/OFF = low level), the OUTPUT pin should not be supplied higher voltage than Vin voltage from external.

2. Design your circuit and mounting with consideration for heat radiation when using this device.

# **RECOMMENDED OPERATING CONDITIONS**

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Input Voltage	Vin	μPC29S78	8.8		18	V
		μPC29S10	11		18	V
Output Current	lo		0		100	mA
Operating Ambient Temperature	TA		-30		+85	°C
Operating Junction Temperature	TJ		-30		+125	°C

Caution If the Absolute Maximum Rating is not exceeded, there is no problem for using recommended operating range or more. Use and evaluate the  $\mu$ PC29S00 series since the leeway is decreased with the Absolute Maximum Rating. Moreover, the recommended operating range is not prescribed to use when all parameters are maximum value.

# **ELECTRICAL CHARACTERISTICS**

μ PC29S78 (T<sub>J</sub> = 25°C, V<sub>IN</sub> = 12 V, Io = 50 mA, V<sub>ON/OFF</sub> = 5 V, unless otherwise specified)

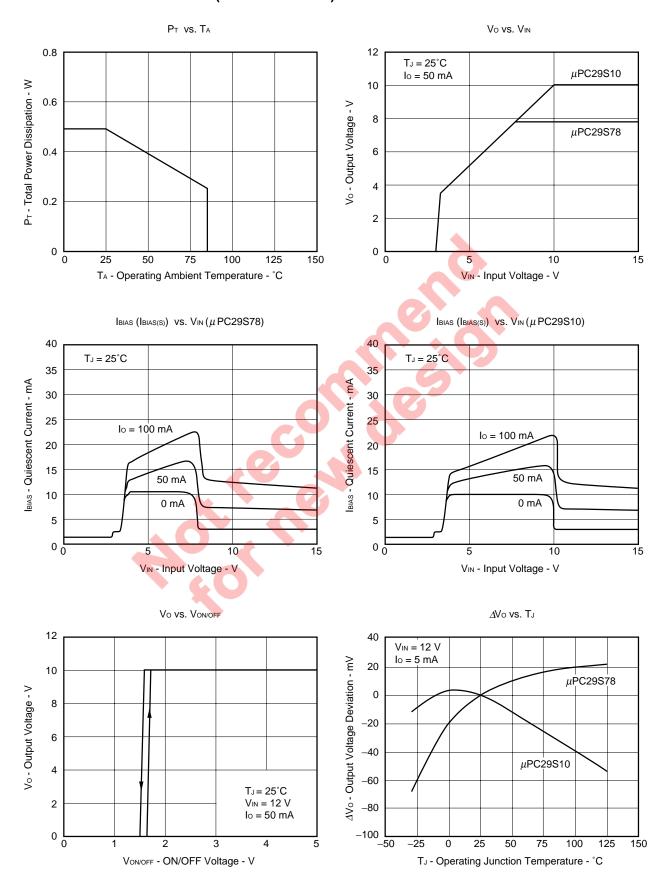
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo		7.64	7.8	7.96	V
		8.8 V ≤ V <sub>IN</sub> ≤ 18 V,	7.56		8.04	V
		0 mA ≤ lo ≤ 50 mA,				
		0°C ≤ T <sub>J</sub> ≤ +125°C				
		0 mA ≤ lo ≤ 100 mA,	7.56		8.04	V
		0°C ≤ TJ ≤ +125°C				
Line Regulation	REGIN	$8.8 \text{ V} \leq \text{V}_{IN} \leq 18 \text{ V}$		22	75	mV
Load Regulation	REGL	0 mA ≤ lo ≤ 100 mA		21	75	mV
Quiescent Current	Івіаѕ	lo = 0 mA		3.0	5.0	mA
		lo = 100 mA		11	25	mA
Startup Quiescent Current	IBIAS(S)1	V <sub>IN</sub> = 7.3 V, Io = 0 mA		10	20	mA
	IBIAS(S)2	Vin = 7.3 V, Io = 100 mA			50	mA
Quiescent Current Change	$\Delta I$ bias	$8.8 \text{ V} \leq \text{V}_{IN} \leq 18 \text{ V},$			10	mA
		0°C ≤ T <sub>J</sub> ≤ +125°C				
Output Noise Voltage	Vn	10 Hz ≤ f ≤ 100 kHz		160		μVr.m.s.
Ripple Rejection	R•R	$f = 120 \text{ Hz}, 8.8 \text{ V} \le V_{IN} \le 13.5 \text{ V}$	42	51		dB
Dropout Voltage	V <sub>DIF</sub>	Io = 100 mA, 0°C ≤ TJ ≤ +125°C			1.0	V
Peak Output Current	lOpeak	V <sub>IN</sub> = 9.8 V	150	250	400	mA
Short Circuit Current	Oshort	V <sub>IN</sub> = 18 V		250		mA
Temperature Coefficient of	ΔVο/ΔΤ	Io = 5 mA, 0°C ≤ T <sub>J</sub> ≤ +125°C		-0.4		mV/°C
Output Voltage						
ON/OFF Voltage	Von/off1	V <sub>IN</sub> = 12 V, I <sub>O</sub> = 10 mA		1.8	2.0	V
	Von/off2	V <sub>IN</sub> = 12 V, I <sub>O</sub> = 0 mA	0.8	1.6		V
ON/OFF Current	lon/off1	Von/off = 2.7 V, Io = 0 mA		250	450	μΑ
	lon/off2	Von/off = 5 V, lo = 0 mA		450	800	μΑ
Standby Current	BIASOFF	Von/off = 0 V, Io = 0 mA			10	μΑ

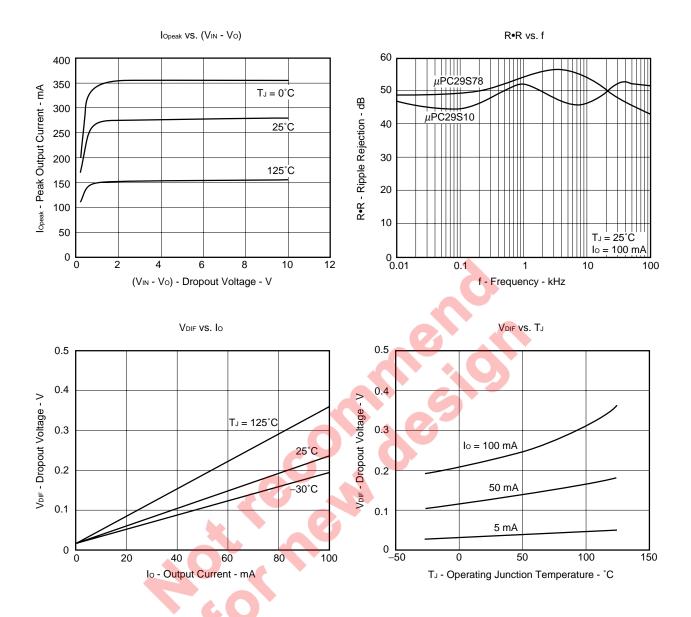
 $\mu$  PC29S10 (T<sub>J</sub> = 25°C, V<sub>IN</sub> = 12 V, Io = 50 mA, VoN/OFF = 5 V, unless otherwise specified)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	Vo		9.80	10.00	10.10	V
		11 V ≤ V <sub>IN</sub> ≤ 18 V,	9.70		10.20	V
		0 mA ≤ lo ≤ 50 mA,				
		0°C ≤ T <sub>J</sub> ≤ +125°C				
		0 mA ≤ lo ≤ 100 mA,	9.70		10.20	V
		0°C ≤ T <sub>J</sub> ≤ +125°C				
Line Regulation	REGIN	11 V ≤ V <sub>IN</sub> ≤ 18 V		27	100	mV
Load Regulation	REGL	0 mA ≤ lo ≤ 100 mA		18	100	mV
Quiescent Current	IBIAS	lo = 0 mA		3.3	5.0	mA
		lo = 100 mA		12	25	mA
Startup Quiescent Current	BIAS(S)1	V <sub>IN</sub> = 9.5 V, I <sub>O</sub> = 0 mA		10	20	mA
	BIAS(S)2	V <sub>IN</sub> = 9.5 V, I <sub>O</sub> = 100 mA			50	mA
Quiescent Current Change	$\Delta I$ BIAS	11 V ≤ V <sub>IN</sub> ≤ 18 V,		1.0	10	mA
		0°C ≤ T <sub>J</sub> ≤ +125°C		4		
Output Noise Voltage	Vn	10 Hz ≤ f ≤ 100 kHz		210		μVr.m.s.
Ripple Rejection	R•R	f = 120 Hz, 11 V ≤ V <sub>IN</sub> ≤ 13.5 V	40	48		dB
Dropout Voltage	VDIF	Io = 100 mA, 0°C ≤ T <sub>J</sub> ≤ +125°C		0.4	1.0	V
Peak Output Current	lOpeak	Vin = 12 V	150	250	400	mA
Short Circuit Current	lOshort	Vin = 18 V		180		mA
Temperature Coefficient of	ΔVο/ΔΤ	lo = 5 mA, 0°C ≤ T <sub>J</sub> ≤ +125°C		-0.5		mV/°C
Output Voltage						
ON/OFF Voltage	Von/off1	Vin = 12 V, Io = 10 mA		1.8	2.0	V
	Von/off2	V <sub>IN</sub> = 12 V, Io = 0 mA	0.8	1.6		V
ON/OFF Current	lon/off1	Von/off = 2.7 V, lo = 0 mA		250	450	μΑ
	lon/off2	Von/off = 5 V, Io = 0 mA		450	800	μΑ
Standby Current	IBIASOFF	Von/off = 0 V, Io = 0 mA			10	μΑ

Data Sheet G12905EJ5V0DS

# **TYPICAL CHARACTERISTICS (Reference Values)**

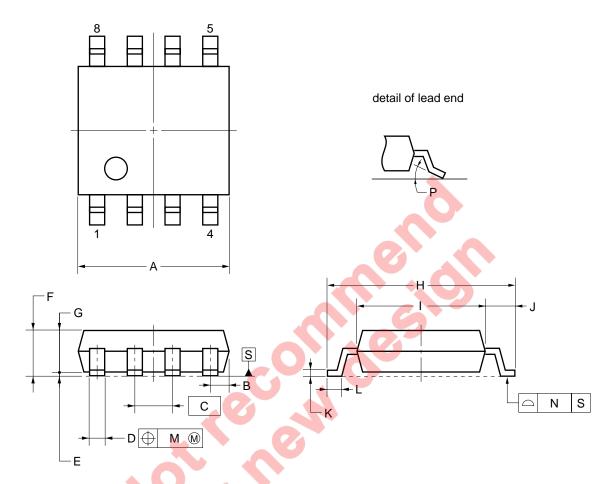




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# **★ PACKAGE DRAWINGS (Unit: mm)**

# 8-PIN PLASTIC SOP (5.72 mm (225))



#### NOTE

Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

MILLIMETERS
$5.2^{\ +0.17}_{\ -0.20}$
0.78 MAX.
1.27 (T.P.)
$0.42^{+0.08}_{-0.07}$
0.1±0.1
1.59±0.21
1.49
6.5±0.3
4.4±0.15
1.1±0.2
$0.17^{+0.08}_{-0.07}$
0.6±0.2
0.12
0.10
3°+7°

S8GM-50-225B-6



#### RECOMMENDED SOLDERING CONDITIONS

When soldering this product, it is highly recommended to observe the conditions as shown below. If other soldering processes are used, of if the soldering is performed under different conditions, please make sure to consult with our sales offices.

For more details, refer to below our document.

"Semiconductor Device Mounting Technology Manual (C10535E)"

# **Type of Surface Mount Device**

 $\mu$  PC29S78GR,  $\mu$  PC29S10GR: 8-PIN PLASTIC SOP (5.72 mm (225))

Process	Conditions	Symbol
Infrared Ray Reflow	Peak temperature: 235°C or below (Package surface temperature), Reflow time: 30 seconds or less (at 210°C or higher), Maximum number of reflow processes: 3 times.	IR35-00-3
Vapor Phase Soldering	Peak temperature: 215°C or below (Package surface temperature), Reflow time: 40 seconds or less (at 200°C or higher), Maximum number of reflow processes: 3 times.	VP15-00-3
Wave Soldering	Solder temperature: 260°C or below, Flow time: 10 seconds or less,  Maximum number of flow processes: 1 time,  Pre-heating temperature: 120°C or below (Package surface temperature).	WS60-00-1
Partial Heating Method	Pin temperature: 300°C or below, Heat time: 3 seconds or less (Per each side of the device).	-

Caution Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

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#### **CAUTION ON USE**

When using the  $\mu$  PC29S00 series at the input voltage which is lower than in the recommended operating condition the big quiescent current flows through device because the transistor of the output paragraph is saturated (Refer to the IBIAS (IBIAS(S)) vs. VIN curves in TYPICAL CHARACTERISTICS). The specification of this characteristics is the start-up quiescent current, IBIAS(S). The  $\mu$  PC29S00 series has saturation protection circuit, but they sometimes need about 50 mA current. Therefore, the power supply on the input needs the enough current capacity to pass this quiescent current when the device starts up.





#### **★** REFERENCE DOCUMENTS

Usage of Three-Terminal Regulators (G12702E)

Quality Grades on NEC Semiconductor Devices (C11531E)

Semiconductor Device Mounting Technology Manual (C10535E)

Review of Quality and Reliability Handbook (C12769E)

Semiconductor Selection Guide -Products and Packages- (X13769X)

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