

Precautions on Using Flash Development Toolkit

READ this document before using .this toolkit
KEEP the document handy for future reference.

1. The action when the FDT sends "Timeout error"

(1) When you use the FDT with the following conditions, you may get "Timeout error"

- Using low performance and small RAM PC
- Using upload function
- Using verify function after programming the flash data
- At the beginning of adjustment of baud rate

Please change baud rate to lower rate, when you get this error.

(2) When you get a message " Error No 15068: Error while reading timed response"
at the connection to a device, please change baud rate to lower rate.

2. Selection of 1200bps communication

FDT supports H8/38024F, H8/38104F and H8/38124F device with 1200bps communication speed.

Please don't select 1200bps communication speed with other devices.

3. Connection with an adapter board(Flash Development Module)

When a target device is a USB boot product (e.g. H8S/2215UF), connection with an adapter board(Flash Development Module) cannot be performed.

4. Setting of Frequency with H8S/2172F device

In the H8S/2172F device, operating frequency are doubled by the PLL circuit, but cannot choose "2" from the ratio conditions in FDT.

So, please set the value of operating frequency that was doubled by the PLL circuit, to the input frequency conditions in FDT.

5. The password of the Access Rights

Please don't forget the passwords, if the Access Rights was enabled.

6. User's Manual

The description of chapter 3.3.9 is a little different from the Help file.

Please refer to Help file if you use the function of this chapter.

7. Readback Verify

The verification error may occur when the programming is not done normally by the electrostatic damage and so on. In this case, FDT might abend.

