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Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# **Evaluation Board Information**

# EC-μPG2163T5N 2.4 GHz AND 5 GHz Dual-band SPDT SW IC Evaluation Board

- Evaluation Board Pattern Layout
- Circuit Description
- Insertion Loss Data (Including loss of the test fixture)
- Isolation Data
- Input Return Loss Data
- Output Return Loss Data
- Loss of the Test Fixture vs. Frequency Data

Document No. PG10666EJ01V0EB (1st edition)
Date Published June 2007 NS

## Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
  - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
  - 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

For the purposes of maintaining up-to-date information, the contents of this document are subject to change without notice.

This document outlines general applications for this product. The application circuits and circuit constants provided in this document are simply examples and should not be used for mass production design. Be aware also that there is no intention to standardize the restrictions and characteristics of these application circuits.

The characteristics of high-frequency devices in particular vary depending on the external components and mounting pattern used.

Customers are requested to confirm all characteristics when designing a system based in part or wholly on the information in this document.

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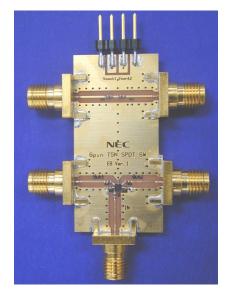
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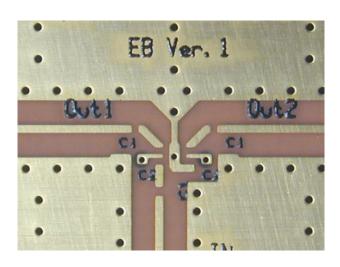
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M8E 02.11-1

# **Evaluation Board Pattern Layout**



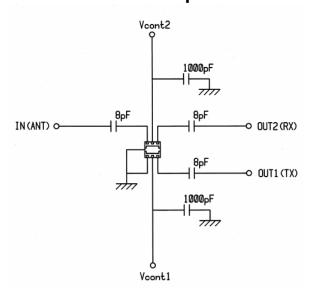


 $\underline{\text{size}}$  20 mm imes 39.5 mm

material FR4 (ELC-4756UV/Sumitomo)

t = 1 mm, four layer substrate,  $\varepsilon r = 4.6$ 

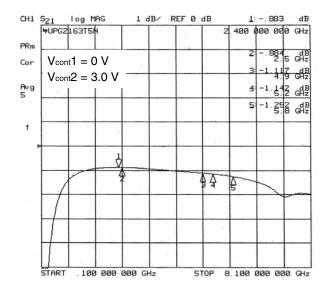
# **Circuit Description**

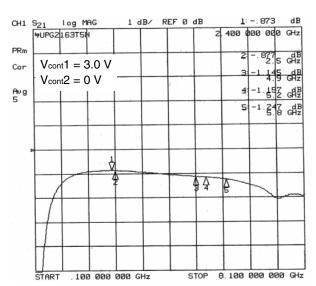


Parts	Model No.	Value	Maker	Symbol
Chip Capacitance	GRM1552C1H8R0DZ01	8 pF	Murata	C1
	GRM155B11H102KA01B	1 000 pF	Murata	C2
PC Terminal	A2-2PA-2.54DSA	_	Hirose	_
RF Connector	01K2266	_	WAKA	_
Substrate	ELC-4756UV	_	Sumitomo	_

# Insertion Loss (Including loss of the test fixture)



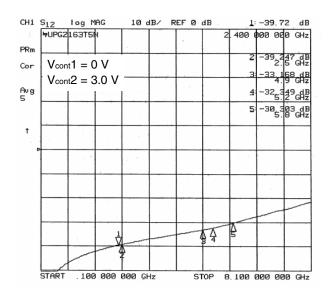


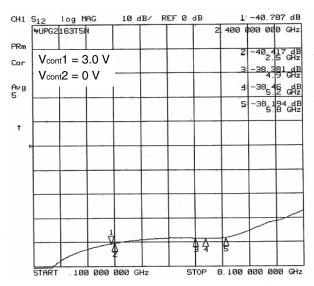


## Isolation

#### <IN-OUT1>

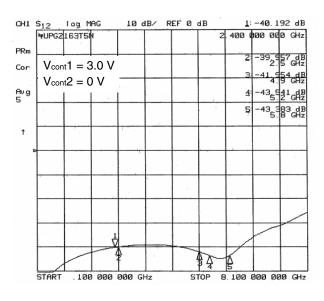
#### <IN-OUT2>

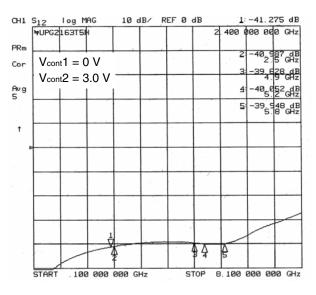




## <OUT1-OUT2>

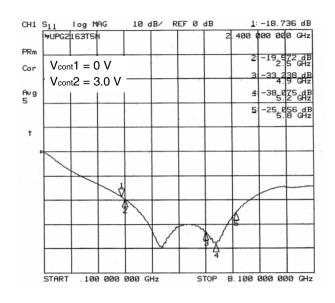
## <OUT1-OUT2>

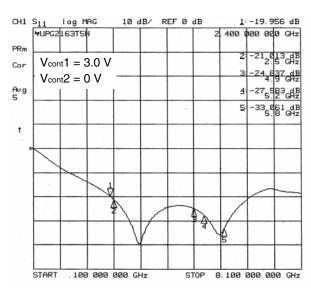




# **Input Return Loss**

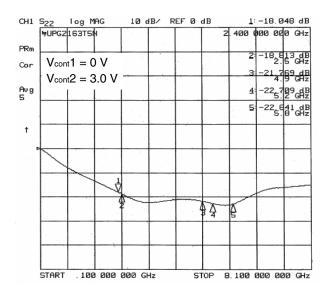


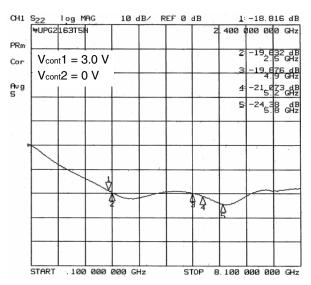




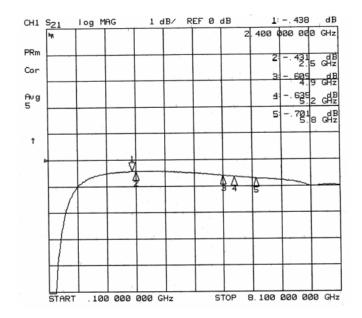
# **Output Return Loss**

<IN-OUT1> <IN-OUT2>





# Loss of the Test Fixture vs. Frequency



# For further information, please contact:

#### **NEC Electronics Corporation**

1753, Shimonumabe, Nakahara-ku, Kawasaki, Kanagawa 211-8668, Japan Tel: 044-435-5111 http://www.necel.com/

#### [America]

#### **NEC Electronics America, Inc.**

2880 Scott Blvd.
Santa Clara, CA 95050-2554, U.S.A.
Tel: 408-588-6000
800-366-9782
http://www.am.necel.com/

#### [Europe]

#### **NEC Electronics (Europe) GmbH**

Arcadiastrasse 10 40472 Düsseldorf, Germany Tel: 0211-65030 http://www.eu.necel.com/

#### Hanover Office

Podbielskistrasse 166 B 30177 Hannover Tel: 0 511 33 40 2-0

#### **Munich Office**

Werner-Eckert-Strasse 9 81829 München Tel: 0 89 92 10 03-0

#### Stuttgart Office

Industriestrasse 3 70565 Stuttgart Tel: 0 711 99 01 0-0

#### **United Kingdom Branch**

Cygnus House, Sunrise Parkway Linford Wood, Milton Keynes MK14 6NP, U.K. Tel: 01908-691-133

#### Succursale Française

9, rue Paul Dautier, B.P. 52 78142 Velizy-Villacoublay Cédex France Tel: 01-3067-5800

## Sucursal en España

Juan Esplandiu, 15 28007 Madrid, Spain Tel: 091-504-2787

## Tyskland Filial

Täby Centrum Entrance S (7th floor) 18322 Täby, Sweden Tel: 08 638 72 00

## Filiale Italiana

Via Fabio Filzi, 25/A 20124 Milano, Italy Tel: 02-667541

## Branch The Netherlands

Steijgerweg 6 5616 HS Eindhoven The Netherlands Tel: 040 265 40 10

#### [Asia & Oceania]

#### NEC Electronics (China) Co., Ltd

7th Floor, Quantum Plaza, No. 27 ZhiChunLu Haidian District, Beijing 100083, P.R.China Tel: 010-8235-1155 http://www.cn.necel.com/

#### NEC Electronics Shanghai Ltd.

Room 2511-2512, Bank of China Tower, 200 Yincheng Road Central, Pudong New Area, Shanghai P.R. China P.C:200120 Tel: 021-5888-5400 http://www.cn.necel.com/

#### **NEC Electronics Hong Kong Ltd.**

Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: 2886-9318 http://www.hk.necel.com/

#### NEC Electronics Taiwan Ltd.

7F, No. 363 Fu Shing North Road Taipei, Taiwan, R. O. C. Tel: 02-8175-9600 http://www.tw.necel.com/

## NEC Electronics Singapore Pte. Ltd.

238A Thomson Road, #12-08 Novena Square, Singapore 307684 Tel: 6253-8311 http://www.sg.necel.com/

#### **NEC Electronics Korea Ltd.**

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