

■Renesas Flash Driver and EEPROM Emulation Software for RL78 Target MCU List - General-Purpose

2023/05

R20UT5228EJ0100

Target Software: **Renesas Flash Driver RL78 Type 01 (Here after "RFD RL78 Type 01")**
 EEPROM Emulation Software RL78 Type 01 (Here after "EES RL78 Type 01")

- This list is a compilation of the target MCUs of the RL78 family RFD RL78 Type 01 and EES RL78 Type 01 for non-automotive MCUs. RFD RL78 Type 01 and EES RL78 Type 01 can be used with the target MCUs in this list. The items [R-x] ([R-1], [R-2], etc.) in the list are items used in RFD RL78 Type 01 and EES RL78 Type 01. For details, refer to the user's manual of the target software.
- RL78 automotive MCUs cannot use RFD RL78 Type 01 and EES RL78 Type 01. For the RL78 automotive MCUs, see "Renesas Flash Driver and EEPROM Emulation Software Target MCU List for RL78 - Automotive" (R20UT5229).
- RL78/x1x (RL78/G1x, and RL78/F1x, and ...etc.) products cannot use RFD RL78 Type 0x and EES RL78 Type 0x. For the target flash libraries of RL78/x1x, see the self RAM List (R20UT2944).

Target MCUs

MCU Group	Code Flash memory		User RAM		Data Flash memory		[R-1]	[R-2]	[R-3]	[R-4]	[R-5]	[R-6]	[R-7]	[R-8]	Target MCU name
	Size (bytes)	Start/End Address	Size (bytes)	Start/End Address	Size (bytes)	Start/End Address	RAM Start Address	ROM End Address 1	ROM End Address 2	Data Flash End Address	OCD_ROM	Trace_RAM	END_BLOCK	CATEGORY	
RL78/G22	32K	0x00000 - 0x07FFF	4K	0xFEF00 - 0xFFEFF	2K	0xF1000 - 0xF17FF	0xFEF00	0x07FFF	-	0xF17FF	0x7E00	0xFF300	16	01	R7F102GxC(x = 4, 6, 7, 8, A, B, C, E, F, G)
	64K	0x00000 - 0x0FFFF	4K	0xFEF00 - 0xFFEFF	2K	0xF1000 - 0xF17FF	0xFEF00	0x0FFFF	-	0xF17FF	0xFE00	0xFF300	32	01	R7F102GxE(x = 4, 6, 7, 8, A, B, C, E, F, G)
RL78/G23	96K	0x00000 - 0x17FFF	12K	0xFCF00 - 0xFFEFF	8K	0xF1000 - 0xF2FFF	0xFCF00	0x0FFFF	0x17FFF	0xF2FFF	0x17E00	0xFD300	48	01	R7F100GxF(x = A, B, C, E, F, G, J, L)
	128K	0x00000 - 0x1FFFF	16K	0xFBF00 - 0xFFEFF	8K	0xF1000 - 0xF2FFF	0xFBF00	0x0FFFF	0x1FFFF	0xF2FFF	0x1FE00	0xFC300	64	01	R7F100GxG(x = A, B, C, E, F, G, J, L, M, P)
	192K	0x00000 - 0x2FFFF	20K	0xFAF00 - 0xFFEFF	8K	0xF1000 - 0xF2FFF	0xFAF00	0x0FFFF	0x2FFFF	0xF2FFF	0x2FE00	0xFB300	96	01	R7F100GxH(x = A, B, C, E, F, G, J, L, M, P)
	256K	0x00000 - 0x3FFFF	24K	0xF9F00 - 0xFFEFF	8K	0xF1000 - 0xF2FFF	0xF9F00	0x0FFFF	0x3FFFF	0xF2FFF	0x3FE00	0xFA300	128	01	R7F100GxJ(x = A, B, C, E, F, G, J, L, M, P, S)
	384K	0x00000 - 0x5FFFF	32K	0xF7F00 - 0xFFEFF	8K	0xF1000 - 0xF2FFF	0xF7F00	0x0FFFF	0x5FFFF	0xF2FFF	0x5FE00	0xF8300	192	01	R7F100GxK(x = F, G, J, L, M, P, S)
	512K	0x00000 - 0x7FFFF	48K	0xF3F00 - 0xFFEFF	8K	0xF1000 - 0xF2FFF	0xF3F00	0x0FFFF	0x7FFFF	0xF2FFF	0x7FE00	0xF4300	256	01	R7F100GxL(x = F, G, J, L, M, P, S)
RL78/G24	768K	0x00000 - 0xBFFFF	48K	0xF3F00 - 0xFFEFF	8K	0xF1000 - 0xF2FFF	0xF3F00	0x0FFFF	0xBFFFF	0xF2FFF	0xBFE00	0xF4300	384	01	R7F100GxN(x = F, G, J, L, M, P, S)
	64K	0x00000 - 0x0FFFF	12K	0xFCF00 - 0xFFEFF	4K	0xF1000 - 0xF1FFF	0xFCF00	0x0FFFF	-	0xF1FFF	0xFE00	0xFD300	32	02	R7F101GxE(x = 6, 7, 8, A, B, E, F, G, J, L)
	128K	0x00000 - 0x1FFFF	12K	0xFCF00 - 0xFFEFF	4K	0xF1000 - 0xF1FFF	0xFCF00	0x0FFFF	0x1FFFF	0xF1FFF	0x1FE00	0xFD300	64	02	R7F101GxG(x = 6, 7, 8, A, B, E, F, G, J, L)

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