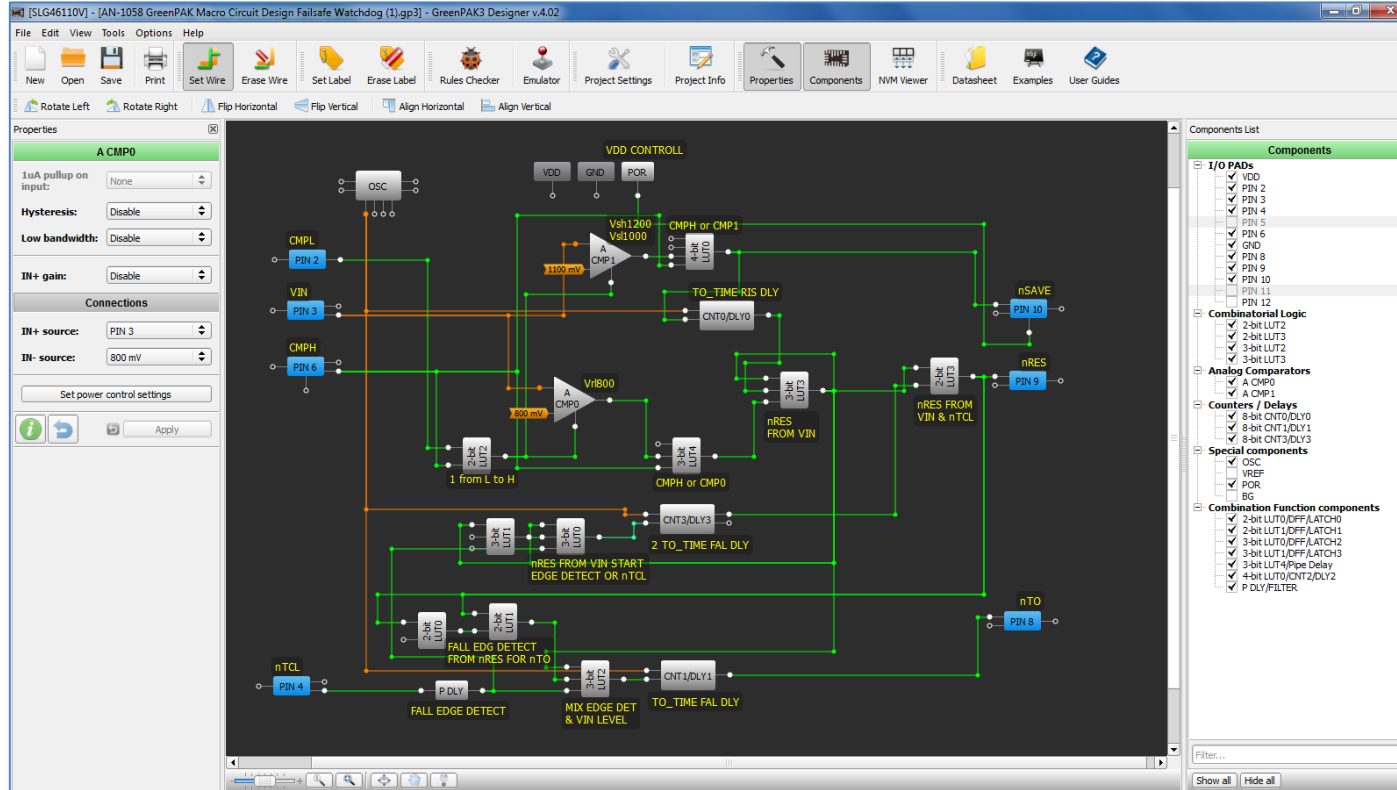


## Free Development Software

Silego Technology's GreenPAK Designer development software enables a completely graphical design process, requiring no programming language or compiler allowing a designer to configure, program, and test custom GreenPAK samples in minutes.

- Schematic capture-like design and routing
- Entire component library showing available resources for each device
- Easy component configuration
- Example projects and support documentation



## UNIVERSAL DEVELOPMENT KIT

Working in tandem with the GreenPAK Designer Emulator, Silego Technology's Universal Development Kit allows designers to

- Program custom samples in minutes
- Test GreenPAK projects in-circuit
- Develop using any GreenPAK 3 or 4 device



**GreenPAK Universal  
Development Kit**

For more information please visit [www.silego.com](http://www.silego.com)

Or email your questions to [info@silego.com](mailto:info@silego.com)

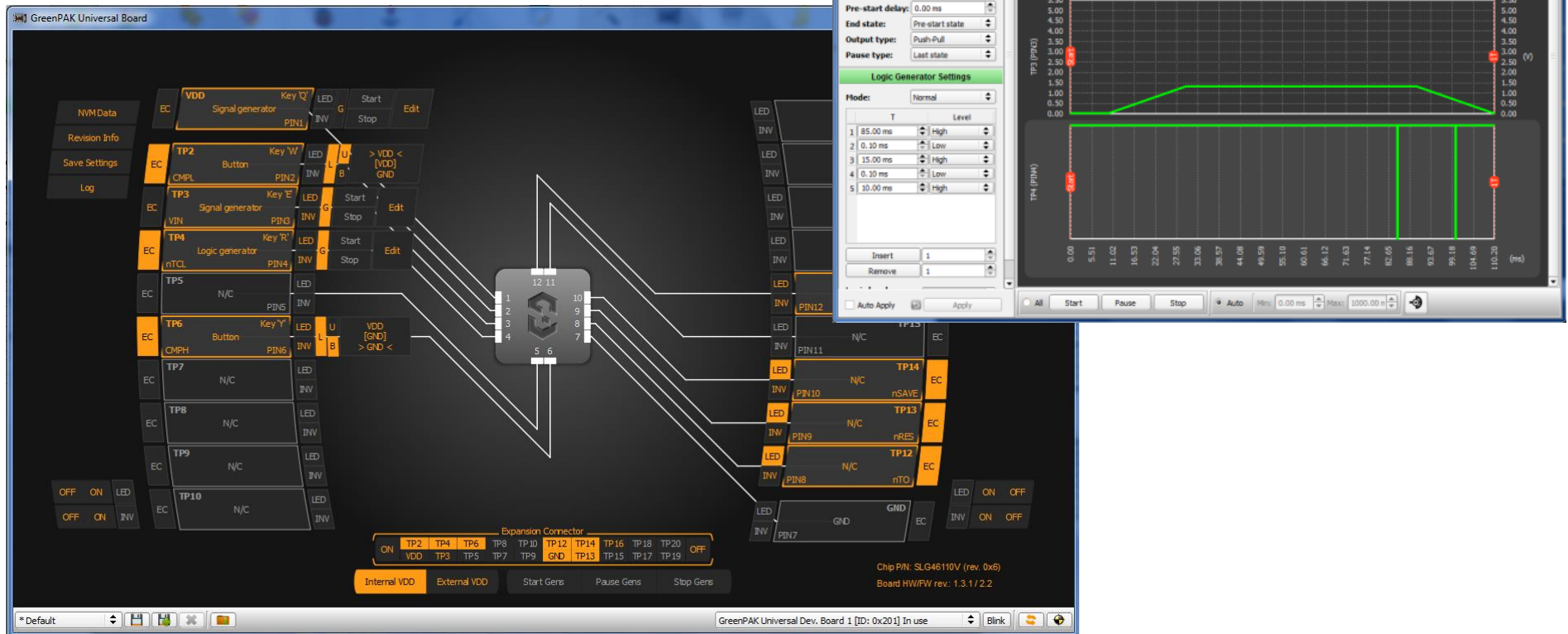
Q2 2015

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# Design Emulator

Using the included GreenPAK design emulator, designers can test their project in the development environment, no soldering required.

- In-circuit testing
- Make real time design changes
- Test and Debug Tools
  - Signal generator
  - Virtual buttons
  - LEDs



The screenshot displays the GreenPAK Designer Development Environment. The main window shows a circuit diagram of the GreenPAK Universal Board with various components like signal generators, buttons, and LEDs connected to the chip. A 'Signal Wizard' window is open, showing configuration options for a signal generator. The 'General' tab is selected, showing settings for a TP4 (PIN4) Logic 5 generator. The 'Logic Generator Settings' tab is also visible, showing a table of logic states.

T	Level
1 85.00 ms	High
2 0.30 ms	Low
3 15.00 ms	High
4 0.30 ms	Low
5 10.00 ms	High

The Signal Wizard window also shows a graph of the signal waveform, with the Y-axis labeled 'VDD (PIN4)' and the X-axis labeled '(ms)'. The graph shows a square wave signal with a period of 100ms and a duty cycle of 50%.