

**Option List** HD6432148S(W),HD6432147S(W),HD6432138S(W),HD6432137S(W)

Please check off the appropriate applications by  ,  or  within  and enter the necessary information.

(1) ROM size

<input type="checkbox"/> HD6432148S(W) : 128-kbytes
<input type="checkbox"/> HD6432147S(W) : 64-kbytes
<input type="checkbox"/> HD6432138S(W) : 128-kbytes
<input type="checkbox"/> HD6432137S(W) : 64-kbytes

(2) System Oscillator

<input type="checkbox"/> Crystal oscillator	f =	MHz
<input type="checkbox"/> External clock	f =	MHz

Date of order	
Customer	
Department	
Name	
ROM code Name	
LSI number (Renesas entry)	

(3) Power Supply Voltage/Maximum Operating Frequency

<input type="checkbox"/> Vcc = 4.5V to 5.5V ( 20MHz max.)
<input type="checkbox"/> Vcc = 4.0V to 5.5V ( 16MHz max.)

- Notes: 1.Please select the power supply voltage/operating frequency version according to the power supply voltage used.  
 Example:For use at Vcc = 4.5V to 5.5V/f = 10MHz select Vcc =4.5V to 5.5V(20MHz max.).  
 2.The power supply voltage and maximum operating frequency of the selected version should also be entered on the Single-Chip Microcomputer Ordering Specifications sheet.

(4) I<sup>2</sup>C Bus Option

<input type="checkbox"/> I <sup>2</sup> C bus used
<input type="checkbox"/> I <sup>2</sup> C bus not used

- Notes: 1. The "I<sup>2</sup>C bus used" option includes all cases where data transfer is performed via the SCL and SDA pins using the on-chip I<sup>2</sup>C bus interface function ( hardware module ). If the I<sup>2</sup>C bus interface function ( hardware module ) in used, various bus interface with different bus specifications and names are also included in "I<sup>2</sup>C bus used".  
 2. When "I<sup>2</sup>C bus not used" is selected, values cannot be set in registers relating to the I<sup>2</sup>C bus interface (ICCR,ICSR,ICDR,ICMR etc...). These register always read H'FF. With emulators, F-ZTAT versions, the "I<sup>2</sup>C bus used" option is selected. If the "I<sup>2</sup>C bus not used" option is selected, it is essential to ensure that "I<sup>2</sup>C bus interface related registers are not accessed.

For the Microcomputer Family item in 1.Basic Specifications in the Single-Chip Microcomputer Ordering Specifications Sheet \*, please specify the appropriate item from the table below according to the combination of items 1 and 4 above. If the "I<sup>2</sup>C bus used" option is selected, please also specify this in Special Specifications(Product Specifications, Mark Specifications, etc....) in 1.Basic specifications.

\* Please contact the relevant sales department for information on the Single-Chip Microcomputer Ordering Specifications Sheet.

ROM Size \ I <sup>2</sup> C	I <sup>2</sup> C bus used	I <sup>2</sup> C bus not used
128-Kbytes	HD6432148SW	HD6432148S
64-Kbytes	HD6432147SW	HD6432147S
128-Kbytes	HD6432138SW	HD6432138S
64-Kbytes	HD6432137SW	HD6432137S