

**Option List HD6433723,HD6433724,HD6433725,HD6433726**

Please check off the appropriate applications by  ,  or  within  and enter the necessary information.

Date of order	
Customer	
Department	
Name	
ROM code Name	
LSI number (Renesas entry)	

1. ROM size

<input type="checkbox"/> HD6433723	24k-word ROM
<input type="checkbox"/> HD6433724	32k-word ROM
<input type="checkbox"/> HD6433725	40k-word ROM
<input type="checkbox"/> HD6433726	48k-word ROM

2. I/O options(Check off the I/O options required and shaded options are not available)

pin name	I/O	I/O option			
		B	C	D	E
P10/IRQ0	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P11/IRQ1	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P12/IRQ2	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P13/IRQ3	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P14/IRQ4	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P15/IRQ5/TMOE	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P16/EVENT	I	<input type="checkbox"/>	<input type="checkbox"/>		
P30/FS24	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P31/FS25	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P32/FS26	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P33/FS27	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P40/FS16	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P41/FS17	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P42/FS18	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P43/FS19	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P44/FS20	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P45/FS21	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P46/FS22	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P47/FS23	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P50/FS15	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P51/FS14	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P52/FS13	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P53/FS12	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P54/FS11	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P55/FS10	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P56/FS9	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P57/FS8	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P17/Vdisp	I	Check 3.			

pin name	I/O	I/O option			
		B	C	D	E
P60/FD0/FS7	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P61/FD1/FS6	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P62/FD2/FS5	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P63/FD3/FS4	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P64/FD4/FS3	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P65/FD5/FS2	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P66/FD6/FS1	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P67/FD7/FS0	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P70/FD8	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P71/FD9	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P72/FD10	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P73/FD11	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P74/FD12	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P75/FD13	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P76/FD14	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P77/FD15	I/O			<input type="checkbox"/>	<input type="checkbox"/>
P80	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P81	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P82	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P83	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P84	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P85	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P86	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P87	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P90/PWM	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P91/SCK1	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P92/SI1	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P93/SO1	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P94/SCK2	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P95/SI2/CS	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P96/SO2	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
P97/UD	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
PA0	I/O	<input type="checkbox"/>	<input type="checkbox"/>		
PA1	I/O	<input type="checkbox"/>	<input type="checkbox"/>		

- B : With pull-up MOS
- C : Without pull-up MOS
- D : Without pull-down resistance
- E : With pull-down resistance

3. P17/Vdisp

<input type="checkbox"/> P17:without pull-down resistance
<input type="checkbox"/> Vdisp

(Note) I/O Option "E" selected at High voltage pins,  
In this case, must be selected Vdisp pin.

4. System Oscillator(OSC1,OSC2)

<input type="checkbox"/> Ceramic oscillator	f =	MHz
<input type="checkbox"/> Crystal oscillator	f =	MHz
<input type="checkbox"/> External clock	f =	MHz

5. Subsystem Oscillator (X1,X2)

<input type="checkbox"/> Used	f = 32.768 kHz
<input type="checkbox"/> Not used (X1 = Vcc)	

6. Package type

<input type="checkbox"/> FP-80A
<input type="checkbox"/> FP-80B