

Product Change Notice (PCN)

Subject: Introduction of second wafer source (GF) as a qualified supplier for the DA1469x family

Publication Date: 7/28/2022

Effective Date: 10/28/2022

Revision Description:

Final

Description of Change:

Datasheet Changes – detailed in Appendix 1

Software Development Kit (SDK) – a new SDK has been released to support both wafer sources

Final Test Program (TP) – a new Final TP has been introduced for the second source material

Affected Product List:

DA14691-00000HQ2
DA14695-00000HQ2
DA14697-00000HR2
DA14699-00000HR2

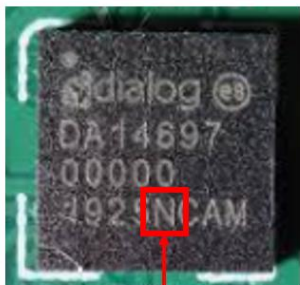
Reason for Change:

Improve reliability of supply

Impact on Fit, Form, Function, Quality & Reliability:

The form, fit and function of the device will not be impacted by the change.

Product Identification: N/A



Wafer fab identifier

N = TSMC

Q = GF

Qualification Status: Done

3 lots HTOL 1000 hrs → All PASS

3 lots Latch-Up → All PASS

3 lots ESD HBM → All PASS

3 lots ESD CDM → All PASS

Full Qualification Report available on request

Sample Availability Date: Now

Device Material Declaration: N/A

Appendix 1: Parameter Changes in Datasheet Rev3.3 to accompany the change

	Rev 3.2	Rev 3.3	
sec 5.3 table6: DC Characteristics			
parameter	typ	typ	unit
I _{BAT_HIBERN}	2	6.8	μA
I _{BAT_DP_SLP}	3	10	μA
I _{BAT_EX_SLP_16K_64K}	4.2	12.3	μA
I _{BAT_EX_SLP_16K_128K}	4.7	13.1	μA
I _{BAT_EX_SLP_16K_256K}	5.4	14.9	μA
I _{BAT_EX_SLP_16K_384K}	6.3	16.7	μA
I _{BAT_EX_SLP_16K_512K}	7.7	18.4	μA
I _{BAT_Gearbox}	41	74	μA
I _{BAT_BLE_RX_32M}	4.5	4.9	mA
I _{BAT_BLE_TX_32M}	5.6	5.8	mA
I _{BAT_BLE_RX_96M}	6.5	7	mA
I _{BAT_BLE_TX_96M}	7.6	7.8	mA
sec 5.7 table 10: BG_REF - DC Characteristics			
parameter	max	max	unit
V _{ACC_TRIM}	1	1.2	%
sec 5.9 table 13: GP_ADC - DC Characteristics			
parameter	min/max	min/max	unit
E _{G_CALIBRATED}	-0.8 / +0.8	-1 / +1	%
	min	min	unit
E _{OFS_CALIBRATED}	-4	-7	LSB
sec 5.11 table 18: DCDC - DC Characteristics			
parameter	min/max	min/max	unit
V _{O_1V2_0}	0.875/0.925	0.85/0.95	V
V _{O_1V2_1}	0.975/1.025	0.95/1.05	V
V _{O_1V2_2}	1.075/1.125	1.05/1.25	V
V _{O_1V2_3}	1.175/1.225	1.15/1.25	V
sec 5.12 table 20: LDO_1v8 - DC Characteristics			
parameter	min/max	min/max	unit
REG _{LINE}	-0.5/+0.5	-0.8/+0.8	%/V
sec 5.12 table 21: LDO_1v8 - AC Characteristics			
parameter	min	min	unit
PSRR _{LDO_1v8}	40	30	dB
sec 5.12 table 32: LDO_CORE_RET - DC Characteristics			
parameter	min/max	min/max	unit
V _{LDO_CORE_RET_0v75}	0.70/0.80	0.68/0.84	V
V _{LDO_CORE_RET_0v8}	0.75/0.85	0.72/0.89	V
V _{LDO_CORE_RET_0v85}	0.80/0.90	0.77/0.94	V
V _{LDO_CORE_RET_0v9}	0.84/0.96	0.82/0.99	V
sec 5.15 table 43: OSC_RCX - Timing Characteristics			
parameter	max	max	unit
f _{RCX}	17	18	kHz

For additional information regarding this notice, please contact your Renesas sales representative.