

## Product Change Notice (PCN)

**Subject:** Electrical Specification Change to Standard Microcircuit Drawing 5962-98613 for Intersil Products HS\*-139RH\* and HS\*-139EH\*

**Publication Date:** 2/3/2017

**Effective Date:** 5/5/2017

**Revision Description:**

Initial Release

**Description of Change:**

This notice is to inform you of changes to the electrical specifications in DLA (Defense Logistics Agency) SMD (Standard Microcircuit Drawing) 5962-98613 for the listed HSx-139RH and HSx-139EH products. The VIO limit @ 25°C is changing from 2mV to 3mV in table IA (electrical performance characteristics) and the VIO delta limit from 1mV to 1.5mV under table IIB (delta parameters) in the SMD.

**Reason for Change:**

The change aligns the SMD with the product characteristics and is necessary to maintain product manufacturability in support of customer delivery requirements. Details regarding the change are contained on the following page. The updated SMD is available on the DLA web site at: <https://landandmaritimeapps.dla.mil/programs/smcr/default.aspx>

**Product Identification:**

There have been no changes to the die/silicon or product itself. There will be no change in the external marking of the packaged parts.

**Qualification status:** Not applicable

**Sample availability:** 2/3/2017

**Device material declaration:** Available upon request

*Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.*

For additional information regarding this notice, please contact your regional change coordinator (below)			
Americas: <a href="mailto:PCN-US@INTERSIL.COM">PCN-US@INTERSIL.COM</a>	Europe: <a href="mailto:PCN-EU@INTERSIL.COM">PCN-EU@INTERSIL.COM</a>	Japan: <a href="mailto:PCN-JP@INTERSIL.COM">PCN-JP@INTERSIL.COM</a>	Asia Pac: <a href="mailto:PCN-APAC@INTERSIL.COM">PCN-APAC@INTERSIL.COM</a>

Appendix A – Affected Products List

Appendix B – SMD changes

## Appendix A - Affected Products List

Standard microcircuit drawing	Intersil Part Number	Standard microcircuit drawing	Intersil Part Number
5962F9861303V9A	HS0-139EH-Q	5962F9861301VCC	HS1-139RH-Q
	HS0-139RH/SAMPLE	5962F9861303VXC	HS9-139EH-Q
5962F9861301V9A	HS0-139RH-Q		HS9-139RH/PROTO
5962F9861303VCC	HS1-139EH-Q	5962F9861301QXC	HS9-139RH-8
	HS1-139RH/PROTO	5962F9861301VXC	HS9-139RH-Q
5962F9861301QCC	HS1-139RH-8		

## Appendix B – SMD changes

**From:**

Test	Symbol	Conditions <sup>1/2/</sup> -55°C ≤ T <sub>A</sub> ≤ +125°C +V = 5 V unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Input offset voltage	V <sub>IO</sub>	V <sub>REF</sub> = 1.4 V, R <sub>S</sub> = 0 Ω, output switch point = 1.4 V, +V = 5 V, +V = 30 V	1	01,03	-2	2	mV
				02	-3	3	
			2,3	All	-5	5	
			M,D,P,L,R,F	1	-8	8	

**To:**

Test	Symbol	Conditions <sup>1/2/</sup> -55°C ≤ T <sub>A</sub> ≤ +125°C +V = 5 V unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Input offset voltage	V <sub>IO</sub>	V <sub>REF</sub> = 1.4 V, R <sub>S</sub> = 0 Ω, output switch point = 1.4 V, +V = 5 V, +V = 30 V	1	01,03	-3	3	mV
				02	-3	3	
			2,3	All	-5	5	
			M,D,P,L,R,F	1	-8	8	

**From:**

 TABLE IIB. Burn-in and life test delta parameters. (T<sub>A</sub> = +25°C). <sup>1/</sup>

Parameters	Symbol	Min	Max	Units
Input offset voltage (+V = 5 V)	V <sub>IO</sub>	-1	+1	mV
Input bias current (+V = 5 V)	±I <sub>B</sub>	-15	+15	nA
Input offset current (+V = 5 V)	I <sub>IO</sub>	-10	+10	nA

**To:**

 TABLE IIB. Burn-in and life test delta parameters. (T<sub>A</sub> = +25°C). <sup>1/</sup>

Parameters	Symbol	Min	Max	Units
Input offset voltage (+V = 5 V)	V <sub>IO</sub>	-1.5	+1.5	mV