Integrated Device Technology

VersaClock® 6 Programmable Clock Family

CLOCKS AND TIMING | INTERFACE AND CONNECTIVITY | MEMORY AND LOGIC | POWER MANAGEMENT | RF PRODUCTS

PERFORMANCE

- < 500 fs RMS phase jitter
- PCIe® Gen 1/2/3/(4)
- 1G and 10G Ethernet
- USB 3.0/Thunderbolt™/RapidIO®

FLEXIBILITY

- 2 to 4 configurable outputs
- 4 OTP configurations
- Fractional Output Divider (FOD) architecture
- Any frequency 1 to 350 MHz
- Instant customization

POWER / SIZE

- 30 mA core current consumption
- 4 x 4 mm 24-VFQFN packages

EASE OF USE

- Online tools
- Timing Commander™ Software to configure, program, and monitor sophisticated timing devices
- 2 weeks to custom factory programmed samples

With its winning combination of low phase jitter and power consumption, the VersaClock 6 device is ideal for such markets as high-end consumer, networking, computing, industrial and communications.

The VersaClock 6 programmable clock generator family delivers flexible, low-power timing for demanding high-performance applications. With RMS phase jitter of less than 500 femtoseconds (fsec), the VersaClock 6 products deliver the best performance to date for IDT's award-winning VersaClock family, offering an exceptional combination of jitter performance, flexibility, and low operating power.

Part Number	Functionality	Output Type
5P49V6901	PLL with 4 FOD	4 configurable output pairs + reference output
5P49V6913	PLL with 2 FOD	2 configurable output pairs + reference output
5P49V6914	PLL with 3 FOD	3 configurable output pairs + reference output

To request samples, download documentation, or learn more, visit:
idt.com/go/versaclock6

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