

August 27, 2014

Product Specifications of the RH850/C1H and RH850/C1M MCUs

	RH850/C1H	RH850/C1M
Product #	R7F701260EABG (BGA252, 240 MHz)	R7F701263EAFP (QFP144, 240 MHz)
CPU	240 MHz (with LSDC) + 240 MHz	240 MHz (with LSDC)
Code flash	2 MB × 2	2 MB
Data flash (E2PROM)	32 KB	
RAM	Local RAM 64 KB × 2 + Global RAM 112 KB	Local RAM 64 KB + Global RAM 64 KB
DMA, DTS	16 (DMAC)/128 (DTS)	
MPU area	12 area	
Timers	Timer array unit D (TAUD): 2 units (16 timers/unit) Timer array unit J (TAUJ): 1 unit (4 timers/unit) Motor control timers (TSG3): 2 units Timer option (TAPA): 4 units Encoder timer (ENCA): 2 units	
	Timer pattern buffer (TPBA): 2 units Timers for operating system (OSTM): 3 units Watchdog timer (WDTA): 2 units	Timer pattern buffer (TPBA): 1 unit Timers for operating system (OSTM): 2 units Watchdog timer (WDTA): 1 unit
Motor control	Enhanced motor control unit (EMU2): 1 unit * 2 motor control is possible per unit	
	Resolver/Digital Converter (RDC2): 2 units	Resolver/Digital Converter (RDC2): 1 unit

		RH850/C1H	RH850/C1M
Serial Interfaces		Clocked serial interface H: 2 channels CAN interface: 4 channels Serial communication interface: 3 channels	
		LIN interface: 3 ch	–
ADC	SAR AD	16 + 21 ch	10 ch + 15 ch
Power supply voltage		5/3.3/1.25 V	
		5/3.3V by using external pass transistor	
Other functions		Safety function (Error control module, CRC, MISR), Security function (ICUSB)	
Package		252-pin P-BGA	144-pin P-LQFP
		(17 mm × 17 mm)	(20 mm × 20 mm)

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