

Separate Sheet

Product Specifications of the RH850/V1R-M MCU

| Group | RH850/V1R-M |
|---------------|--|
| CPU | 320MHz G3MH (1046 DMIPS) × 2 |
| DSP | 320 MHz x 1 |
| Code Flash | 2 MB |
| Data Flash | 64 kB |
| RAM | 2 MB |
| DMA | 16 (DMAC) / 128 (DTS) |
| Timer | OS Timer (OSTM): 16 unit Watchdog Timer (WDTA): 3 unit Timer Array Unit D (TAUD): 2 unit (16 ch/unit) Timer Array Unit J (TAUJ): 2 unit (4 ch/unit) |
| Communication | CAN Interface (RS-CANFD) : 6 units Flex Ray Controller (FLX) : 1 unit Ethernet Controller (ETNA) : 1 unit Clocked Serial Interface H (CSIH) : 4 units High Speed Serial Peripheral Interface (HS-SPI) : 4 units LIN/UART Interface (RLIN3) : 1 unit MIPI-CS12 (MCSIA) : 3 units (4 lanes / unit) |
| 12-bit ADC | A/D Converter (ADCF) : 2 units (10 ch / unit) |

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| Group | RH850/V1R-M |
| Operation Voltage | 3.3 V / 1.25 V |
| Other feature | Power on clear, Error control module, Data CRC, Core voltage monitor, Temperature sensor, Security module (ICU-SE) |
| Debug | Debug Control : JTAG, LPD Trace Interface: Aurora 2 lanes |
| Package | 292-pin BGA (17 mm × 17 mm) |

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