

May 24, 2011

Product Specifications of the V850ES/JG3-L MCUs

Product name		V850ES/JG3-L			
		μPD70F3841	μPD70F3842	μPD70F3843	μPD70F3844
Internal memory	Flash memory (KB)	768	1024	768	1024
	RAM (KB)	80	80	80	80
Clocks	Main clock	2.5 to 20 MHz			
	Sub clock	32.768 kHz			
	Internal oscillator	220 kHz (for watchdog timer)			
	Minimum instruction execution time	50 ns (20 MHz operation)			
I/O ports		83 (5 V tolerant: 31)		80 (5 V tolerant: 28)	
Timers	16-bit timer	8 ch			
	Real-time counter	1 ch			
	Watchdog timer	1 ch			
10-bit A/D converter		12 ch			
8-bit D/A converter		2 ch			
Serial interfaces		CSI: 3 ch CSI/UART: 1 ch CSI/I2C bus: 1 ch UART/I2C bus: 2 ch UART: 3 ch UART (9-bit supported): 1 ch			
USB controller		—		USB 2.0 function (full-speed): 1 ch	
DMA controller		4 ch			

Interrupt sources	External	9
	Internal	55
Power save functions	HALT/IDLE1/IDLE2/STOP/subclock/sub-IDLE/low-voltage STOP/ low-voltage subclock/low-voltage sub-IDLE/RTC backup mode	
Other functions	Low-voltage detector, real-time output function, key interrupt, CRC, clock monitor	
On-chip debugging	MINICUBE, MINICUBE2 supported	
Operating power supply voltage	VDD = 2.0 to 3.6 V	
Operating ambient temperature	TA = -40 to +85°C	
Packages	100-pin-LQFP (14 x 14 mm) 121-pin-FBGA (8 x 8 mm)	