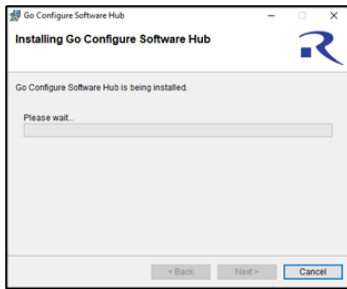## SLG51000C - EVB

SLG51000C Power GreenPAK Evaluation Board R1.1
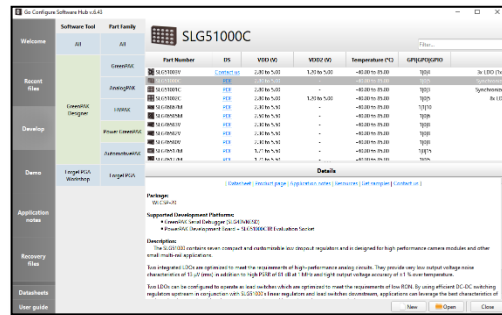
1. Install Go Configure™ Software Hub

Download and install the latest Go Configure™ Software Hub software from https://www.renesas.com/software-tool/go-configure-software-hub.
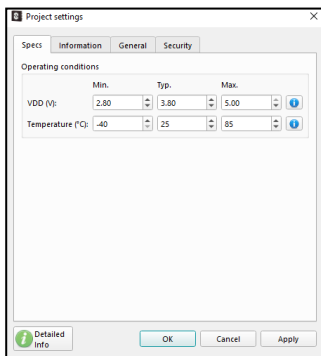

2. Create a New Project

Start Go Configure™ Software Hub and select the SLG51000C device from the Power GreenPAK™ section.
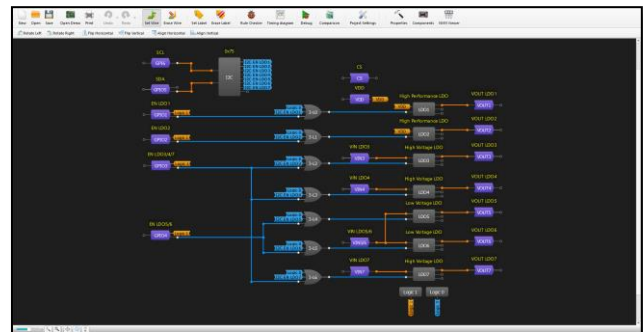

3. Set Project Settings

Set desired voltages and other settings according to your project.
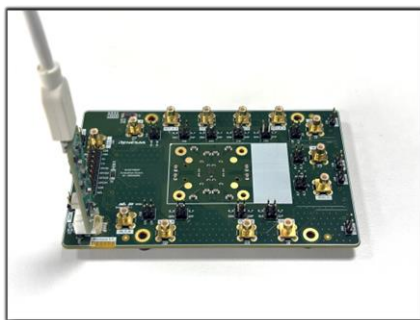

4. Develop the Project

You are now ready to start developing using all available components including Power Sequencer, LDOs, LUTs, DFFs, DLYs, and much more.


5. Prepare Development Board

Using external power supplies, apply 3.8 V to V_{DD} and 1.8 V to CS pins. Insert the GreenPAK Serial Debugger (GSD) into the Evaluation Board for chip emulation.

6. Emulate the Chip

Click the toolbar icon [Debug] to start the Power GreenPAK™ Emulation Tool. Choose the GreenPAK Serial Debugger from the Development Platform Selector window. Press the Emulation button to load the project code onto the chip. At this point the chip will operate according to the project code. Press the Sync button to upload project changes to the chip. Emulation can be performed multiple times, even on programmed chips. Press the Emulation button again to exit the emulation mode.
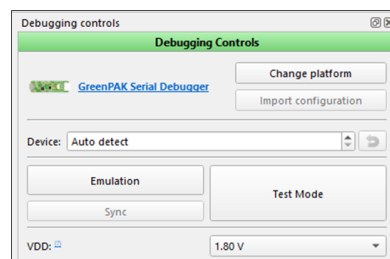
IMPORTANT NOTICE AND DISCLAIMER

RENESAS ELECTRONICS CORPORATION AND ITS SUBSIDIARIES ("RENESAS") PROVIDES TECHNICAL SPECIFICATIONS AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS OR IMPLIED, INCLUDING, WITHOUT LIMITATION, ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT OF THIRD-PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for developers who are designing with Renesas products. You are solely responsible for (1) selecting the appropriate products for your application, (2) designing, validating, and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. Renesas grants you permission to use these resources only to develop an application that uses Renesas products. Other reproduction or use of these resources is strictly prohibited. No license is granted to any other Renesas intellectual property or to any third-party intellectual property. Renesas disclaims responsibility for, and you will fully indemnify Renesas and its representatives against, any claims, damages, costs, losses, or liabilities arising from your use of these resources. Renesas' products are provided only subject to Renesas' Terms and Conditions of Sale or other applicable terms agreed to in writing. No use of any Renesas resources expands or otherwise alters any applicable warranties or warranty disclaimers for these products.

(Disclaimer Rev.1.01)

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.

Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit www.renesas.com/contact-us/.