

SLG51000C - EVB

SLG51000C Power GreenPAK Evaluation Board R1.1

1. Install Go Configure™ Software Hub

Download and install the latest Go Configure™ Software Hub software from <u>https://www.renesas.com/software-tool/go-configure-software-hub</u>.

Go Configure Software Hub		×
Go Configure Software Hub is being installed. Please wait		1
< Back	Next > Cancel	

3. Set Project Settings

Set desired voltages and other settings according to your project.

pecs	Informat	ion	General	Security			
perati	ng conditio	ns					
		Min.		Typ.	Max.		
VDD (v):	2.80	-	3.80	\$ 5.00	ŵ	0
Tempe	rature (°C):	-40	\$	25	\$ 85	\$	0

5. Prepare Development Board

Using external power supplies, apply 3.8 V to V_{DD} and 1.8 V to CS pins. Insert the GreenPAK Serial Debugger (GSD) into the Evaluation Board for chip emulation.



2. Create a New Project

Start Go Configure™ Software Hub and select the SLG51000C device from the Power GreenPAK™ section.

	Software Tool	Part Family			-				
	40	м	SLC	51000	C			Fite.	
		Granitia	Fart Number	DS	V80 (M)	N002 (V)	Temperature CO	eulesoleuo	
		Greenway	3 SLG51001V	Contect as	2,80 to 5,00	1,20 to 5,00	-8120 to 7520	104	3/10015
Recent			SIGNICOC	HCE.					
		AnalogPVK	\$1.651001C	PER	2,80 to 5,00		-82.20 to 85.20	1993	Synchroniz
			R 51.651002C	PER	2,80 to 5,00	1,30 to 5,00	-40.00 to 85.00	1925	B(U
	GreenRisk	LINKS	AN SLOUNDAR	PER	230 to 5.90		-40.00 15 85.00	1(1)10	
	Designer		SLORNSM	100	2.50 to 5.90		-80.00 to 85.00	1908	
Develop			SLORNSV	100	2.30 to 5.30		-9220 15 2520	19051	
Ownerop		Fower Greeking	SLGRM2V	PER	230 to 530		-40.00 to 85.00	19081	
			SIGNNEY	808	7.80 to 5.90		-9010 to 8510	19091	
		Automotive Mag	SIGNSTM.	858	3.71 to 5.90		-4010 to 7510	10019	
		MI STORES		1.716-5.91		4110144 IN IS	1010		
	Lorget PGA	LonetRich	Details						
	Workshop			Datasta	et (Freduct page) /	(galantian notes) Re	names Get samples Gr	(au trate	
opilcation			Packages W.CSH-M Supported Developme						
			 CorroRACSectal 	behugger (SLG40)	NESE) (STROCTE Evoluation	on Seclet			
			Description: The SLGS1033 contain small multi-reil applicat		nd ordeninshie les	r dropout regulation o	ind is designed for high p	eformance camera mod	ules and other
			Too integrated LDOs are characteristics of 13 pV	optimized to meeting in addition to	the requirements of high PSR of St cB	l high-padormanca a at 1 MPir and tight os	nalog circuits. They provi ripat voltage accuracy of	de very lese eutput veibeg ±1 % ever berependsze.	e noise
							t the requirements of low on deventeers, applicat		

4. Develop the Project

You are now ready to start developing using all available components including Power Sequencer, LDOs, LUTs, DFFs, DLYs, and much more.

		KR COLOR	High Rufurmana (20)	VOUT 12001 VOUT 12002 VOUT 12002 VOUT 12003	
e not tiittes		VED COLOR		VCUT LDO2	
a vice				VCUT LDO2	
a vice				NOUT LOO2	
			1001		
	(10) (a)	- (MRI)	1000	VSID	
		VN DO4	High Voltage LDD	VOUT LOOP	
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6. Emulate the Chip

Click the toolbar icon to start the Power GreenPAK™ Emulation Tool. Choose the GreenPAK Serial Debugger from the Development Platform Selector window. Press the Emulation button to load the project code onto the chip. At this point the chip will operate according to the project code. Press the Sync button to upload project changes to the chip. Emulation can be performed multiple times, even on programmed chips. Press the Emulation button again to exit the emulation mode.

Debugging controls	0 🗙				
Debuggin	g Controls				
GreenPAK Serial Debugge	Change platform				
Greenrak Senar Debugge	Import configuration				
Device: Auto detect	•				
Emulation	Test Mode				
Sync					
VDD: @	1.80 V 👻				



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