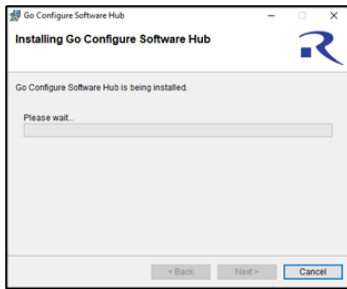


SLG51000C - EVB

SLG51000C Power GreenPAK Evaluation Board R1.1

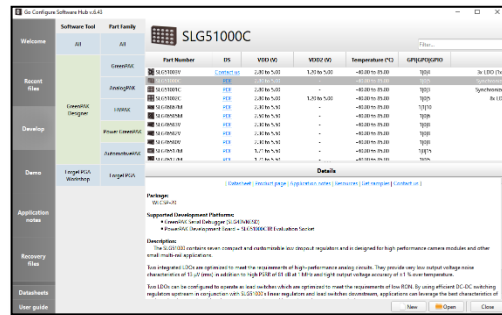
1. Install Go Configure™ Software Hub

Download and install the latest Go Configure™ Software Hub software from <https://www.renesas.com/software-tool/go-configure-software-hub>.



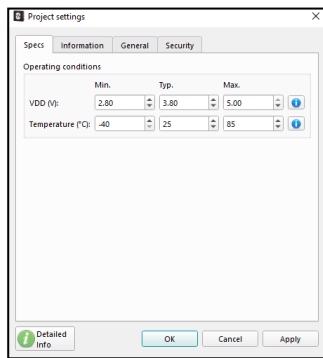
2. Create a New Project

Start Go Configure™ Software Hub and select the SLG51000C device from the Power GreenPAK™ section.



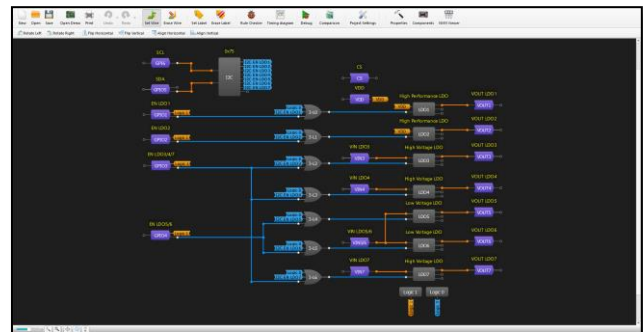
3. Set Project Settings

Set desired voltages and other settings according to your project.



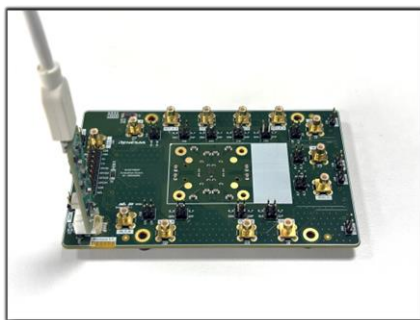
4. Develop the Project

You are now ready to start developing using all available components including Power Sequencer, LDOs, LUTs, DFFs, DLYs, and much more.




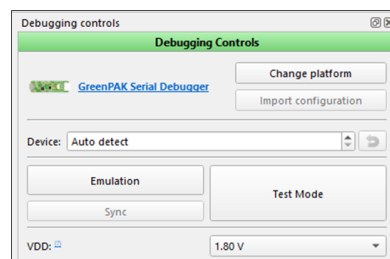
5. Prepare Development Board

Using external power supplies, apply 3.8 V to V_{DD} and 1.8 V to CS pins. Insert the GreenPAK Serial Debugger (GSD) into the Evaluation Board for chip emulation.



6. Emulate the Chip

Click the toolbar icon  to start the Power GreenPAK™ Emulation Tool. Choose the GreenPAK Serial Debugger from the Development Platform Selector window. Press the Emulation button to load the project code onto the chip. At this point the chip will operate according to the project code. Press the Sync button to upload project changes to the chip. Emulation can be performed multiple times, even on programmed chips. Press the Emulation button again to exit the emulation mode.



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