

## AMD ZCU102 with Renesas ClockMatrix, ITU-T G.8261

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# 1. Results Summary

Standard	Test Case	Results
G.8261	Noise Generation	Pass
G.8261	Holdover	Pass
G.8261	Test Case 12 (SyncE Assist)	Pass
G.8261	Test Case 13 Network Traffic Model 2 (SyncE Assist)	Pass
G.8261	Test Case 14 Network Traffic Model 2 (SyncE Assist)	Pass
G.8261	Test Case 15 Network Traffic Model 2 (SyncE Assist)	Pass
G.8261	Test Case 16 Network Traffic Model 2 (SyncE Assist)	Pass
G.8261	Test Case 17 (10µs) Network Traffic Model 2 (SyncE Assist)	Pass
G.8261	Test Case 17 (200µs) Network Traffic Model 2 (SyncE Assist)	Pass
G.8261	Test Case 12	Pass
G.8261	Test Case 13 Network Traffic Model 2	Pass
G.8261	Test Case 14 Network Traffic Model 2	Pass
G.8261	Test Case 15 Network Traffic Model 2	Pass
G.8261	Test Case 16 Network Traffic Model 2	Pass
G.8261	Test Case 17 (10µs) Network Traffic Model 2	Pass
G.8261	Test Case 17 (200µs) Network Traffic Model 2	Pass
G.8261	Test Case12 Network Traffic Model 2 (SinglePath)	Pass
G.8261	Test Case13 Network Traffic Model 2 (SinglePath)	<b>Fail</b> (expected)
G.8261	Test Case14 Network Traffic Model 2 (SinglePath)	<b>Fail</b> (expected)
G.8261	Test Case15 Network Traffic Model 2 (SinglePath)	Pass
G.8261	Test Case16 Network Traffic Model 2 (SinglePath)	Pass
G.8261	Test Case17b (10µs) Network Traffic Model 2 (SinglePath)	Fail (expected)
G.8261	Test Case17b (200µs) Network Traffic Model 2 (SinglePath)	<b>Fail</b> (expected)

## 1.1 Notes on Testing with SyncE Assistance

SyncE from the Calnex measurement equipment (Paragon Neo and Paragon X) is used as the physical layer assistance for each test in this report. The quality level of the SyncE level is manually set greater than or equal to the quality level threshold in the PCM4L Json configuration file.

```
"physicalPllClockCategory": 1,
"physicalPllClockCategoryThreshold": 1,
```

The SyncE recovered clock is an input to a DPLL channel running in DPLL Mode with the G.8262 EEC1 preset. The output from this DPLL is filtered (3mHz filter bandwidth) and used as a combo source for the PTP DPLL.

## 1.2 Notes on Testing with a Physical Layer Clock

FTS and PTS with SyncE Assistance test both use a SyncE clock from the test equipment as an additional clock source. In both cases, a DPLL (configured for ITU-T G.8262 EEC1) is locked to the SyncE source and is connected to the PTP DPLL via the combo bus. For FTS there is no filter on the combo bus connection. For PTS with SyncE Assistance there is a 3mHz filter on the combo bus connection.

Synced software is used to manage the SyncE clock based on the QL level from the test equipment for tests required a physical layer clock. A category 1 (QL-PRTC) SyncE source from the test equipment is connected to the SyncE input on Clock Matrix and is qualified before PTP (PCM4L/PTP4I) is started.

## 1.3 Notes on Single Path Operation

When operating in single path mode, only the Sync packets from the master are used for timing. This results in an unknown floor delay. A floor delay is manually entered by the user in the PCM4L Json file:

```
"floorDelayEstimateSeconds": 0.000008800,
```

The test description contains the floor delay estimate used in each case. The floor delay estimate changes depending on the PDV pattern being tested as well as the inherent delay in the measurement device.

## 2. Test Configuration

Table 1. Test Configuration 1

Device Under Test	AMD + CM
Oscillator	Rakon M6141 TCXO
1pps Source	Symmetricom TP5000
Instrument	Paragon Neo
Instrument Serial Number	36081
Ethernet Interface	Optical
CAT Version	27.0.21154.2116 [S]
Sections using this Configuration	3, 4, 13, 19, 25

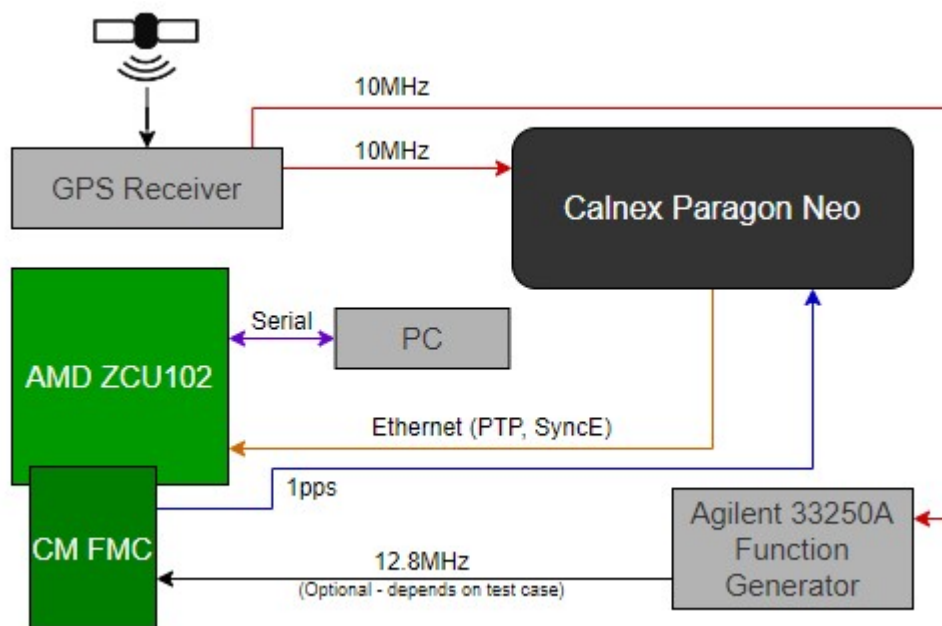


Figure 1. Equipment Configuration 1

Table 2. Test Configuration 2

Device Under Test	AMD + CM
Oscillator	Rakon M6141 TCXO
1pps Source	Symmetricom TP5000
Instrument	Paragon X
Instrument Serial Number	25060
Ethernet Interface	Optical
CAT Version	27.0.21154.2116 [S]
Sections using this Configuration	5–11, 14, 20–24

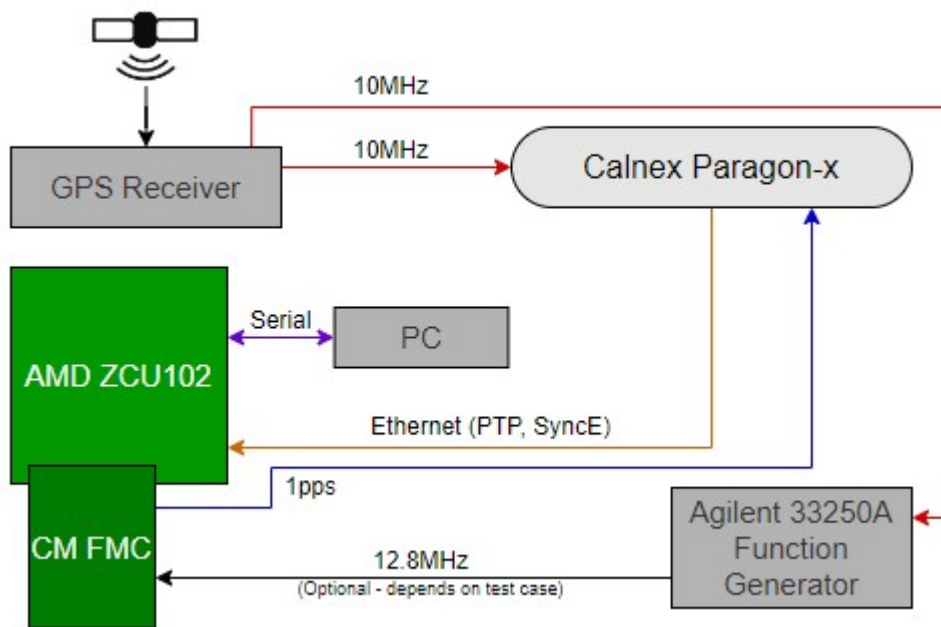


Figure 2. Equipment Configuration 2

Table 3. Test Configuration 3

Device Under Test	AMD + CM
1pps Source	Symmetricom TP5000
Software Version	4.2.1-rc7
Instrument	Paragon X
Instrument Serial Number	25060
Ethernet Interface	Optical
CAT Version	28.10.22111.2025 [S] (C)
Sections using this Configuration	12, 15

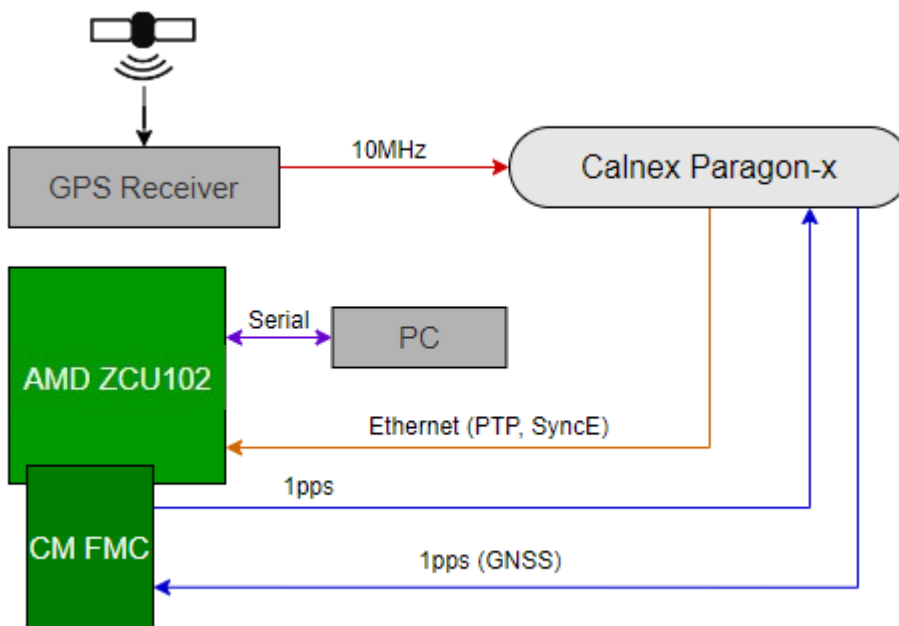


Figure 3. Equipment Configuration 3



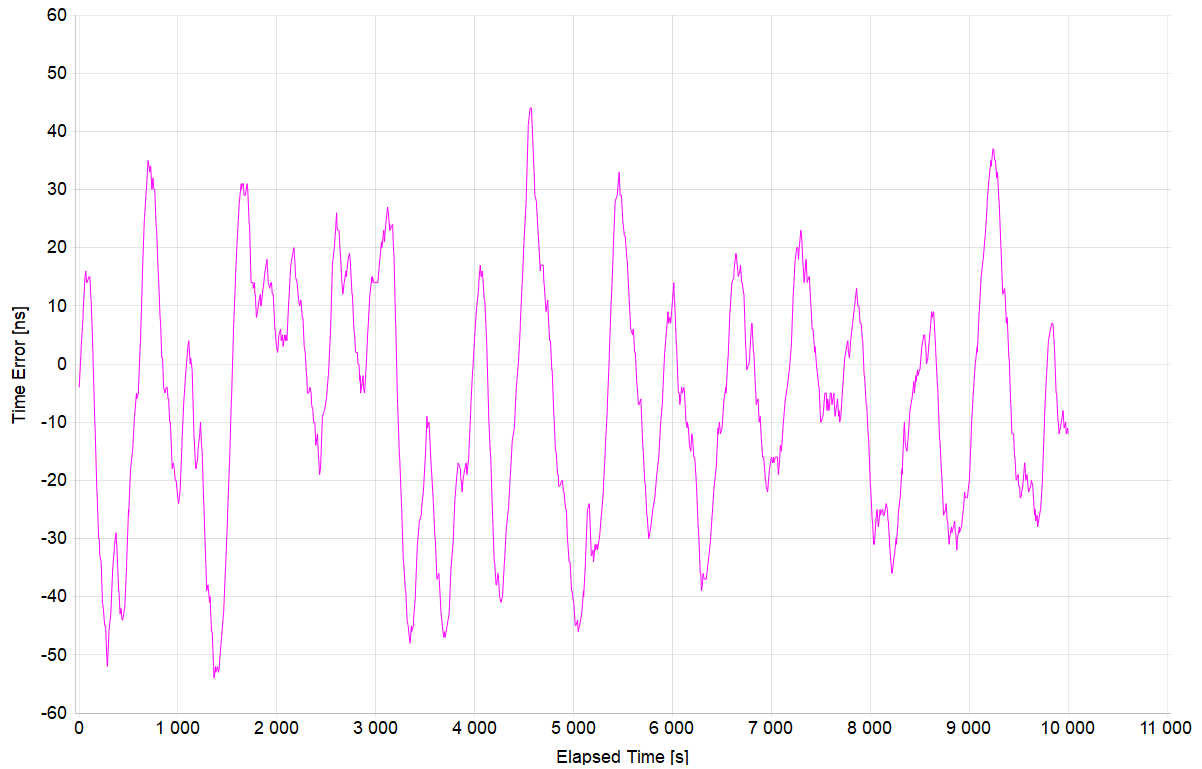
### 3. G.8261: Noise Generation

<b>Test Description</b>	Noise Generation
<b>Report Date</b>	22-04-27_15-55-48
<b>Packet Rate (pkt/s)</b>	16
<b>Beginning of Test</b>	2022-04-05 8:50:09 PM
<b>Test Duration</b>	02:46:40
<b>Test Configuration</b>	1
<b>Time to Phase Lock (s)</b>	61

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

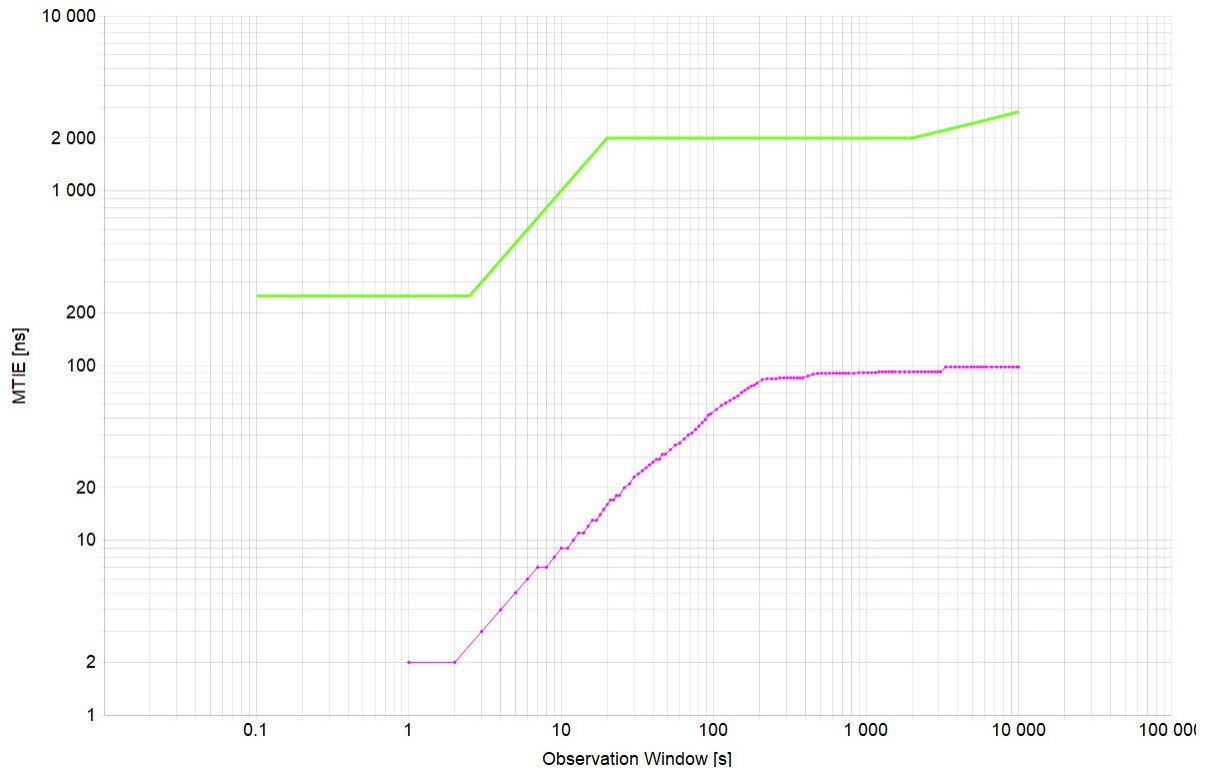
### 3.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	On
<b>Zero Offset</b>	-4ns



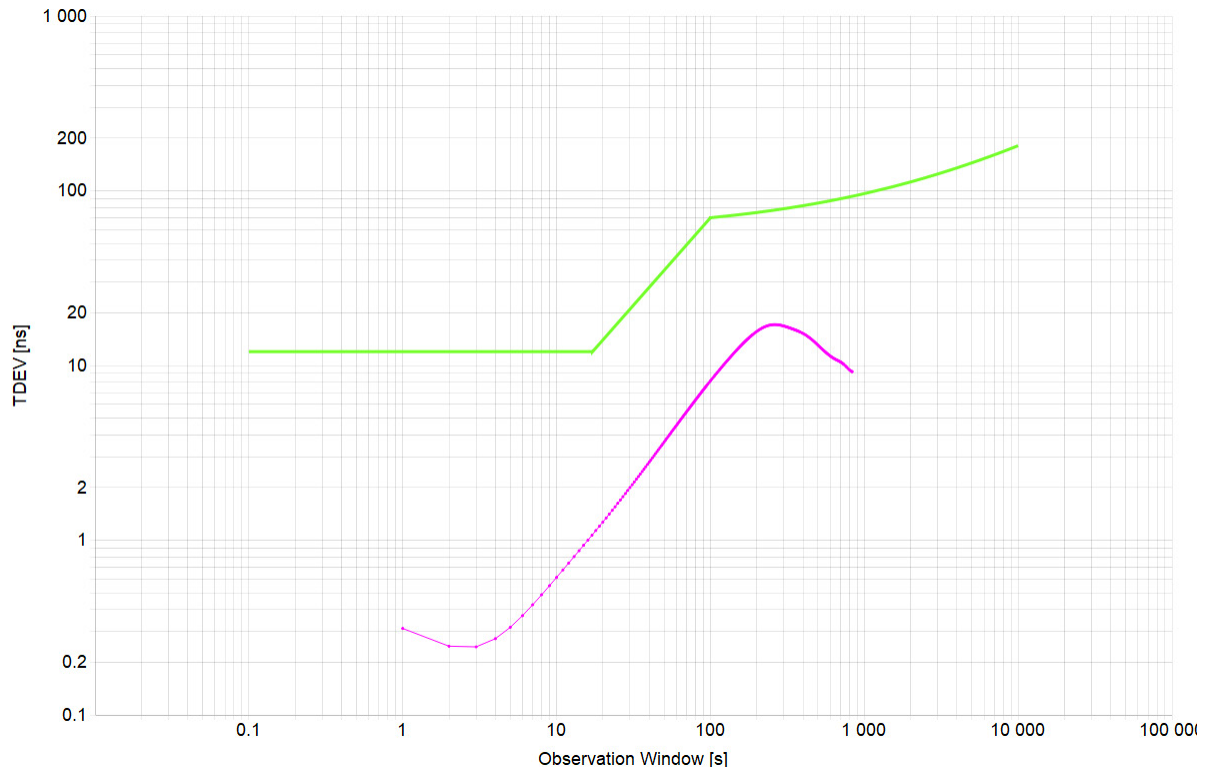
<b>Mean [ns]</b>	-6.92
<b>Min [ns]</b>	-54
<b>Max [ns]</b>	44
<b>Max-Min [ns]</b>	98

### 3.2 MTIE Analysis



<b>Min [ns]</b>	2
<b>Max [ns]</b>	98
<b>Max-Min [ns]</b>	96

### 3.3 TDEV Analysis



<b>Min [ns]</b>	0.245
<b>Max [ns]</b>	17.127
<b>Max-Min [ns]</b>	16.882

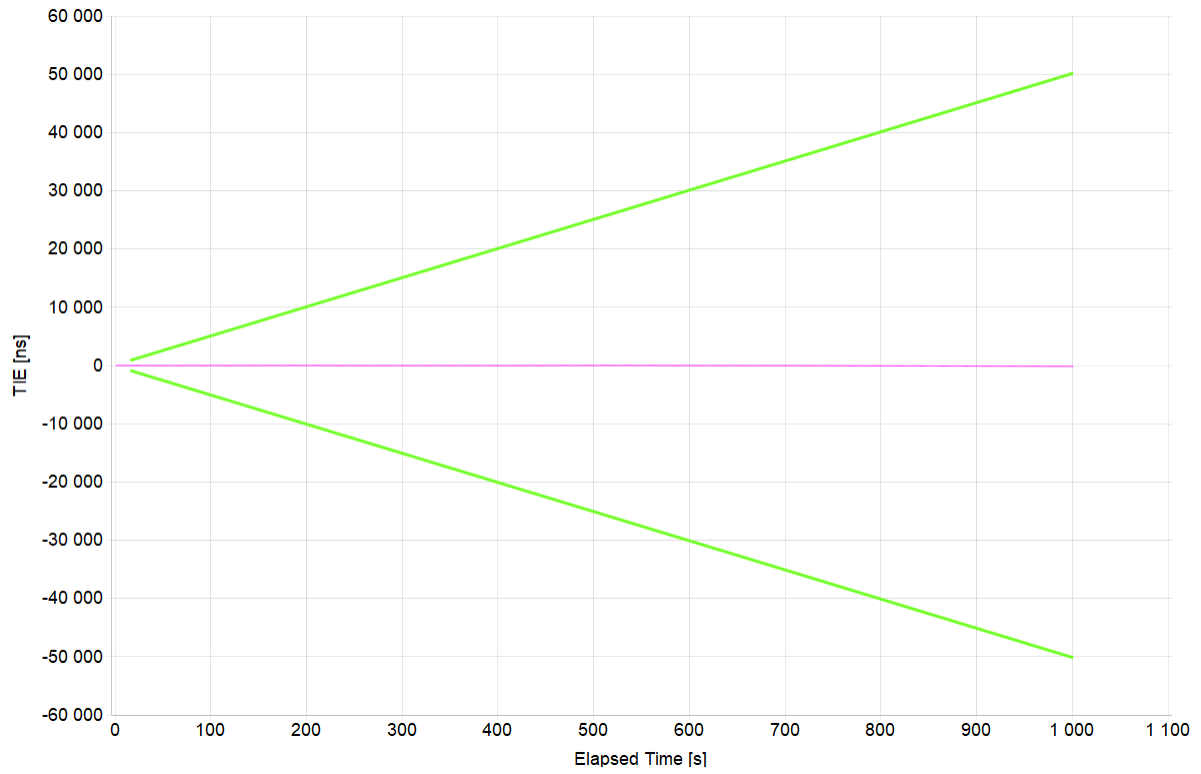
## 4. G.8261: Holdover

<b>Test Description</b>	Holdover
<b>Report Date</b>	22-04-27_15-55-48
<b>Packet Rate (pkt/s)</b>	16
<b>Beginning of Test</b>	2022-04-05 8:50:09 PM
<b>Test Duration</b>	00:16:40
<b>Test Configuration</b>	1
<b>Time to Phase Lock (s)</b>	N/A

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask TIE</b>	G.8262 EEC Opt. 1 Long-Term Holdover Const. Temp.
<b>Mask TIE Result</b>	<b>Pass</b>

1. This test is a continuation of the previous Noise Generation test. This allows for an appropriate amount of settling time before collecting holdover data (10 000s). The results are split because holdover requires a different mask than noise generation.

## 4.1 TIE Analysis



<b>Mean [ns]</b>	-36.272
<b>Min [ns]</b>	-155
<b>Max [ns]</b>	2
<b>Max-Min [ns]</b>	157

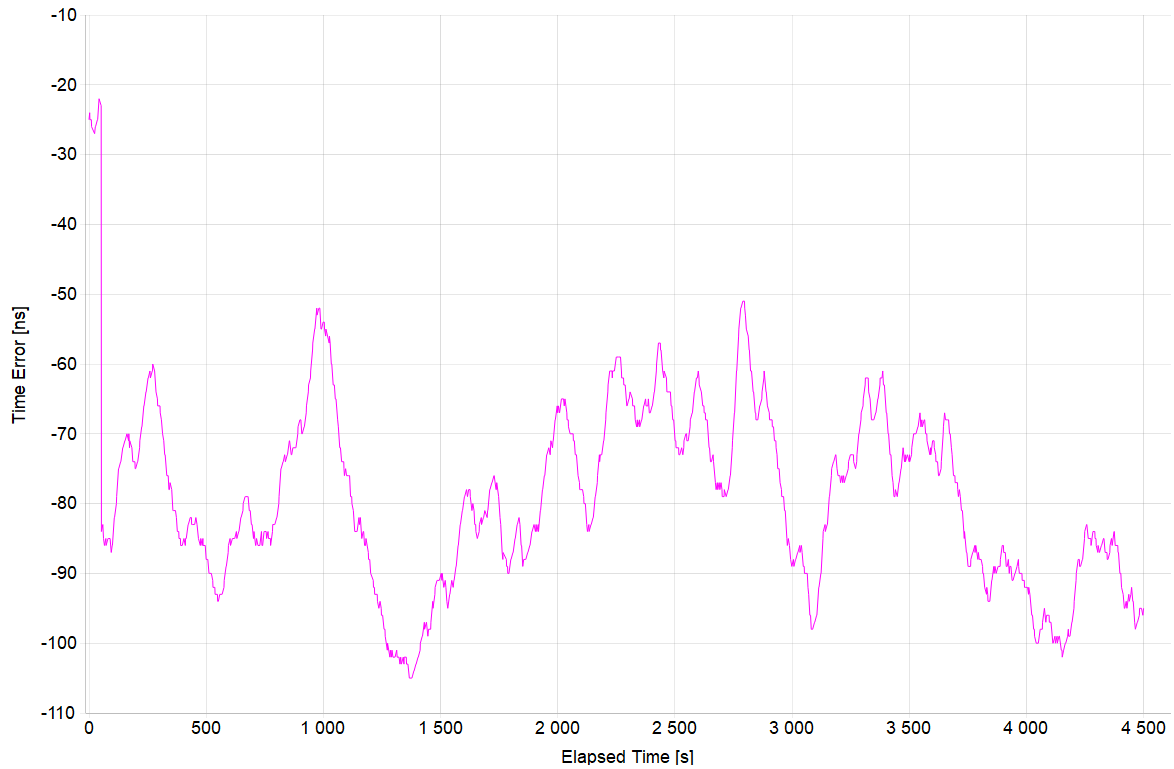
## 5. G.8261: Test Case 12 (SyncE Assist)

<b>Test Description</b>	Test Case 12
<b>Report Date</b>	22-04-27_17-35-10
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:15:00
<b>Time to Phase Lock (s)</b>	378

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

### 5.1 ONEPPS Analysis

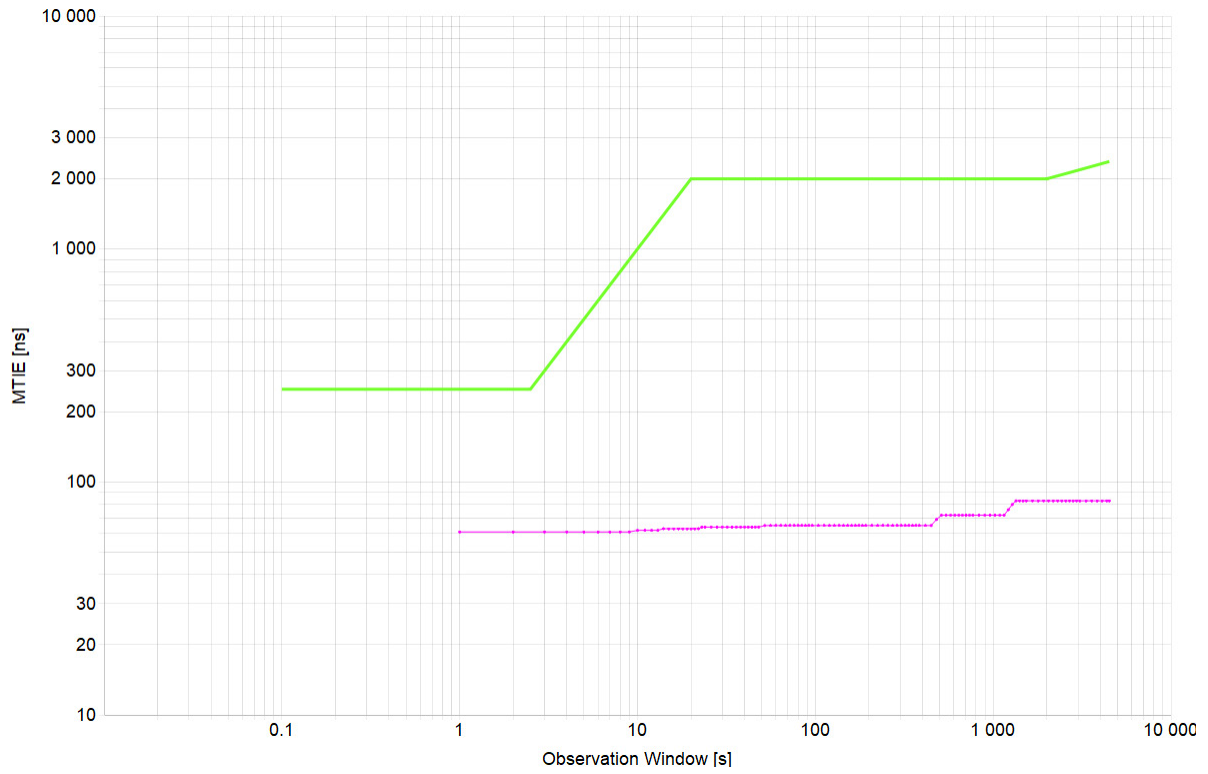
<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	-25ns



<b>Mean [ns]</b>	-78.845
<b>Min [ns]</b>	-105
<b>Max [ns]</b>	-22
<b>Max-Min [ns]</b>	83

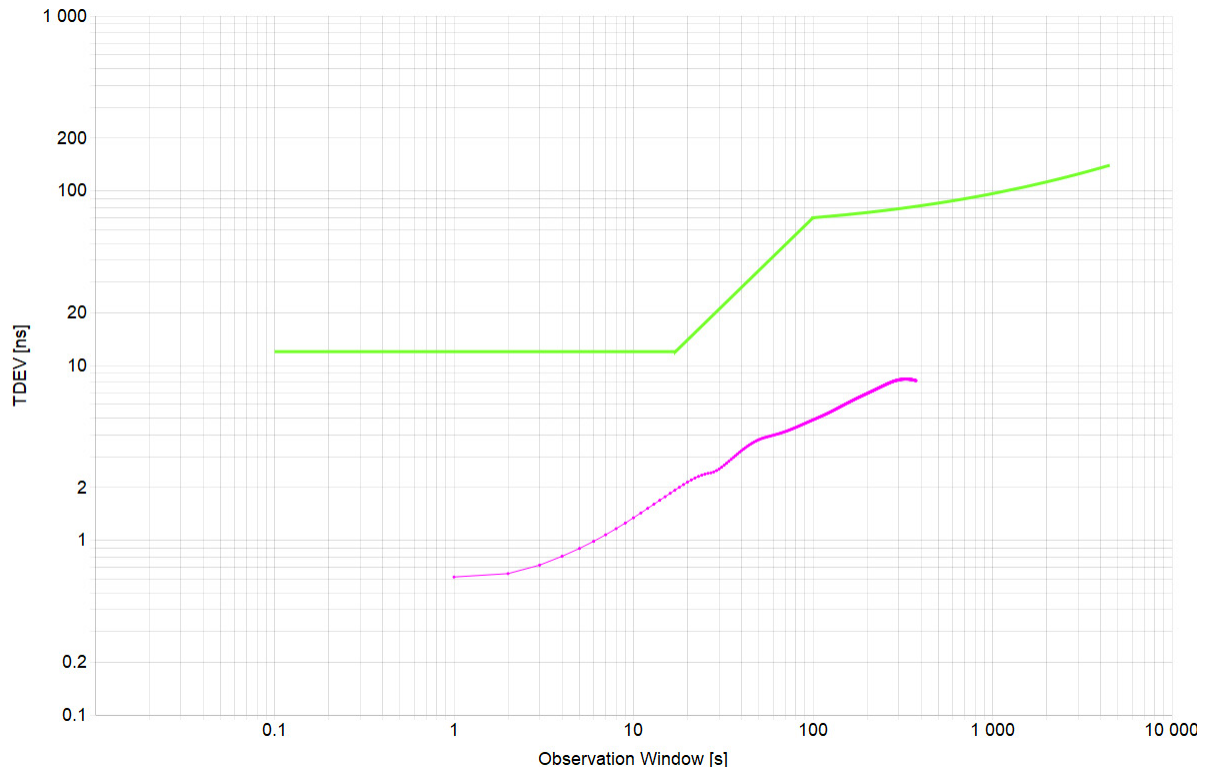


## 5.2 MTIE Analysis



<b>Min [ns]</b>	61
<b>Max [ns]</b>	83
<b>Max-Min [ns]</b>	22

### 5.3 TDEV Analysis



<b>Min [ns]</b>	0.616
<b>Max [ns]</b>	8.357
<b>Max-Min [ns]</b>	7.741

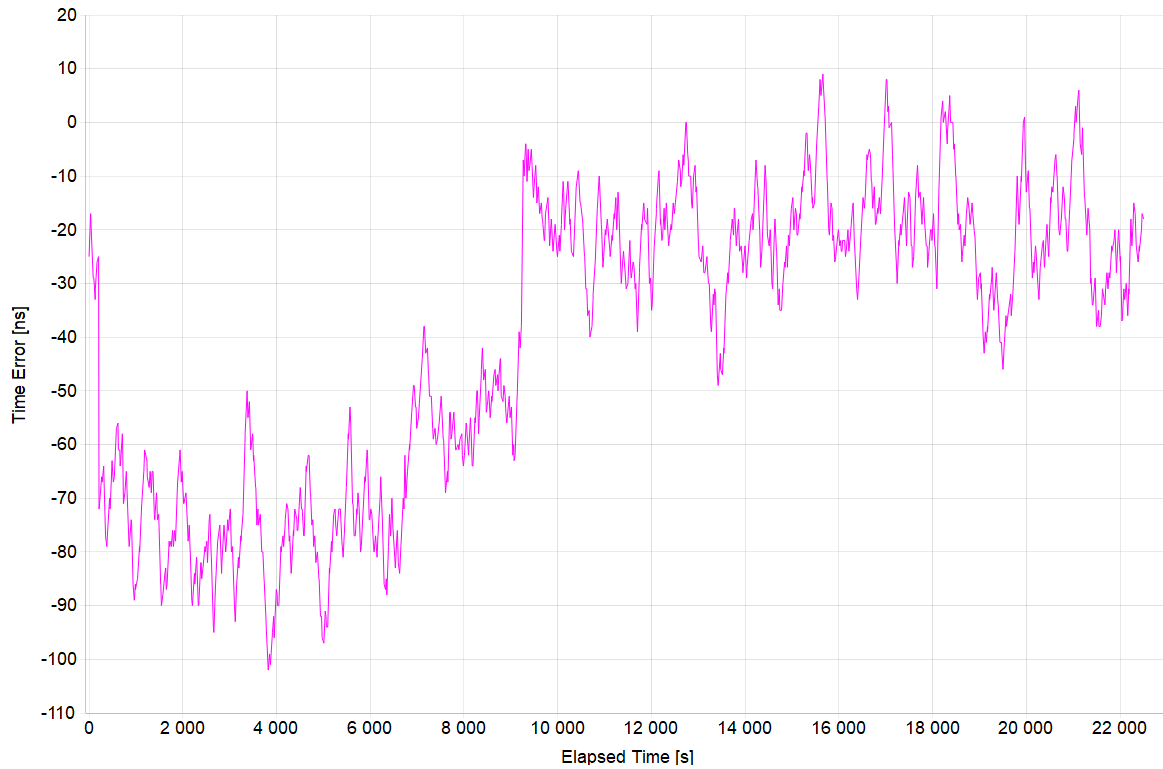
## 6. G.8261: Test Case 13b (SyncE Assist)

<b>Test Description</b>	Test Case 13b
<b>Report Date</b>	22-04-27_17-35-10
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	06:15:00
<b>Time to Phase Lock (s)</b>	377

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

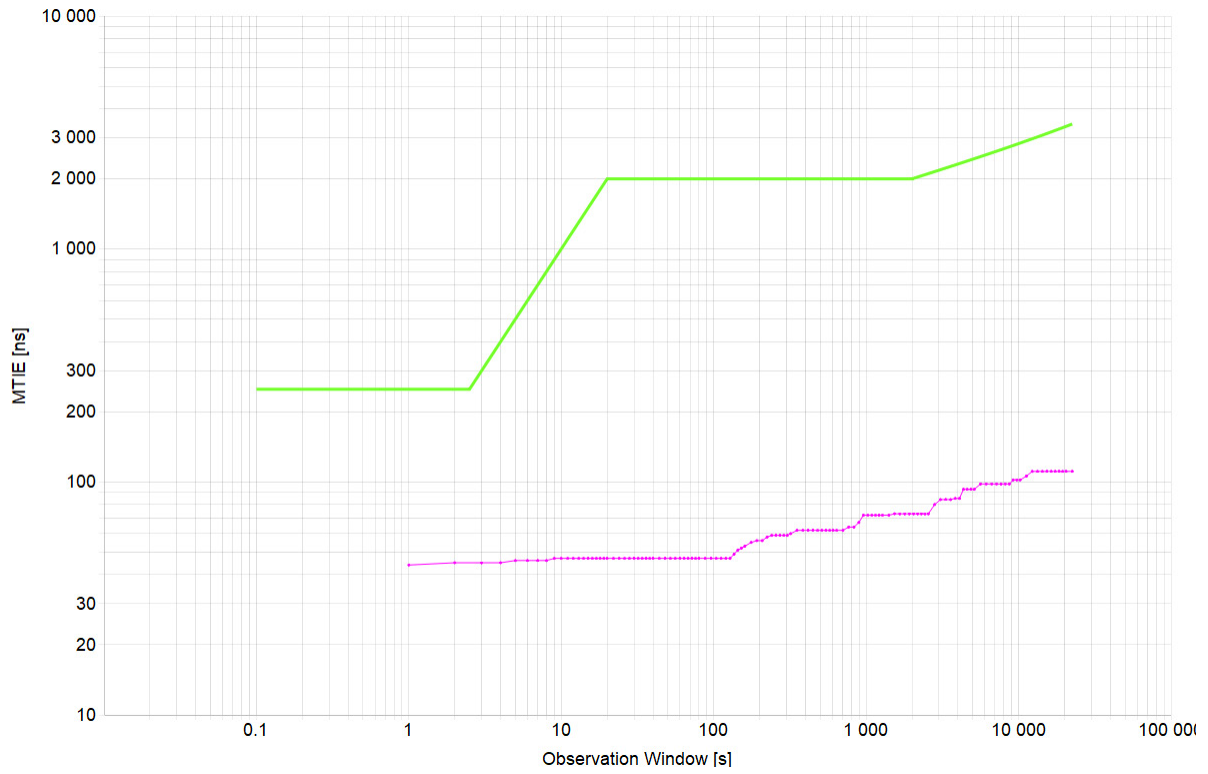
## 6.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	-25ns



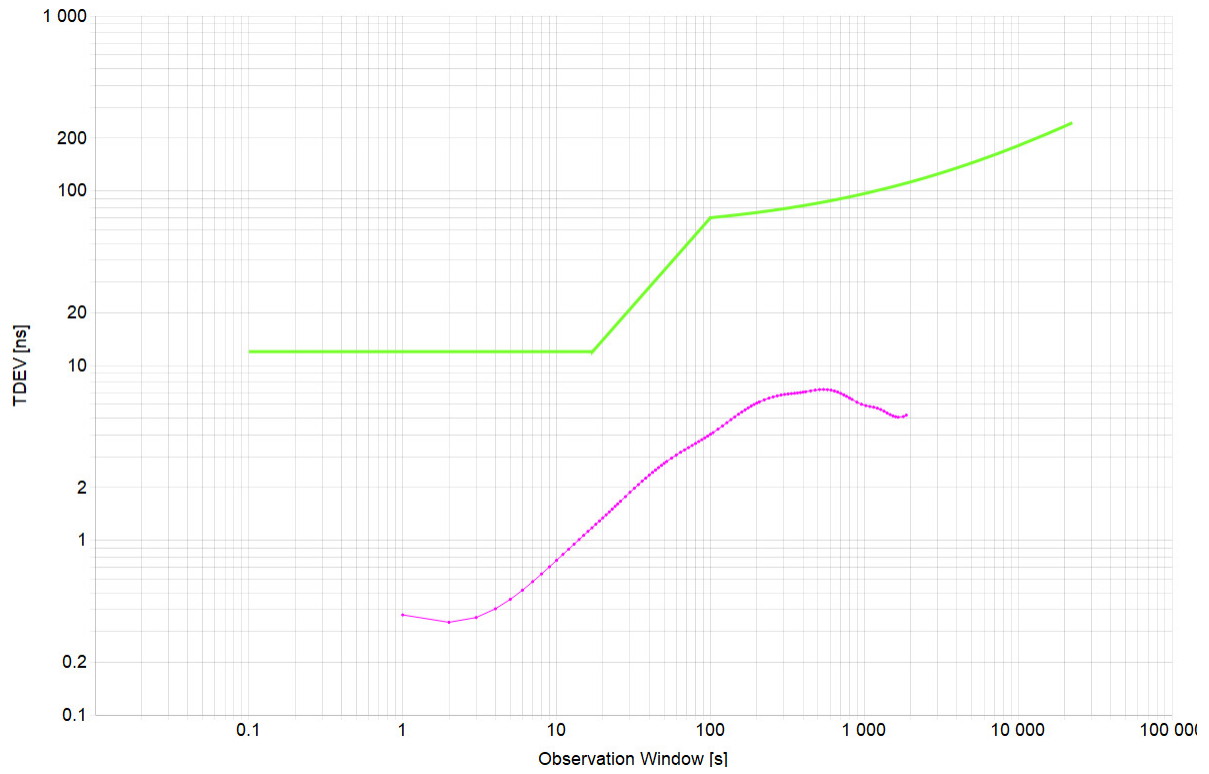
<b>Mean [ns]</b>	-39.958
<b>Min [ns]</b>	-102
<b>Max [ns]</b>	9
<b>Max-Min [ns]</b>	111

## 6.2 MTIE Analysis



<b>Min [ns]</b>	44
<b>Max [ns]</b>	111
<b>Max-Min [ns]</b>	67

### 6.3 TDEV Analysis



<b>Min [ns]</b>	0.339
<b>Max [ns]</b>	7.289
<b>Max-Min [ns]</b>	6.95

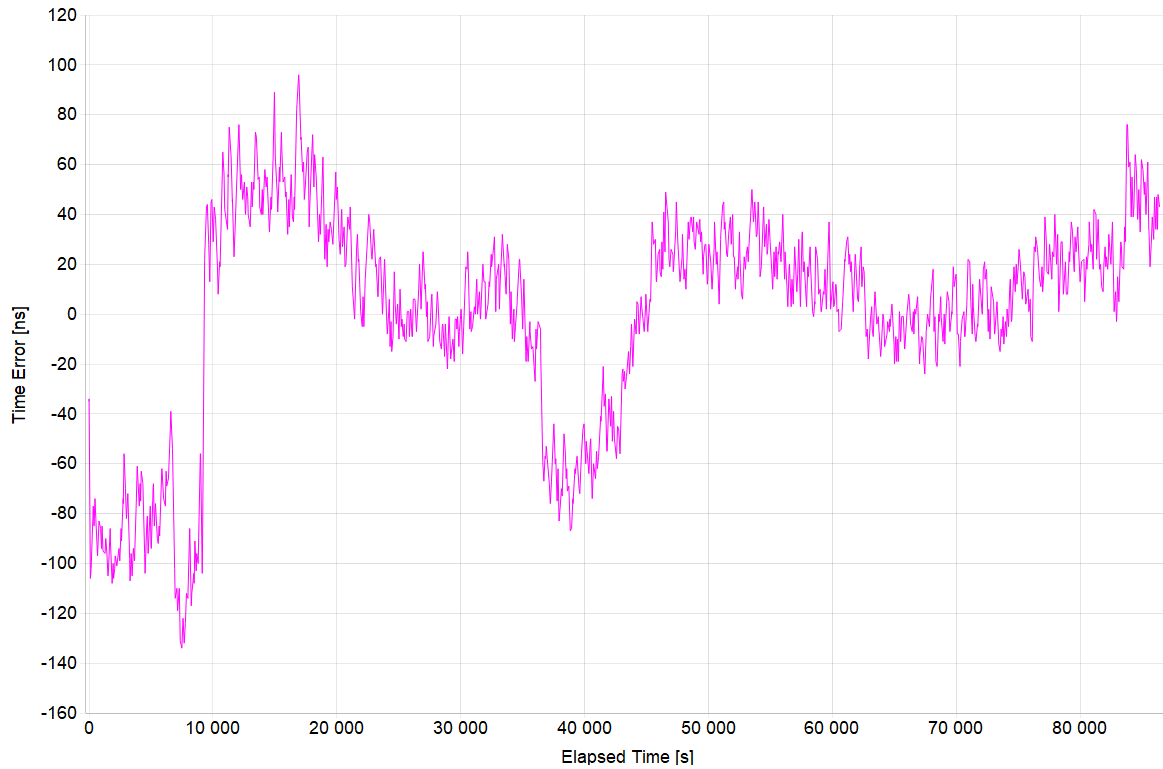
## 7. G.8261: Test Case 14b (SyncE Assist)

<b>Test Description</b>	Test Case 14b
<b>Report Date</b>	22-04-27_17-35-10
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	24:00:01
<b>Time to Phase Lock (s)</b>	379

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

## 7.1 ONEPPS Analysis

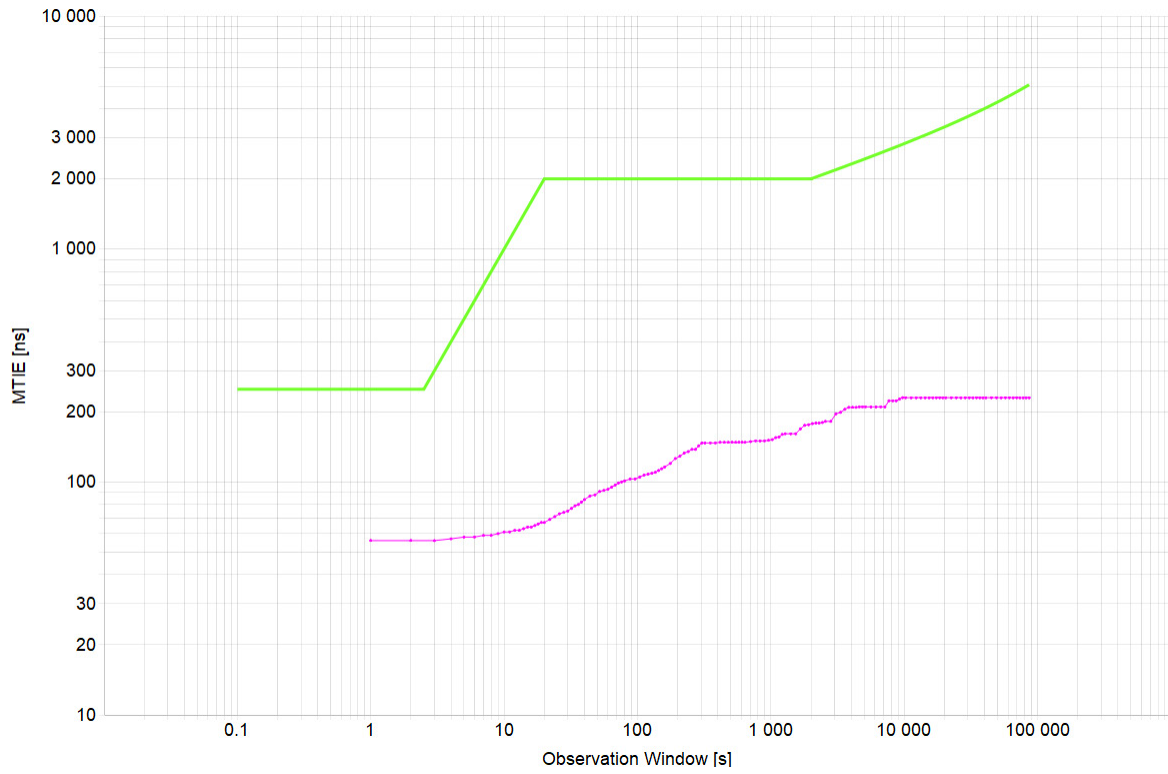
<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	-35ns



<b>Mean [ns]</b>	0.536
<b>Min [ns]</b>	-134
<b>Max [ns]</b>	96
<b>Max-Min [ns]</b>	230

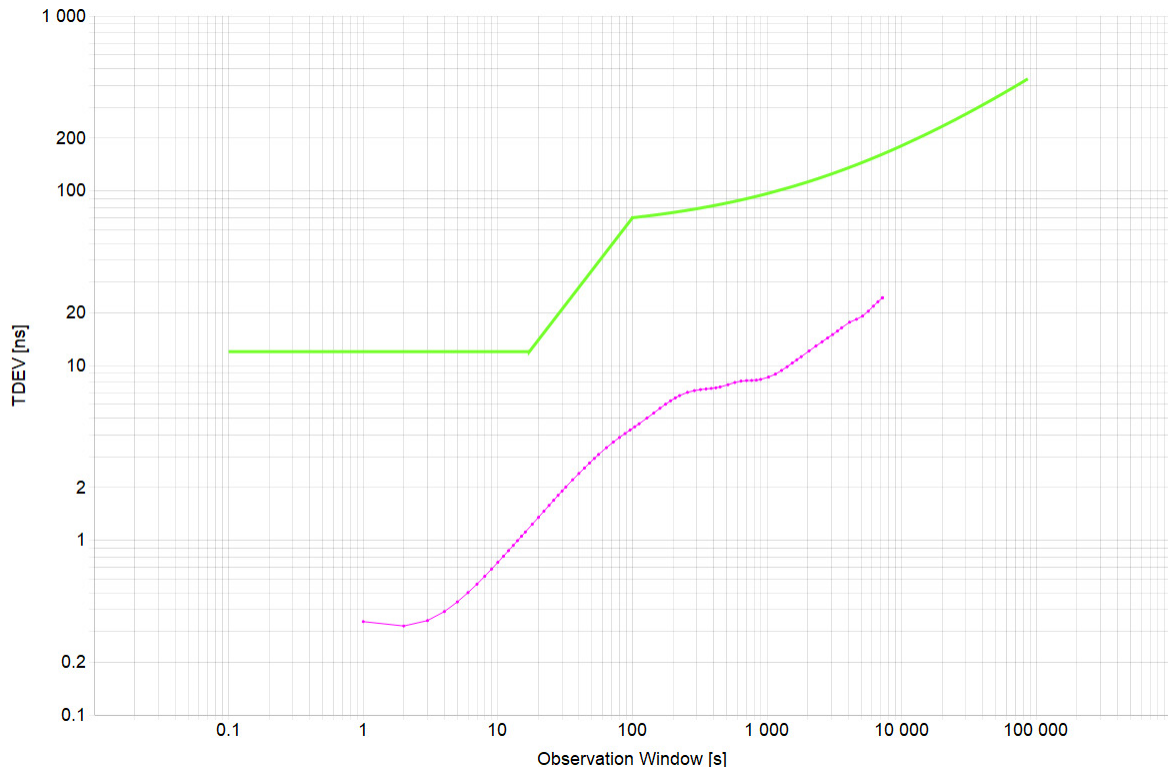


## 7.2 MTIE Analysis



<b>Min [ns]</b>	56
<b>Max [ns]</b>	230
<b>Max-Min [ns]</b>	174

### 7.3 TDEV Analysis



<b>Min [ns]</b>	0.323
<b>Max [ns]</b>	24.415
<b>Max-Min [ns]</b>	24.092

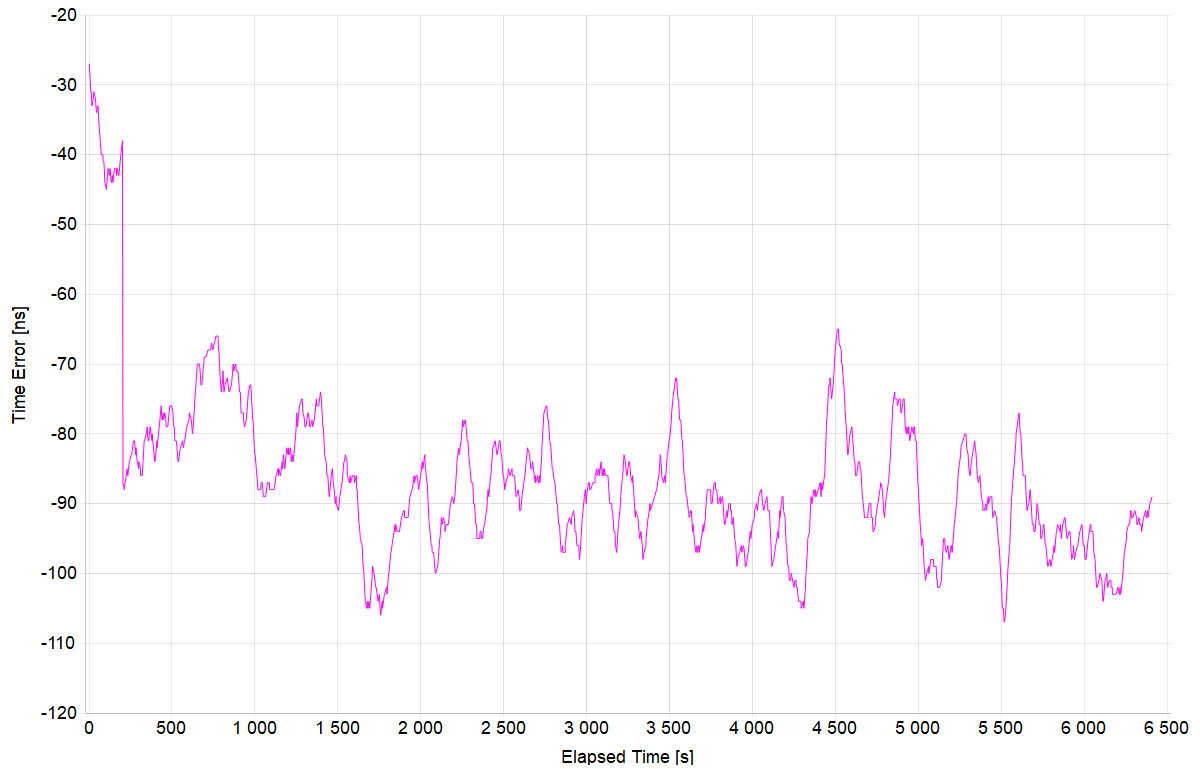
## 8. G.8261: Test Case 15b (SyncE Assist)

<b>Test Description</b>	Test Case 15b
<b>Report Date</b>	22-04-27_17-35-10
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:46:50
<b>Time to Phase Lock (s)</b>	389

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

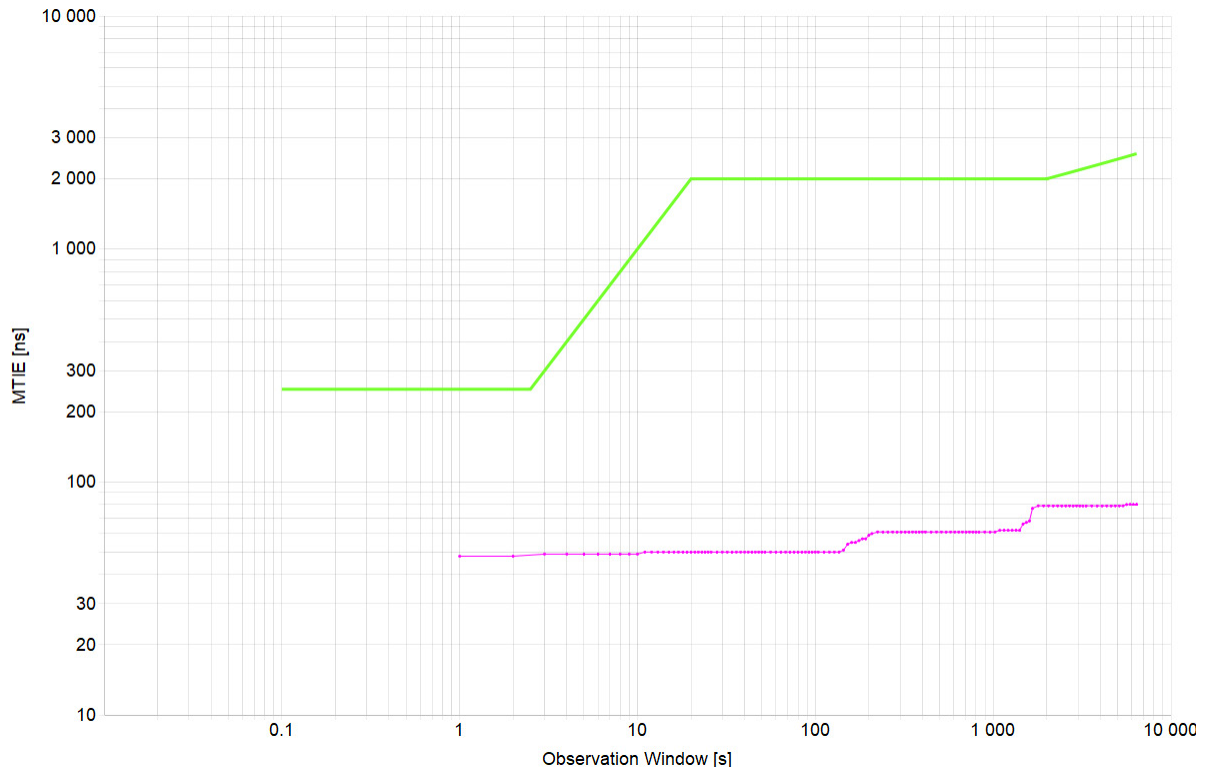
## 8.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	-27ns



<b>Mean [ns]</b>	-86.473
<b>Min [ns]</b>	-107
<b>Max [ns]</b>	-27
<b>Max-Min [ns]</b>	80

## 8.2 MTIE Analysis



<b>Min [ns]</b>	48
<b>Max [ns]</b>	80
<b>Max-Min [ns]</b>	32

## 8.3 TDEV Analysis

<b>Min [ns]</b>	0.462
<b>Max [ns]</b>	5.596
<b>Max-Min [ns]</b>	5.135

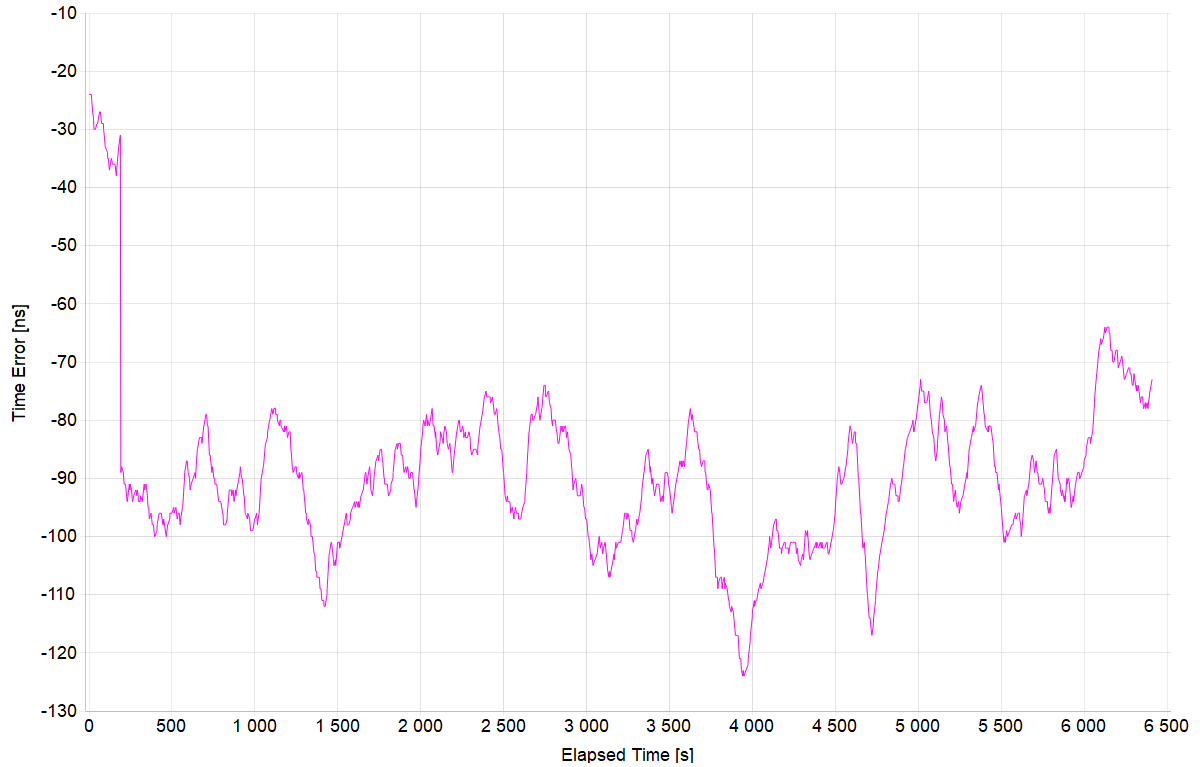
## 9. G.8261: Test Case 16b (SyncE Assist)

<b>Test Description</b>	Test Case 16b
<b>Report Date</b>	22-04-27_17-35-10
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:46:50
<b>Time to Phase Lock (s)</b>	378

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

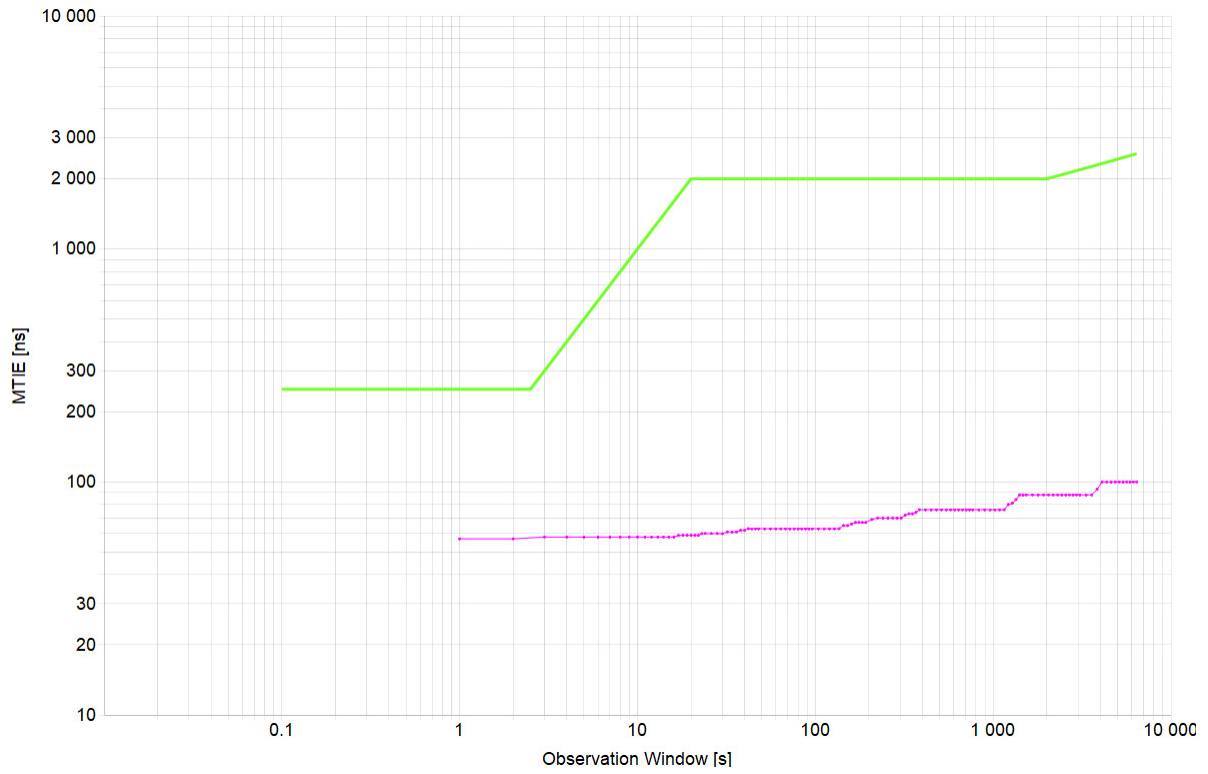
### 9.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	-24ns



<b>Mean [ns]</b>	-89.238
<b>Min [ns]</b>	-124
<b>Max [ns]</b>	-24
<b>Max-Min [ns]</b>	100

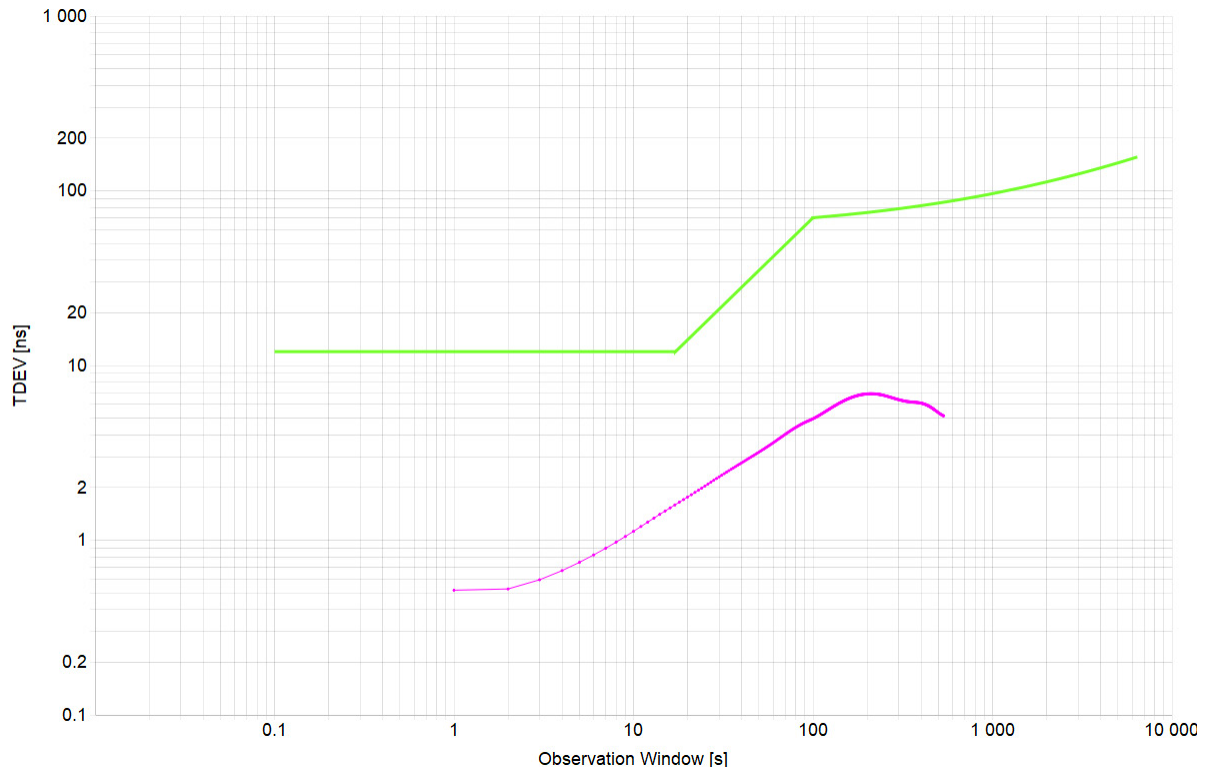
## 9.2 MTIE Analysis



<b>Min [ns]</b>	57
<b>Max [ns]</b>	100
<b>Max-Min [ns]</b>	43



### 9.3 TDEV Analysis



<b>Min [ns]</b>	0.517
<b>Max [ns]</b>	6.887
<b>Max-Min [ns]</b>	6.37

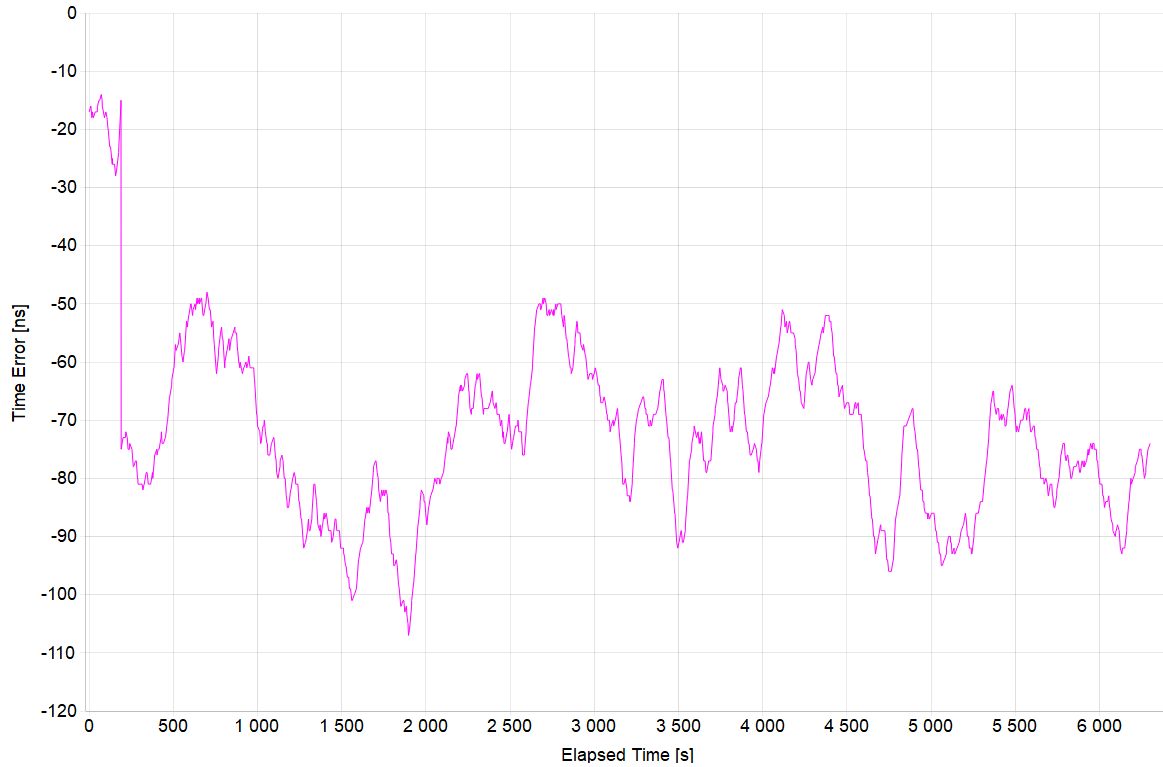
## 10. G.8261: Test Case 17b\_10us (SyncE Assist)

<b>Test Description</b>	Test Case 17b_10us
<b>Report Date</b>	22-04-27_17-35-10
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:45:00
<b>Time to Phase Lock (s)</b>	374

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

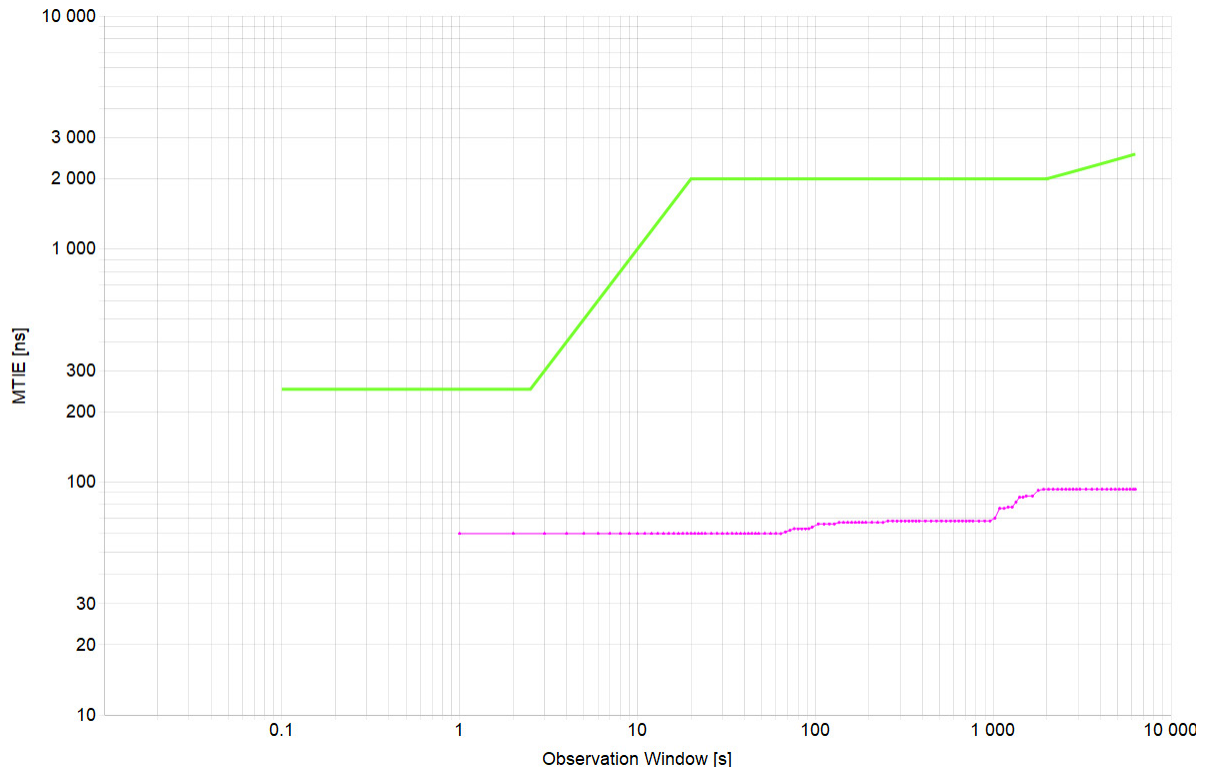
### 10.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	-17ns



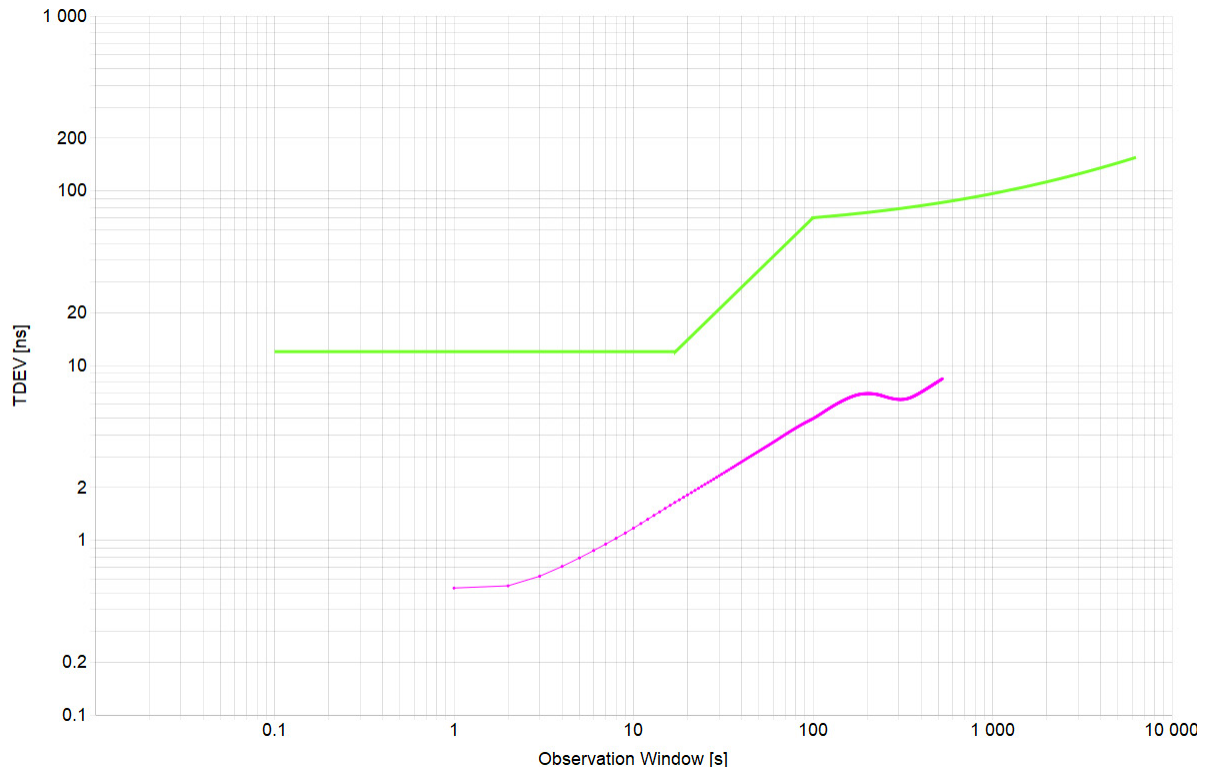
<b>Mean [ns]</b>	-72.085
<b>Min [ns]</b>	-107
<b>Max [ns]</b>	-14
<b>Max-Min [ns]</b>	93

## 10.2 MTIE Analysis



<b>Min [ns]</b>	60
<b>Max [ns]</b>	93
<b>Max-Min [ns]</b>	33

### 10.3 TDEV Analysis



<b>Min [ns]</b>	0.532
<b>Max [ns]</b>	8.382
<b>Max-Min [ns]</b>	7.849

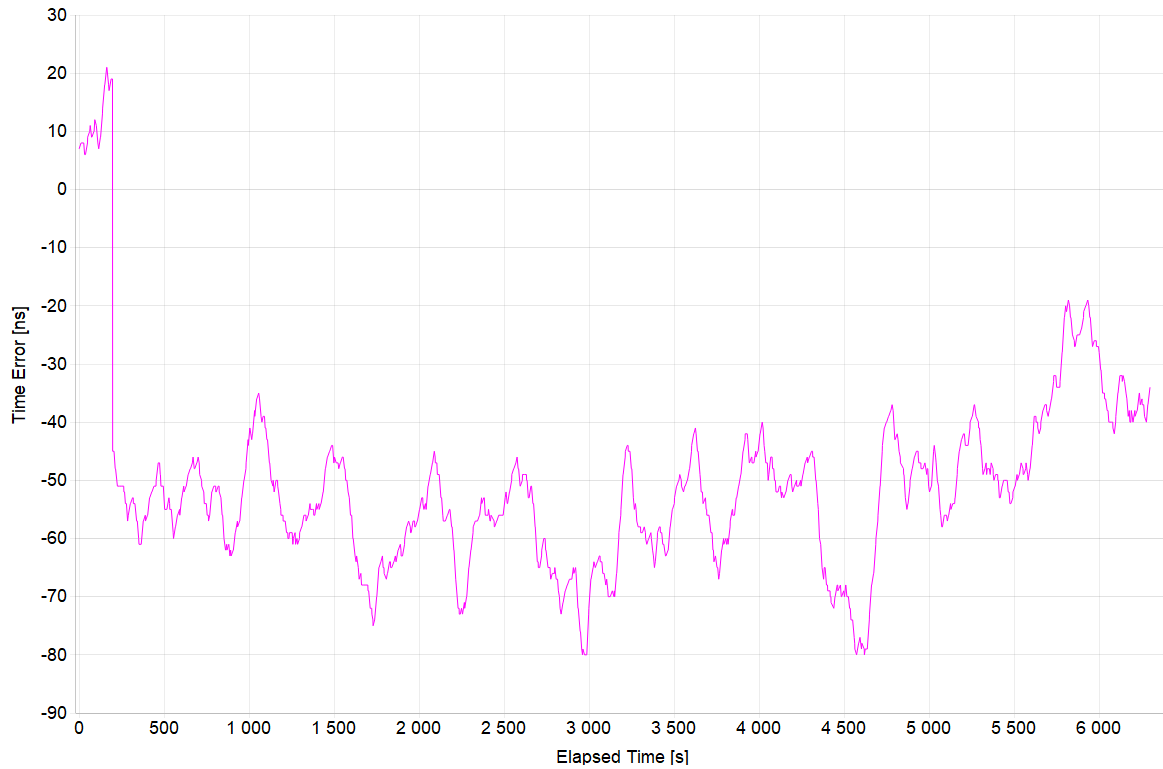
## 11. G.8261: Test Case 17b\_200us (SyncE Assist)

<b>Test Description</b>	Test Case 17b_200us
<b>Report Date</b>	22-04-27_17-35-10
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:45:00
<b>Time to Phase Lock (s)</b>	373

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

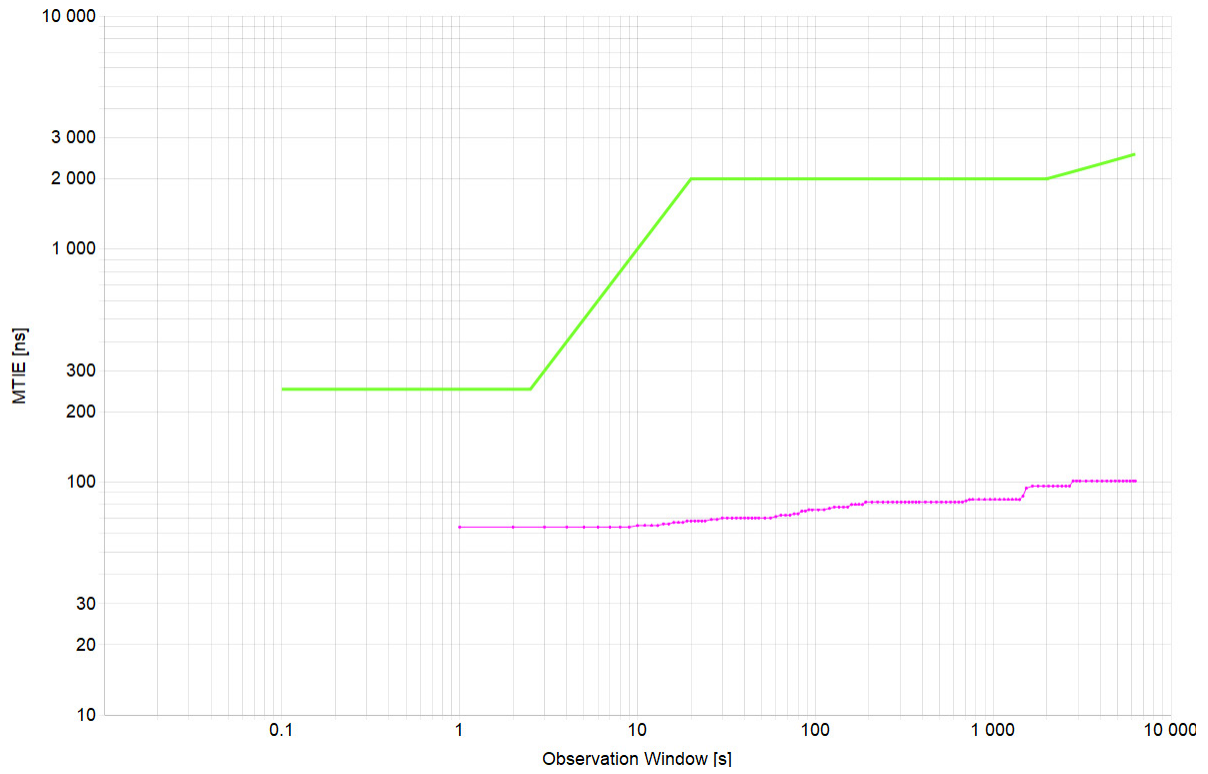
### 11.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	7ns



<b>Mean [ns]</b>	-50.782
<b>Min [ns]</b>	-80
<b>Max [ns]</b>	21
<b>Max-Min [ns]</b>	101

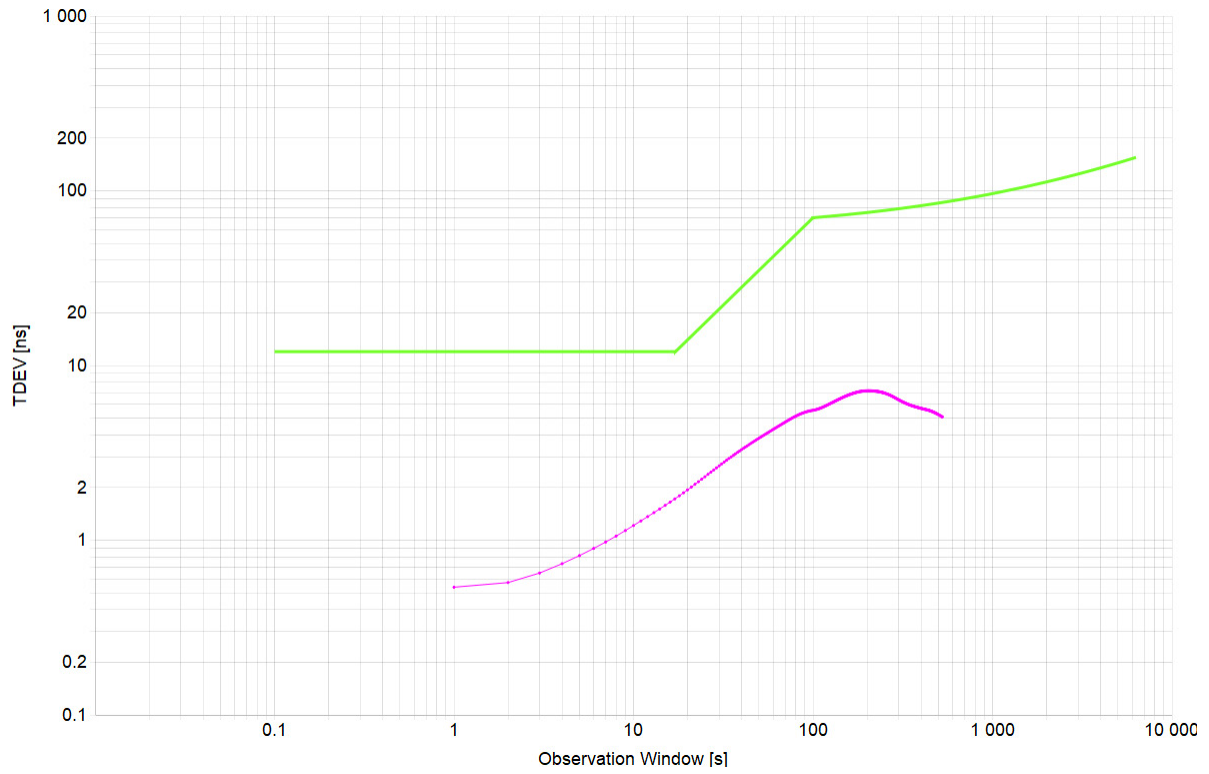
## 11.2 MTIE Analysis



<b>Min [ns]</b>	64
<b>Max [ns]</b>	101
<b>Max-Min [ns]</b>	37



### 11.3 TDEV Analysis



<b>Min [ns]</b>	0.538
<b>Max [ns]</b>	7.162
<b>Max-Min [ns]</b>	6.624

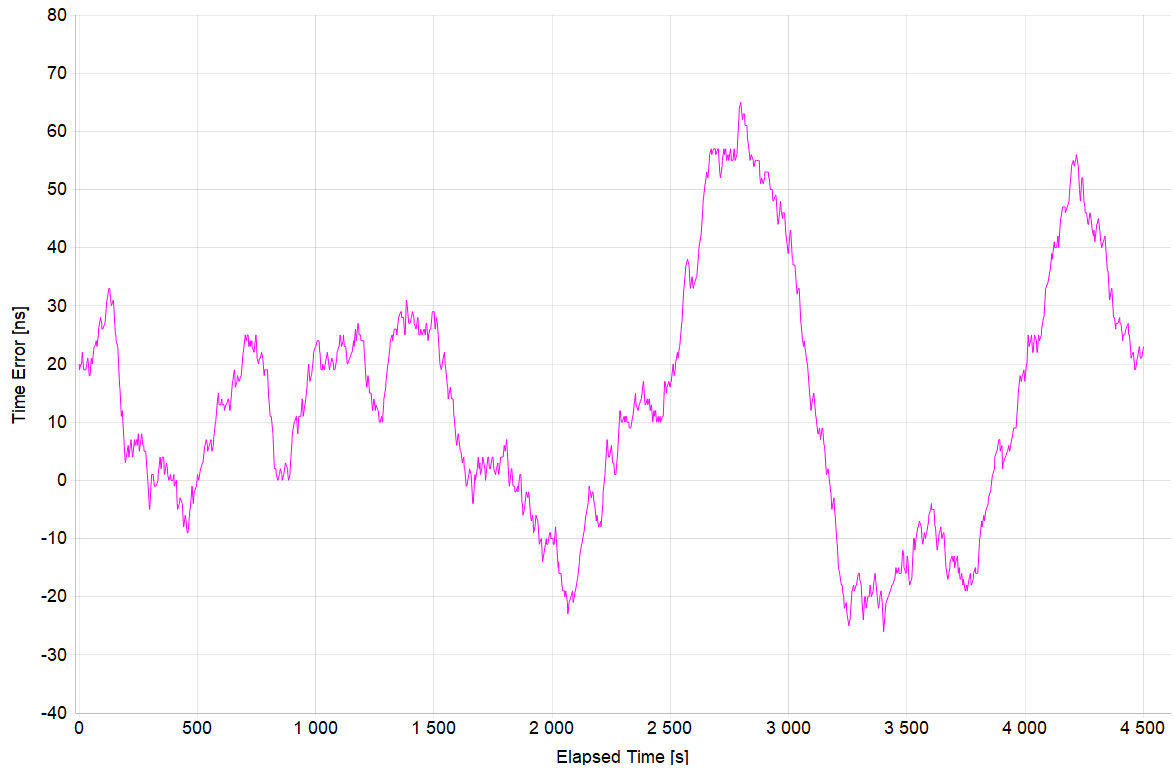
## 12. G.8261: Test Case 12

<b>Test Description</b>	Test Case 12
<b>Report Date</b>	22-10-18_13-29-39
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:15:00
<b>Test Configuration</b>	2
<b>Time to Phase Lock (s)</b>	372
<b>Oscillator</b>	Rakon M6141 MiniOcxo

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

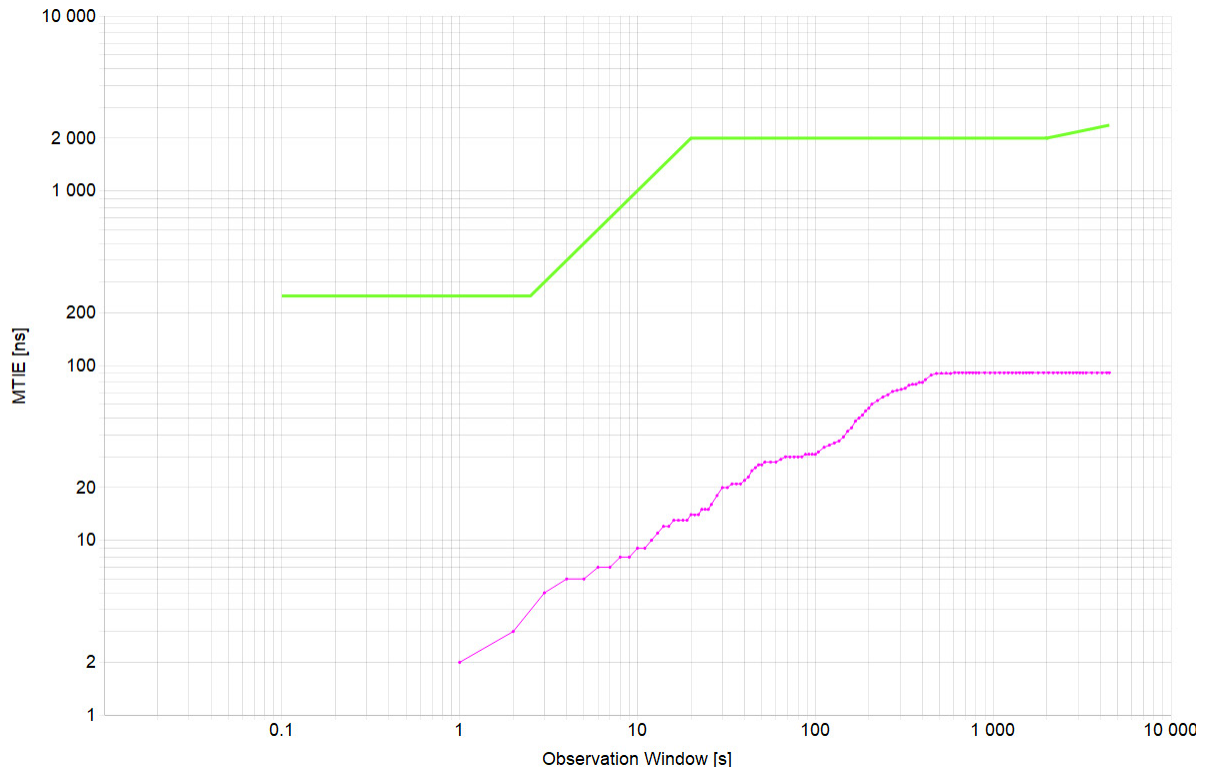
## 12.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	20ns



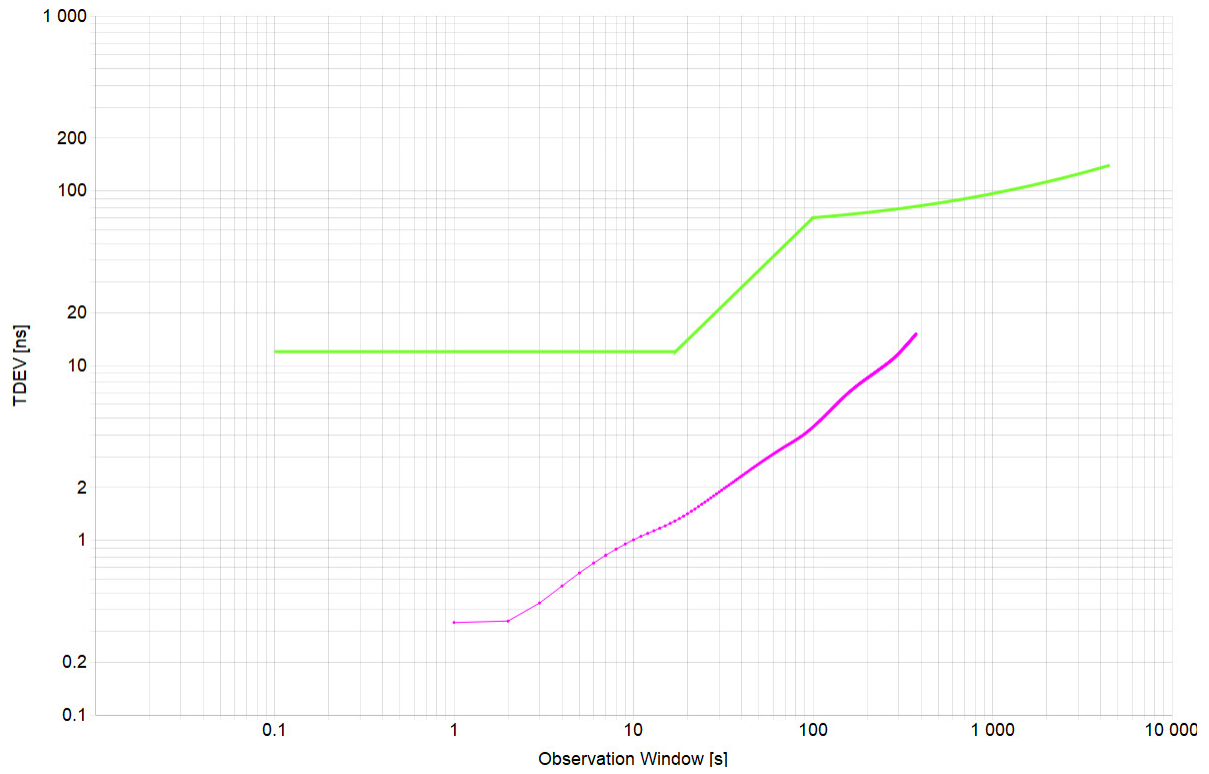
<b>Mean [ns]</b>	13.247
<b>Min [ns]</b>	-26
<b>Max [ns]</b>	65
<b>Max-Min [ns]</b>	91

## 12.2 MTIE Analysis



<b>Min [ns]</b>	2
<b>Max [ns]</b>	91
<b>Max-Min [ns]</b>	89

### 12.3 TDEV Analysis



<b>Min [ns]</b>	0.338
<b>Max [ns]</b>	15.117
<b>Max-Min [ns]</b>	14.779

### 13. G.8261: Test Case 13 Network Traffic Model 2

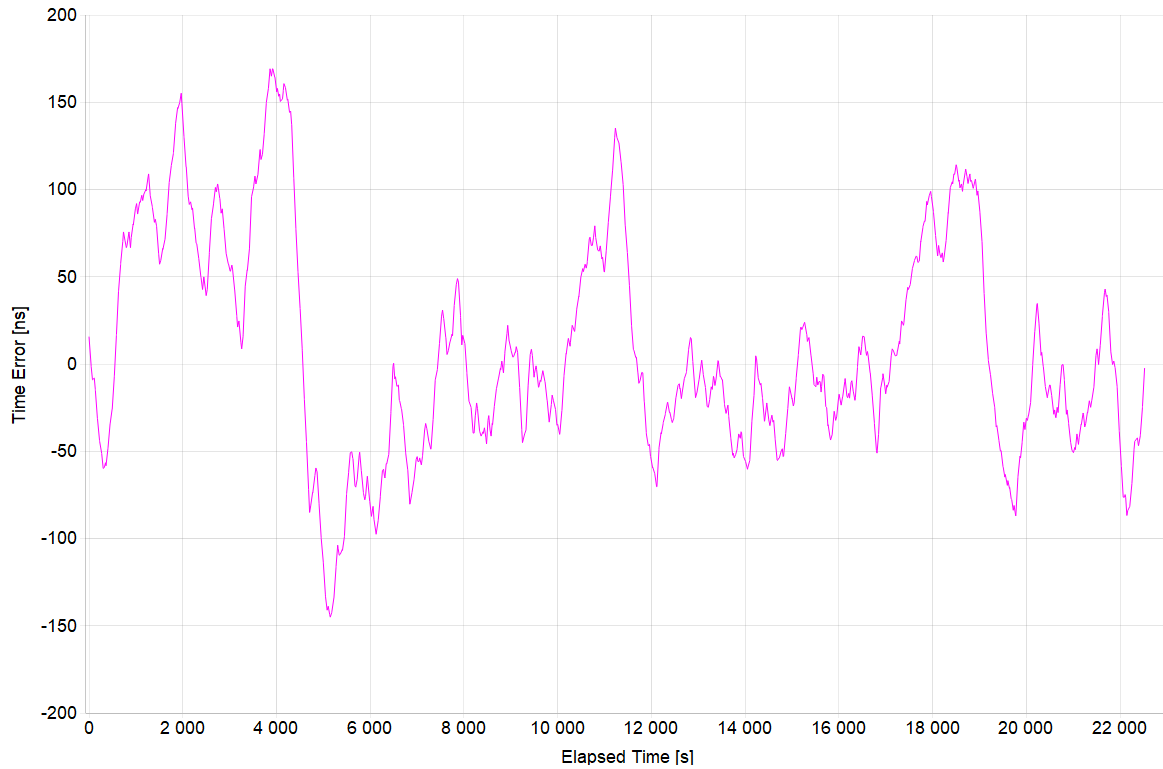
<b>Test Description</b>	Test Case 13 Network Traffic Model 2
<b>Report Date</b>	22-04-27_15-55-48
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	06:15:22
<b>Test Configuration</b>	1
<b>Time to Phase Lock (s)</b>	63

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

1. Data presented in the images below was collected using the following procedure:
  - Start PTP
  - Stabilize for 600s
  - Start PDV
  - Start Data Capture
2. This results in a short time to phase lock (see above table).Lock time under PDV conditions was measured to be 372s. The following procedure was used for locking under PDV conditions:
  - Start PDV
  - Start PTP
  - Start Data Capture

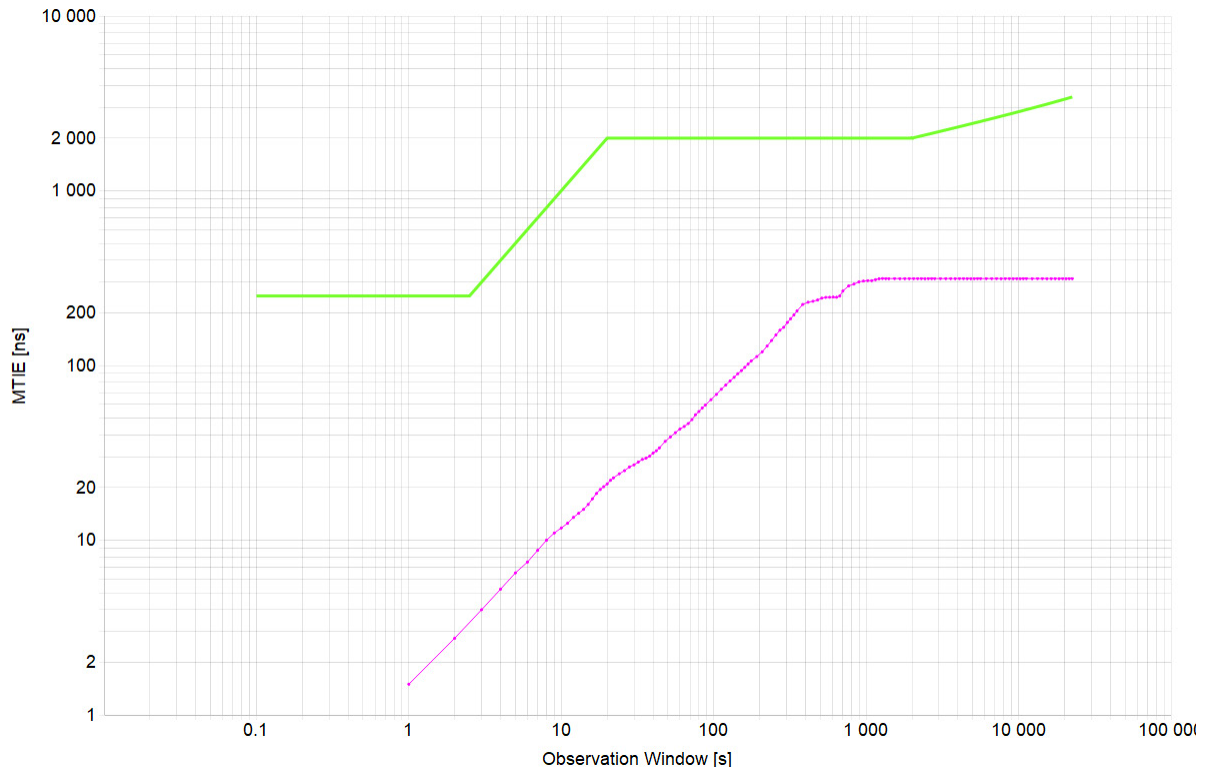
### 13.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	15.677ns



<b>Mean [ns]</b>	7.238
<b>Min [ns]</b>	-145.073
<b>Max [ns]</b>	169.177
<b>Max-Min [ns]</b>	314.25

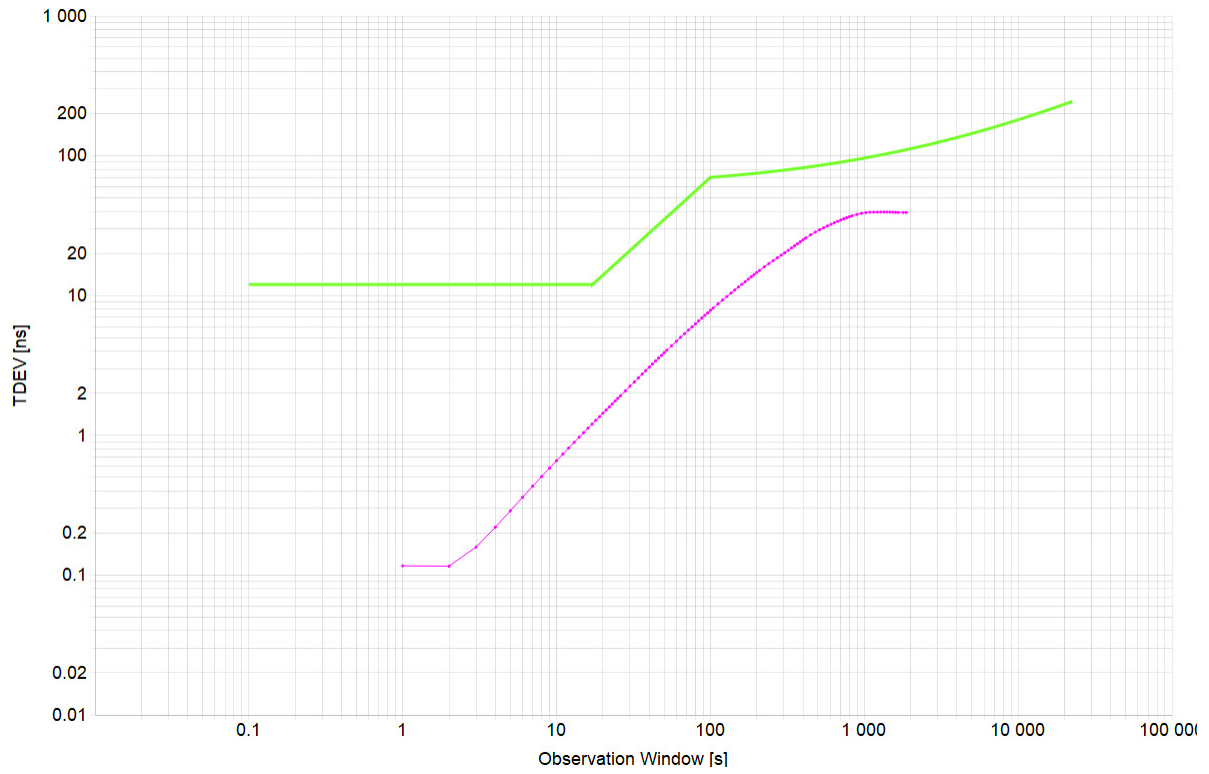
### 13.2 MTIE Analysis



<b>Min [ns]</b>	1.5
<b>Max [ns]</b>	314.25
<b>Max-Min [ns]</b>	312.75



### 13.3 TDEV Analysis



<b>Min [ns]</b>	0.116
<b>Max [ns]</b>	39.584
<b>Max-Min [ns]</b>	39.468

## 14. G.8261: Test Case 14 Network Traffic Model 2

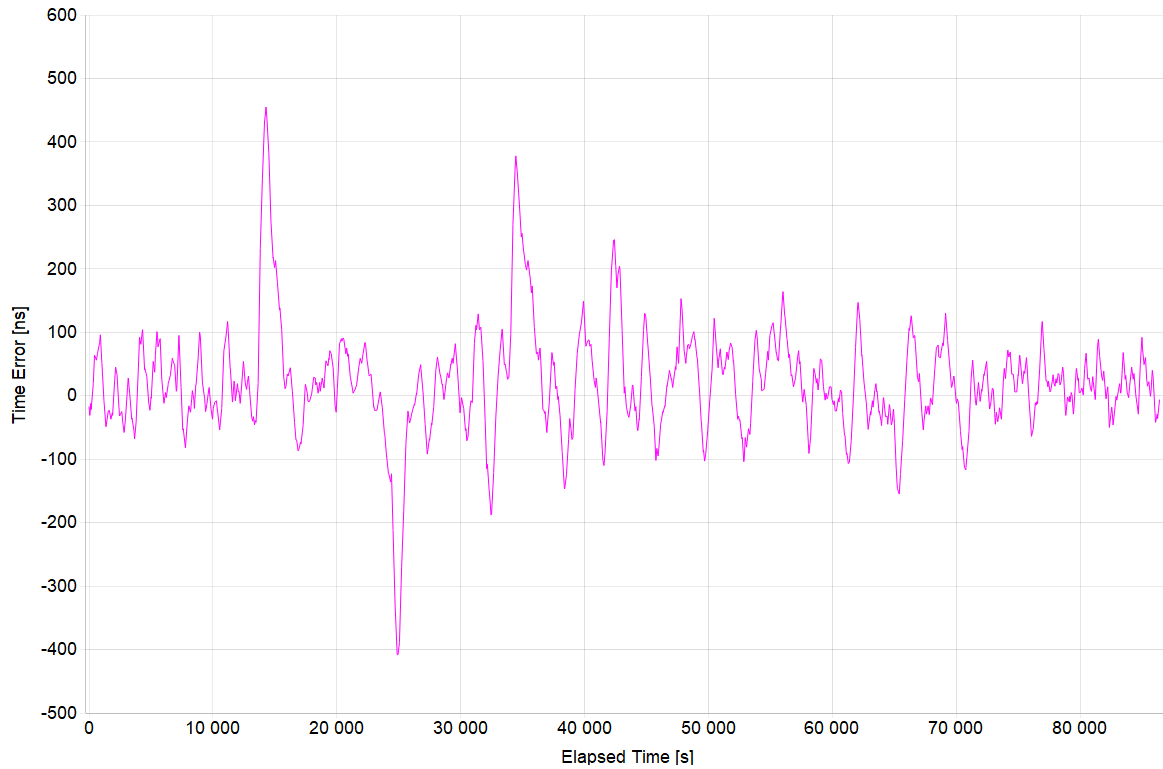
<b>Test Description</b>	Test Case 14 Network Traffic Model 2
<b>Report Date</b>	22-04-27_15-55-48
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	24:00:01
<b>Test Configuration</b>	2
<b>Time to Phase Lock (s)</b>	62

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

1. Data presented in the images below was collected using the following procedure:
  - Start PTP
  - Stabilize for 600s
  - Start PDV
  - Start Data Capture
2. This results in a short time to phase lock (see above table). Lock time under PDV conditions was measured to be 370s. The following procedure was used for locking under PDV conditions:
  - Start PDV
  - Start PTP
  - Start Data Capture

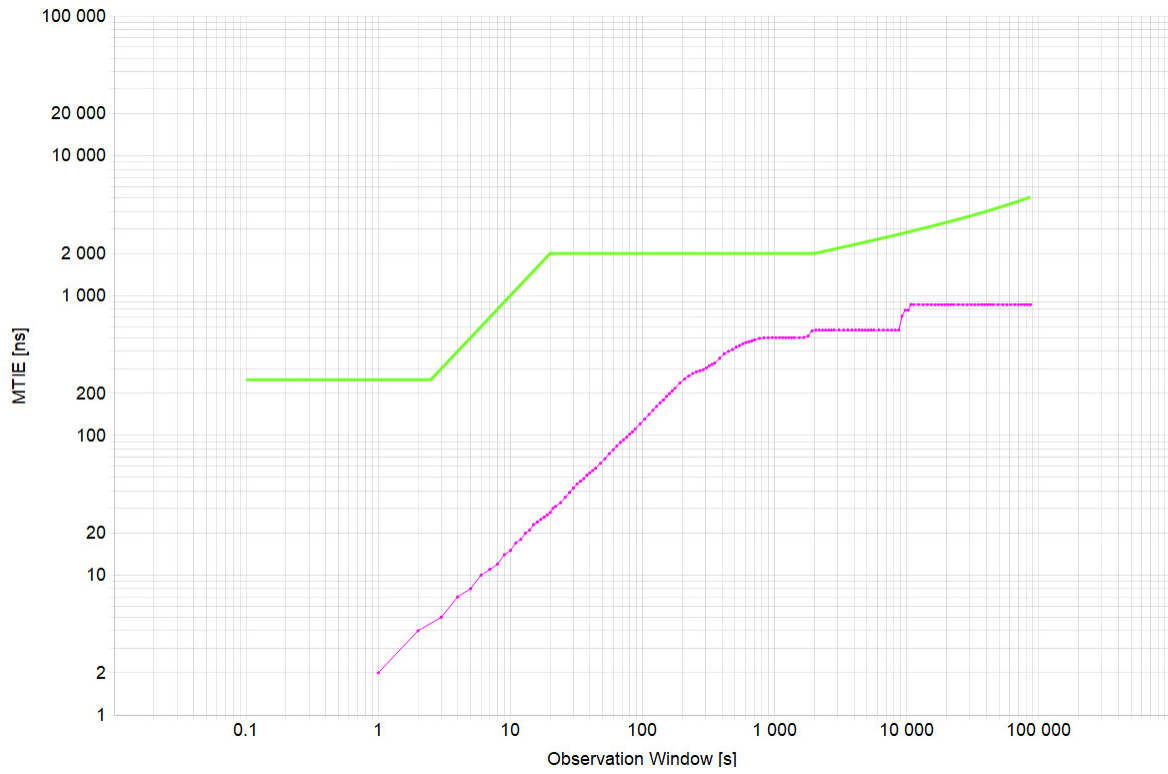
### 14.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	-17ns



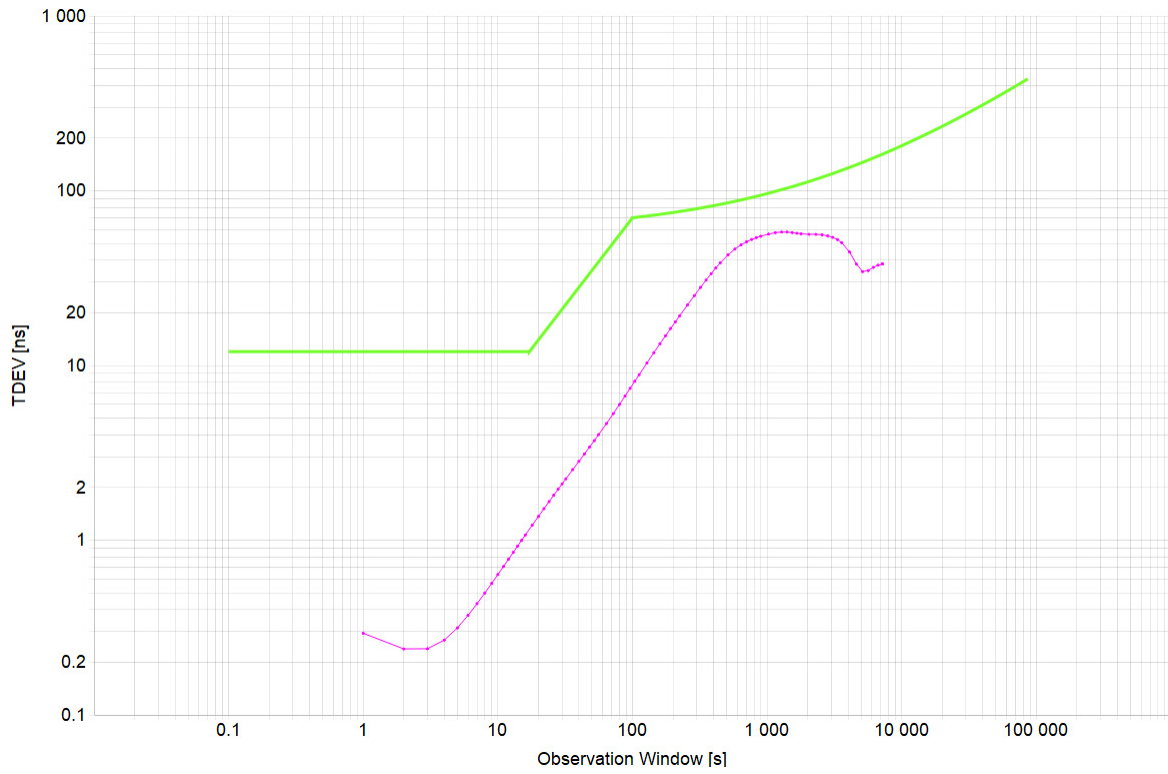
<b>Mean [ns]</b>	21.13
<b>Min [ns]</b>	-408
<b>Max [ns]</b>	455
<b>Max-Min [ns]</b>	863

## 14.2 MTIE Analysis



<b>Min [ns]</b>	2
<b>Max [ns]</b>	863
<b>Max-Min [ns]</b>	861

### 14.3 TDEV Analysis



<b>Min [ns]</b>	0.239
<b>Max [ns]</b>	58.038
<b>Max-Min [ns]</b>	57.8

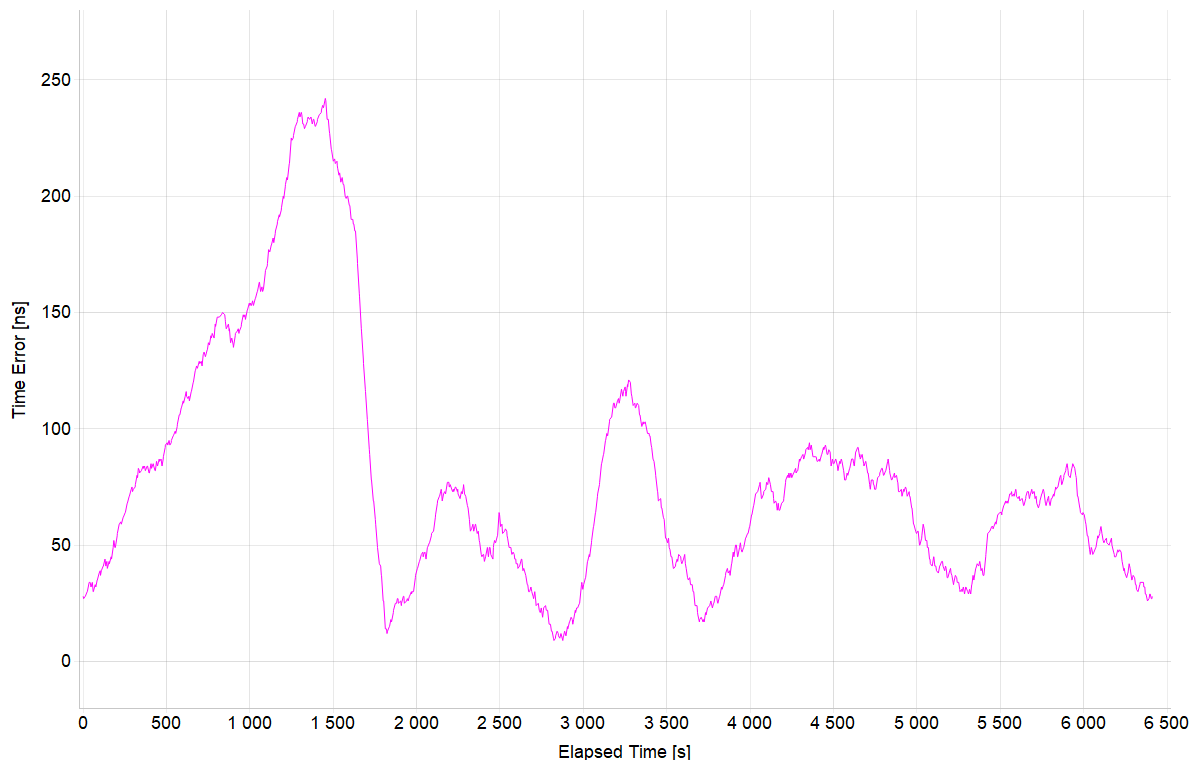
## 15. G.8261: Test Case 15b

<b>Test Description</b>	Test Case 15b
<b>Report Date</b>	22-10-18_13-29-39
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:46:50
<b>Test Configuration</b>	2
<b>Time to Phase Lock (s)</b>	372
<b>Oscillator</b>	Rakon M6141 MiniOcxo

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

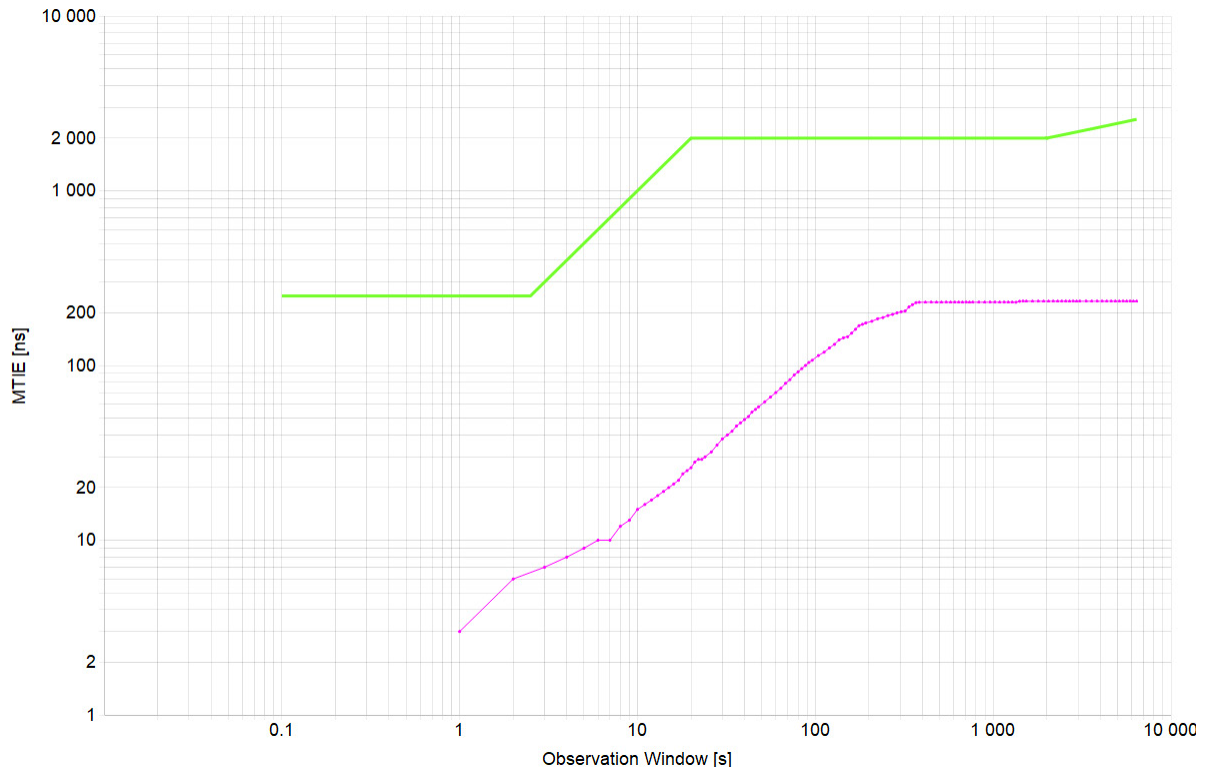
## 15.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	28ns



<b>Mean [ns]</b>	79.26
<b>Min [ns]</b>	9
<b>Max [ns]</b>	242
<b>Max-Min [ns]</b>	233

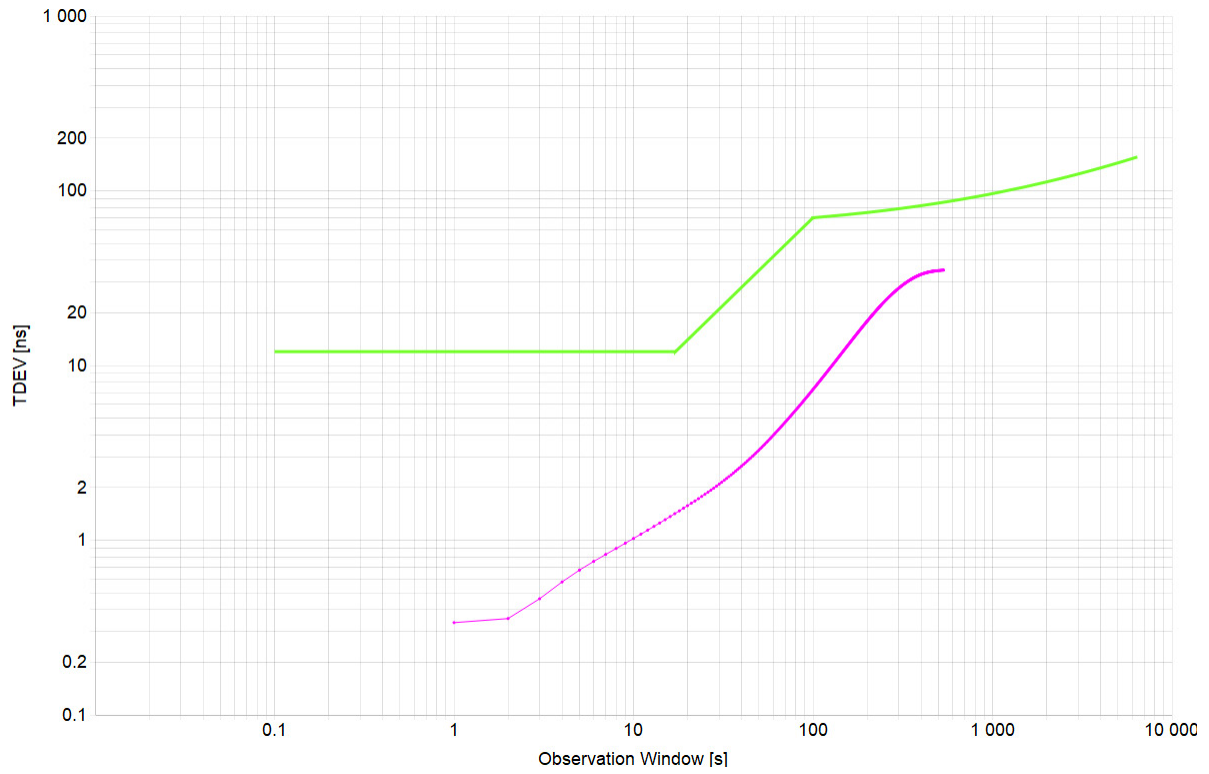
## 15.2 MTIE Analysis



<b>Min [ns]</b>	3
<b>Max [ns]</b>	233
<b>Max-Min [ns]</b>	230



### 15.3 TDEV Analysis



<b>Min [ns]</b>	0.337
<b>Max [ns]</b>	35.161
<b>Max-Min [ns]</b>	34.823

## 16. G.8261: Test Case 16 Network Traffic Model 2

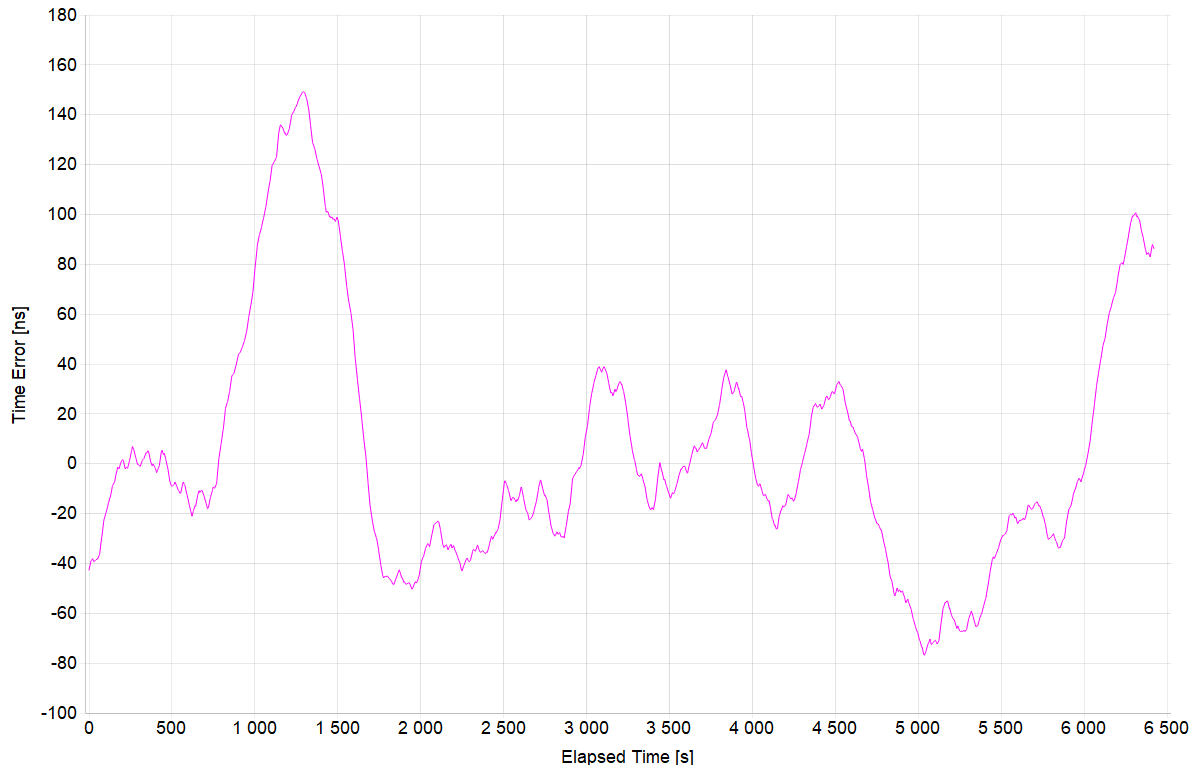
<b>Test Description</b>	Test Case 16 Network Traffic Model 2
<b>Report Date</b>	22-04-27_15-55-48
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:47:02
<b>Test Configuration</b>	1
<b>Time to Phase Lock (s)</b>	61

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

1. Data presented in the images below was collected using the following procedure:
  - Start PTP
  - Stabilize for 600s
  - Start PDV
  - Start Data Capture
2. This results in a short time to phase lock (see above table). Lock time under PDV conditions was measured to be 372s. The following procedure was used for locking under PDV conditions:
  - Start PDV
  - Start PTP
  - Start Data Capture

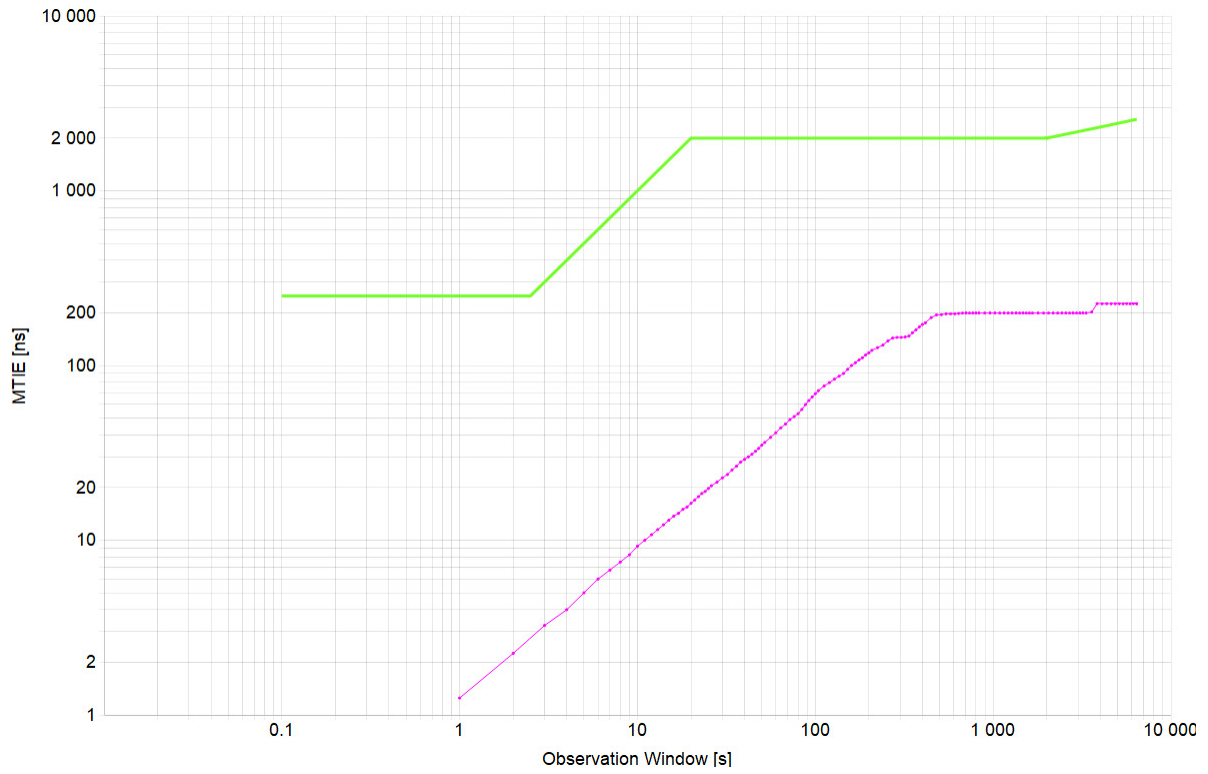
### 16.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	-42.823ns



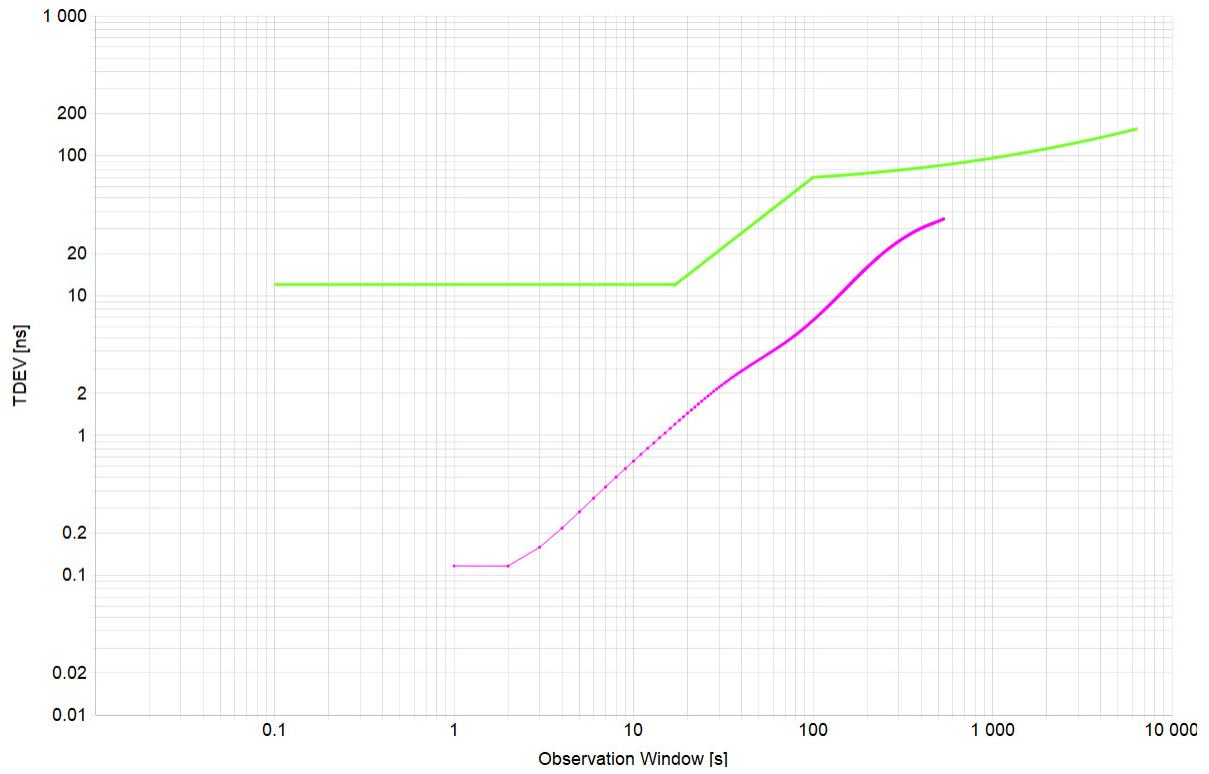
<b>Mean [ns]</b>	3.558
<b>Min [ns]</b>	-76.823
<b>Max [ns]</b>	149.177
<b>Max-Min [ns]</b>	226

## 16.2 MTIE Analysis



<b>Min [ns]</b>	1.25
<b>Max [ns]</b>	226
<b>Max-Min [ns]</b>	224.75

### 16.3 TDEV Analysis



<b>Min [ns]</b>	0.116
<b>Max [ns]</b>	35.344
<b>Max-Min [ns]</b>	35.227

## 17. G.8261: Test Case 17 (10µs) Network Traffic Model 2

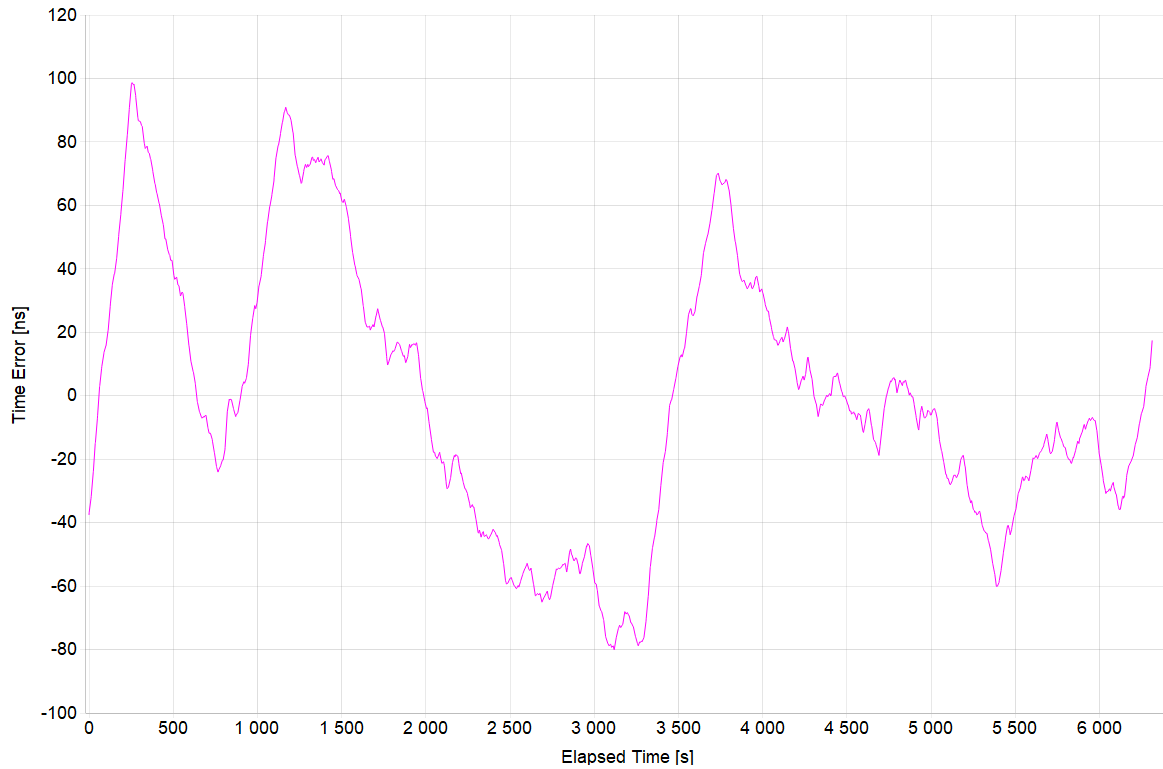
<b>Test Description</b>	Test Case 17 (10µs) Network Traffic Model 2
<b>Report Date</b>	22-04-27_15-55-48
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:45:12
<b>Test Configuration</b>	1
<b>Time to Phase Lock (s)</b>	63

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

1. Data presented in the images below was collected using the following procedure:
  - Start PTP
  - Stabilize for 600s
  - Start PDV
  - Start Data Capture
2. This results in a short time to phase lock (see above table). Lock time under PDV conditions was measured to be 558s. The following procedure was used for locking under PDV conditions:
  - Start PDV
  - Start PTP
  - Start Data Capture

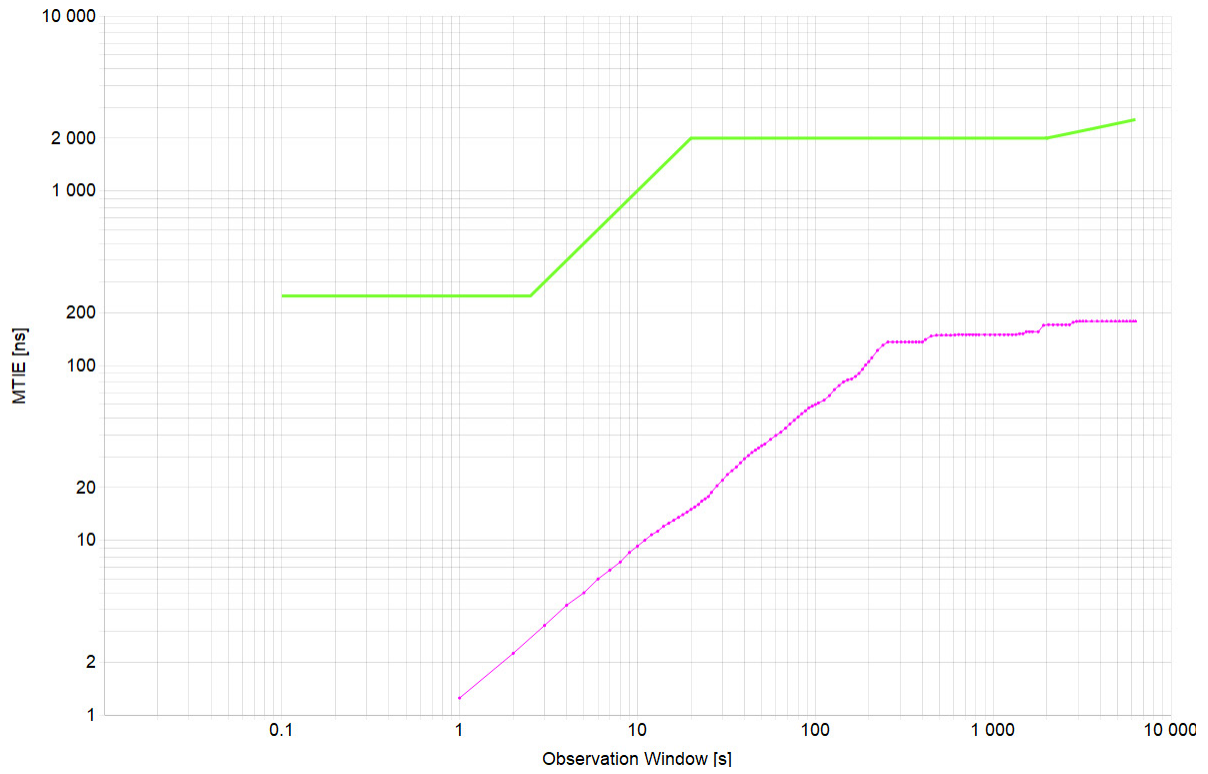
### 17.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	-37.573ns



<b>Mean [ns]</b>	-0.911
<b>Min [ns]</b>	-80.073
<b>Max [ns]</b>	98.677
<b>Max-Min [ns]</b>	178.75

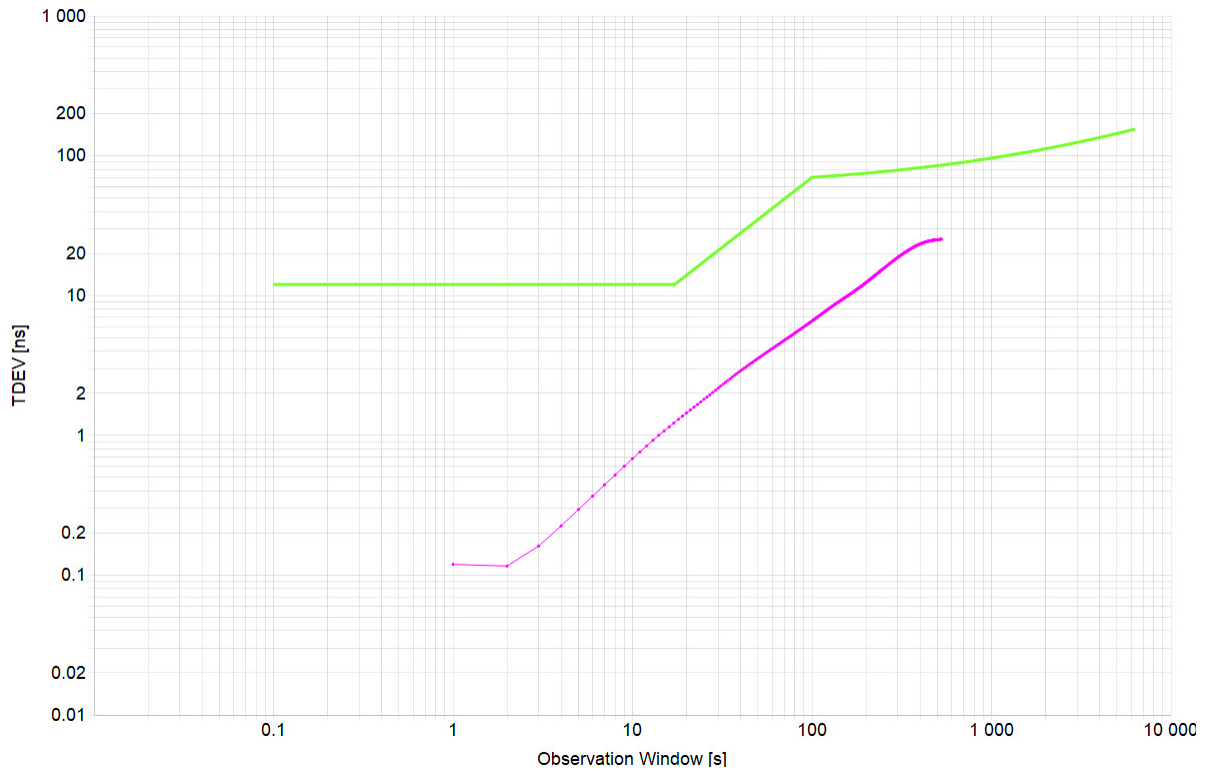
## 17.2 MTIE Analysis



<b>Min [ns]</b>	1.25
<b>Max [ns]</b>	178.75
<b>Max-Min [ns]</b>	177.5



### 17.3 TDEV Analysis



<b>Min [ns]</b>	0.116
<b>Max [ns]</b>	25.315
<b>Max-Min [ns]</b>	25.199

## 18. G.8261: Test Case 17 (200µs) Network Traffic Model 2

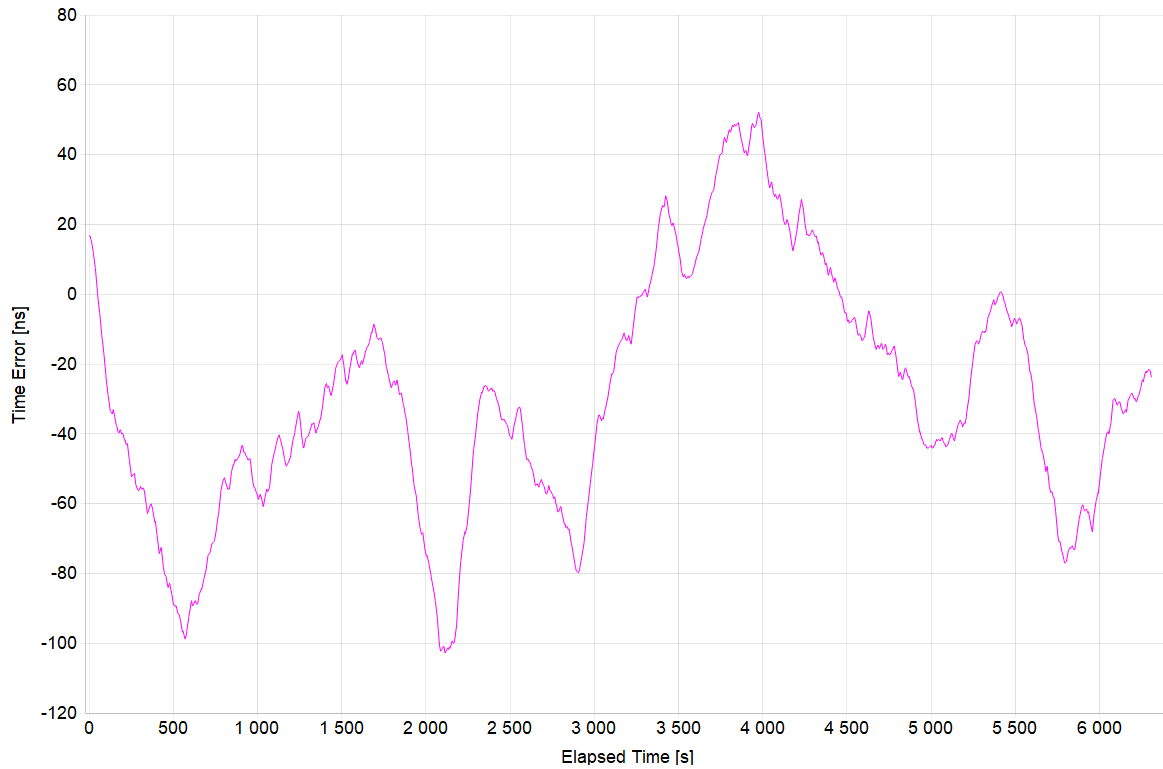
<b>Test Description</b>	Test Case 17 (200µs) Network Traffic Model 2
<b>Report Date</b>	22-04-27_15-55-48
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:45:07
<b>Test Configuration</b>	1
<b>Time to Phase Lock (s)</b>	56

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

1. Data presented in the images below was collected using the following procedure:
  - Start PTP
  - Stabilize for 600s
  - Start PDV
  - Start Data Capture
2. This results in a short time to phase lock (see above table). Lock time under PDV conditions was measured to be 374s. The following procedure was used for locking under PDV conditions:
  - Start PDV
  - Start PTP
  - Start Data Capture

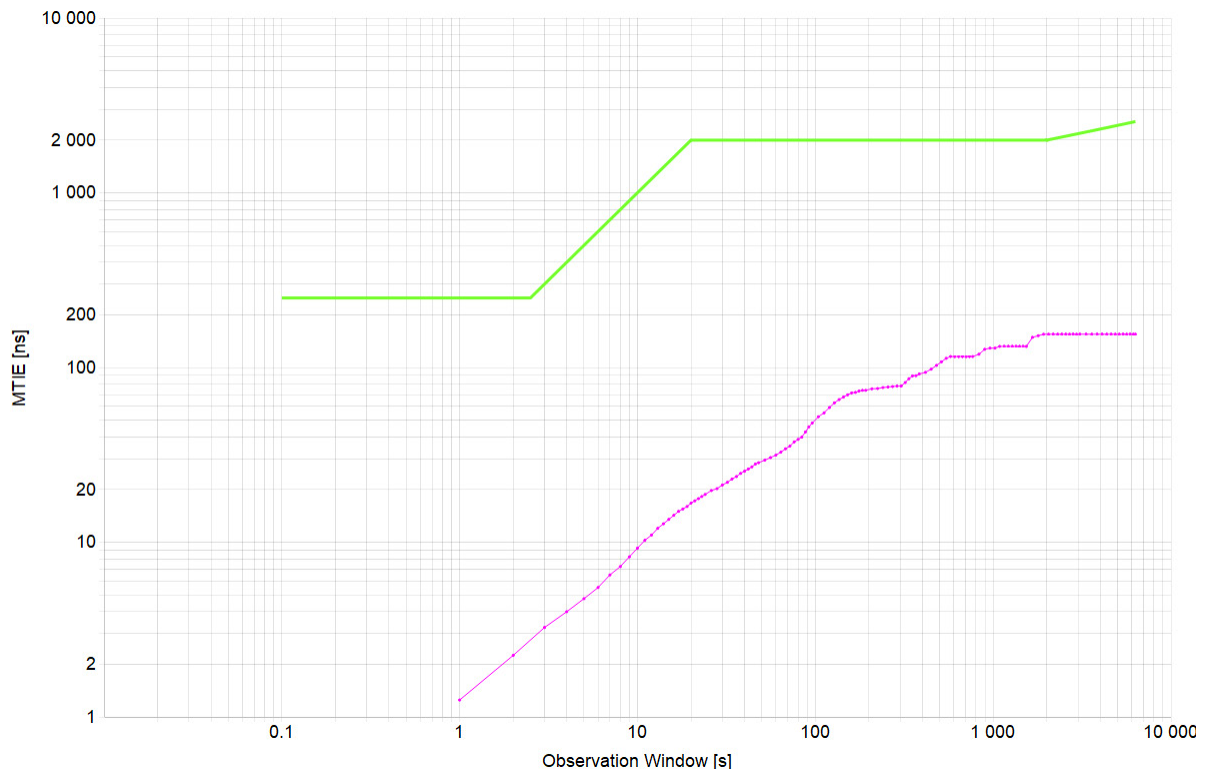
### 18.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	16.677ns



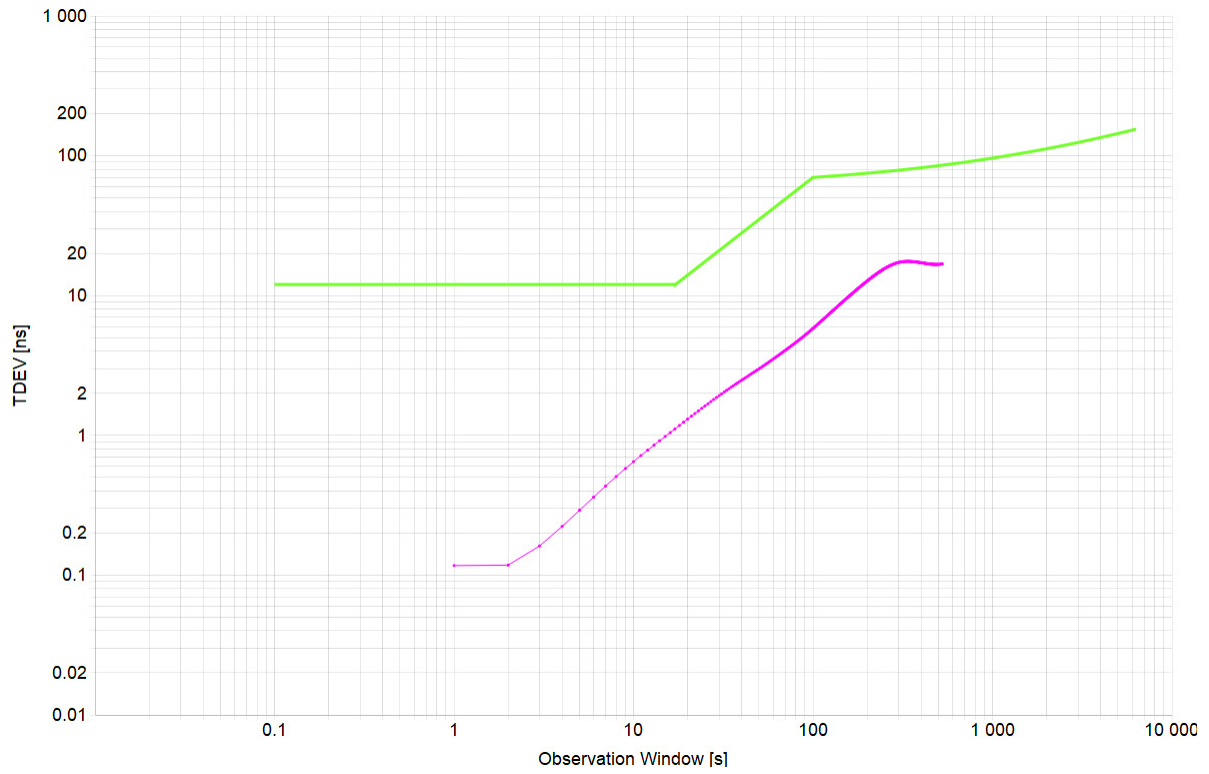
<b>Mean [ns]</b>	-29.338
<b>Min [ns]</b>	-102.823
<b>Max [ns]</b>	52.177
<b>Max-Min [ns]</b>	155

## 18.2 MTIE Analysis



<b>Min [ns]</b>	1.25
<b>Max [ns]</b>	155
<b>Max-Min [ns]</b>	153.75

### 18.3 TDEV Analysis



<b>Min [ns]</b>	0.117
<b>Max [ns]</b>	17.569
<b>Max-Min [ns]</b>	17.452

## 19. G.8261: Test Case 12 (SinglePath)

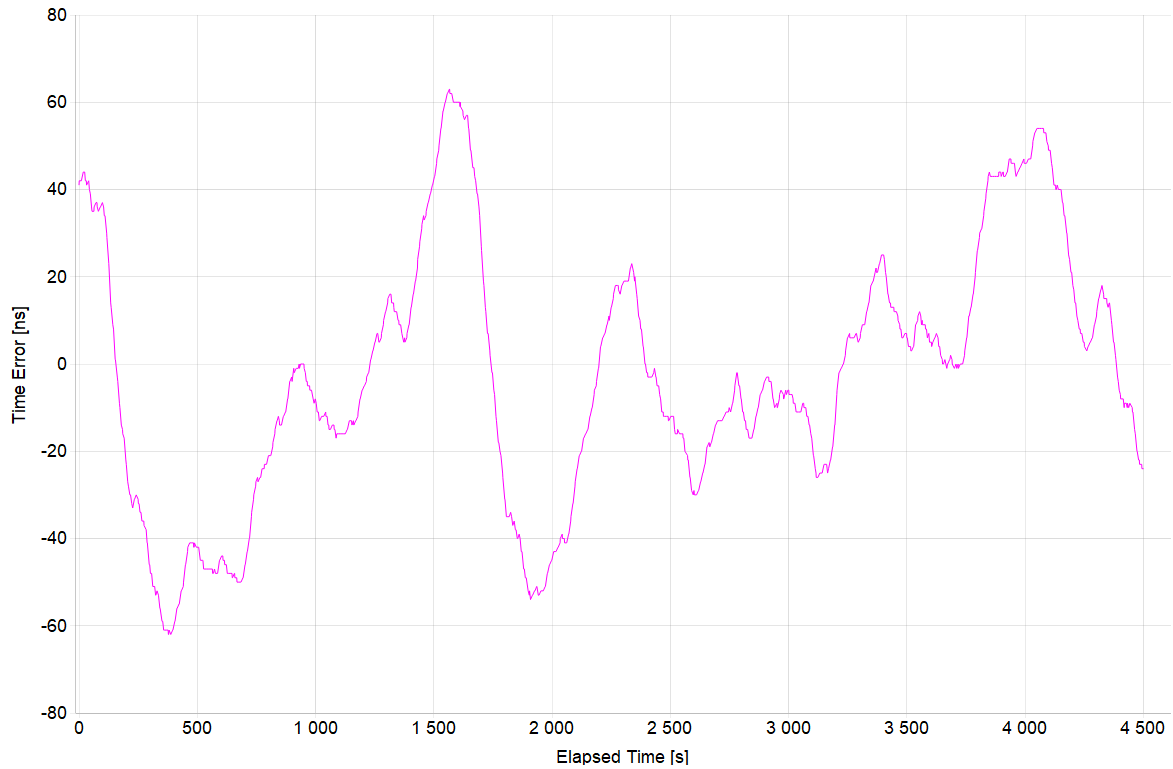
<b>Test Description</b>	Test Case 12
<b>Report Date</b>	22-04-27_17-14-34
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:15:01
<b>Test Configuration</b>	1
<b>Time to Phase Lock (s)</b>	452
<b>Floor Delay Estimate (ns)</b>	8780

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

1. Test Case 12 delay request PDV was used on the sync packets for this test. The Test Case 12 sync packet PDV has a Gaussian distribution that cannot be tracked in Single Path mode.

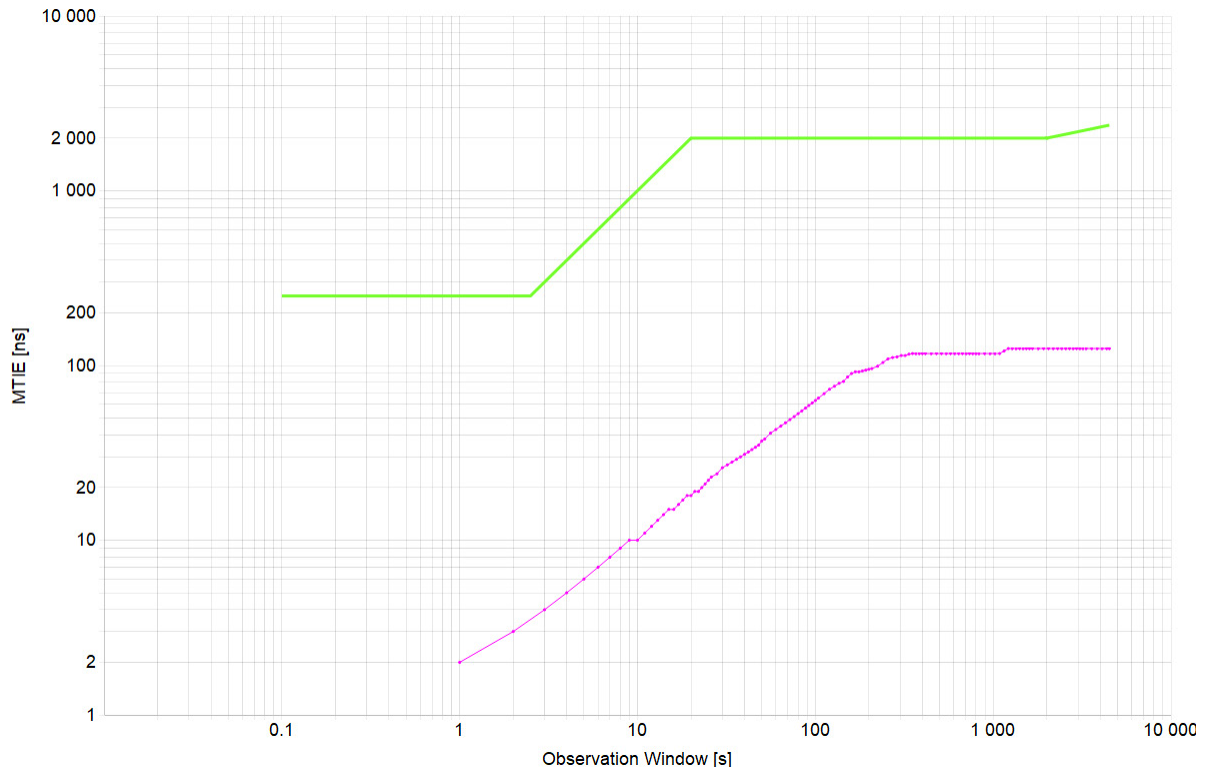
### 19.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	41ns



<b>Mean [ns]</b>	-2.588
<b>Min [ns]</b>	-62
<b>Max [ns]</b>	63
<b>Max-Min [ns]</b>	125

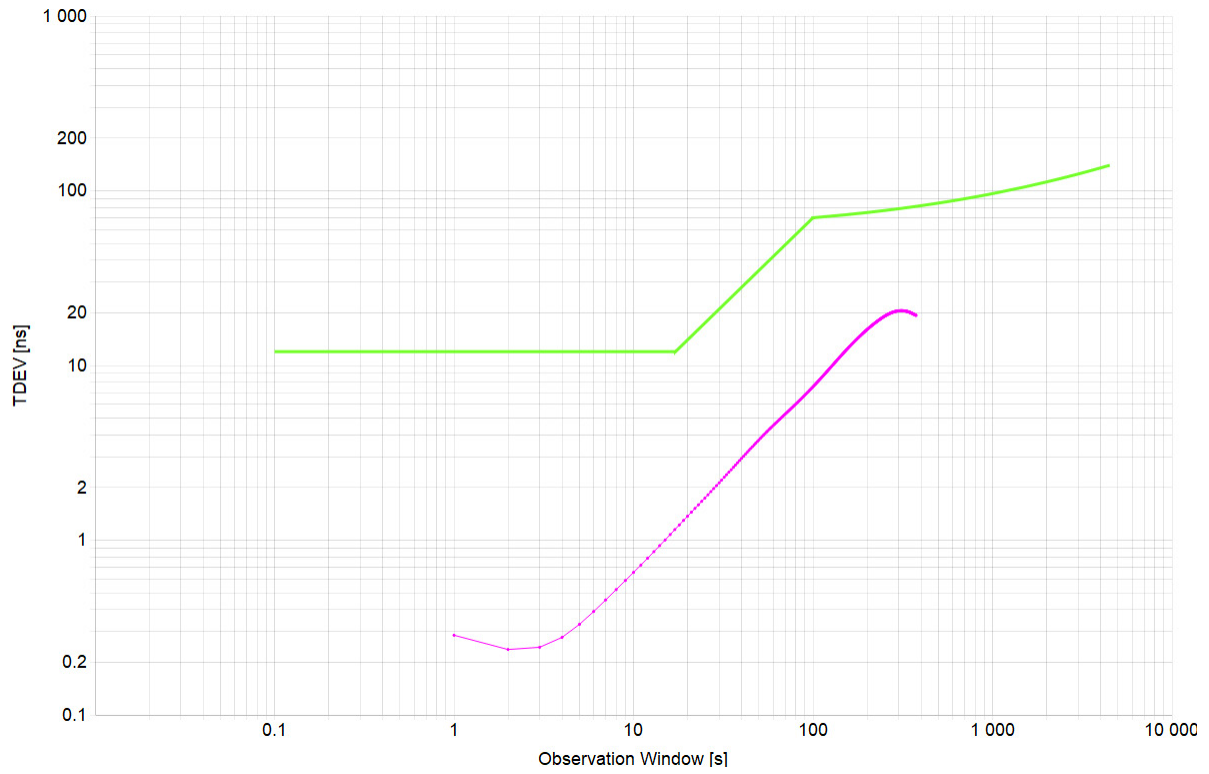
## 19.2 MTIE Analysis



<b>Min [ns]</b>	2
<b>Max [ns]</b>	125
<b>Max-Min [ns]</b>	123



### 19.3 TDEV Analysis



<b>Min [ns]</b>	0.237
<b>Max [ns]</b>	20.581
<b>Max-Min [ns]</b>	20.344

## 20. G.8261: Test Case 13b (SinglePath)

<b>Test Description</b>	Test Case 13 Network Traffic Model 2
<b>Report Date</b>	22-04-27_17-14-34
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	06:15:01
<b>Test Configuration</b>	2
<b>Time to Phase Lock (s)</b>	372
<b>Floor Delay Estimate (ns)</b>	6730

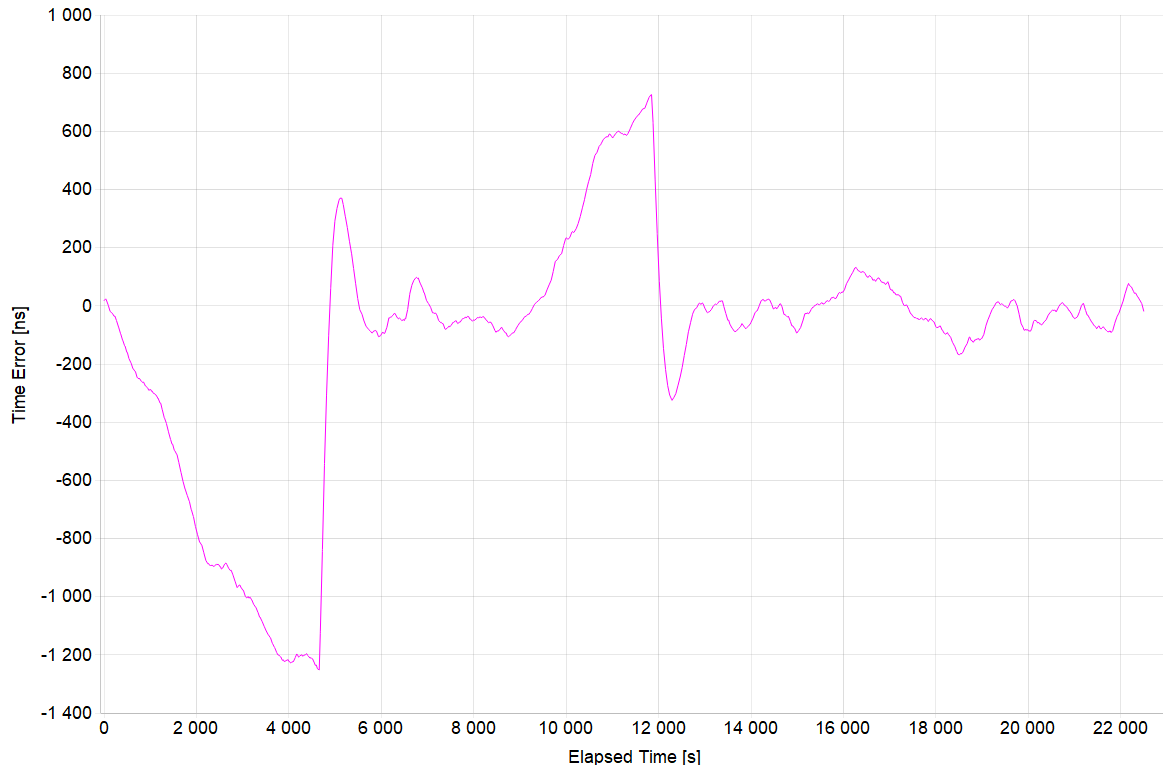
<b>All Mask Results</b>	<b>Fail</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Fail</b>

1. This test is expected to fail the G.8261 EEC Option 1 masks.

The PDV pattern does not have a solid noise floor during the first 3600s (the PDV follows a Gaussian distribution). When there is a solid noise floor after the first 3600s, the performance returns to normal. At 7200 the noise distribution returns to Gaussian and the change in performance can be seen in the Time Error plot. At 14400, the distribution is again Gaussian, but at this point the servo has had enough time to collect information on the PDV and maintain good performance throughout the Gaussian noise distribution portion from 14400 to 18000s.

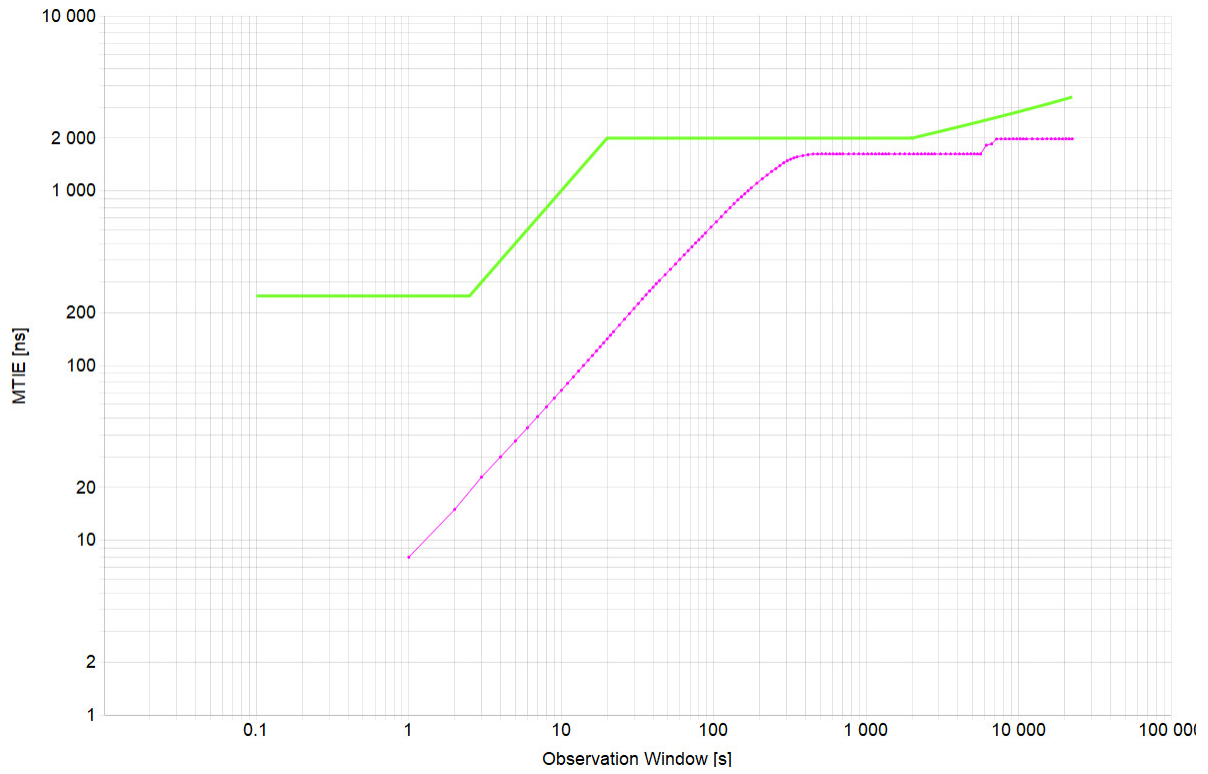
## 20.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	19ns



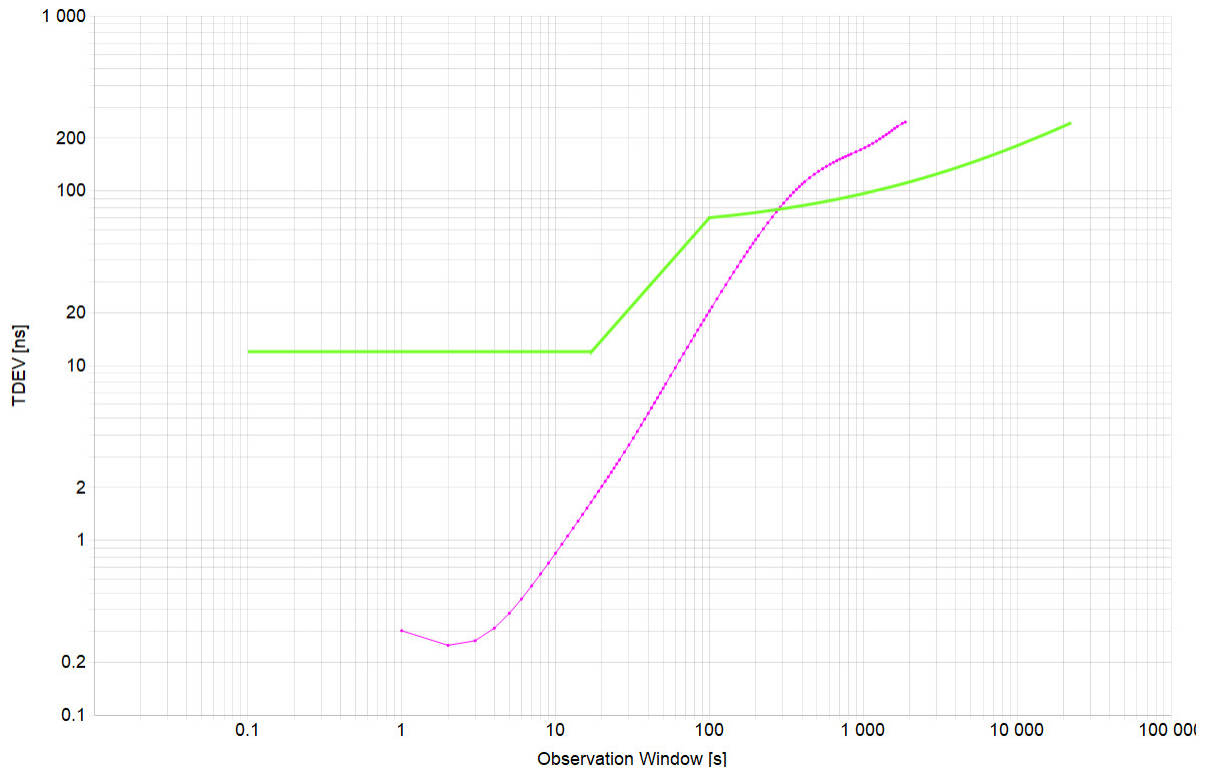
<b>Mean [ns]</b>	-125.643
<b>Min [ns]</b>	-1252
<b>Max [ns]</b>	726
<b>Max-Min [ns]</b>	1978

## 20.2 MTIE Analysis



<b>Min [ns]</b>	8
<b>Max [ns]</b>	1978
<b>Max-Min [ns]</b>	1970

### 20.3 TDEV Analysis



<b>Min [ns]</b>	0.25
<b>Max [ns]</b>	247.036
<b>Max-Min [ns]</b>	246.786

## 21. G.8261: Test Case 14b (SinglePath)

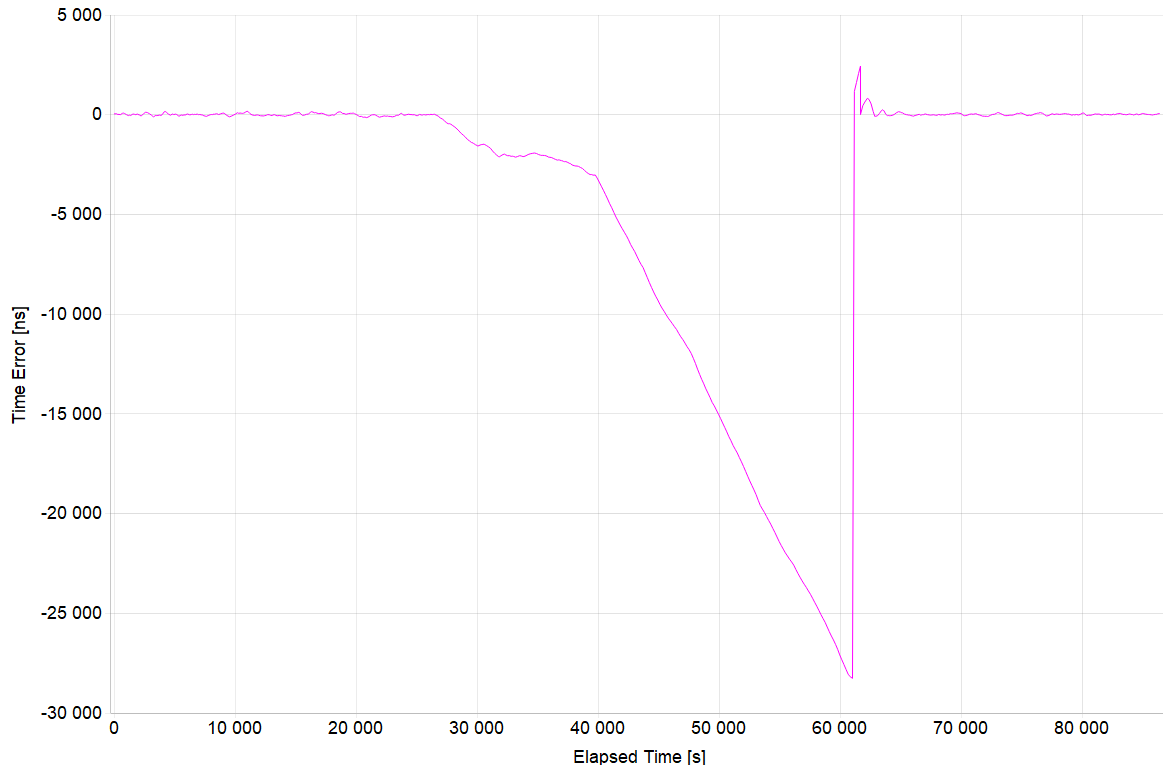
<b>Test Description</b>	Test Case 14 Network Traffic Model 2
<b>Report Date</b>	22-04-27_17-14-34
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	24:00:01
<b>Test Configuration</b>	2
<b>Time to Phase Lock (s)</b>	372
<b>Floor Delay Estimate (ns)</b>	6730

<b>All Mask Results</b>	<b>Fail</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Fail</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Fail</b>

1. This test is expected to fail the G.8261 EEC Option 1 masks.  
 In this PDV pattern, the noise floor gradually increases for 12 hours and then decreases for 12 hours. At Approximately 26000, lock is lost because of the change in PDV noise floor. When the PDV pattern noise floor decreases to the previous level at around 62000, time lock is restored.

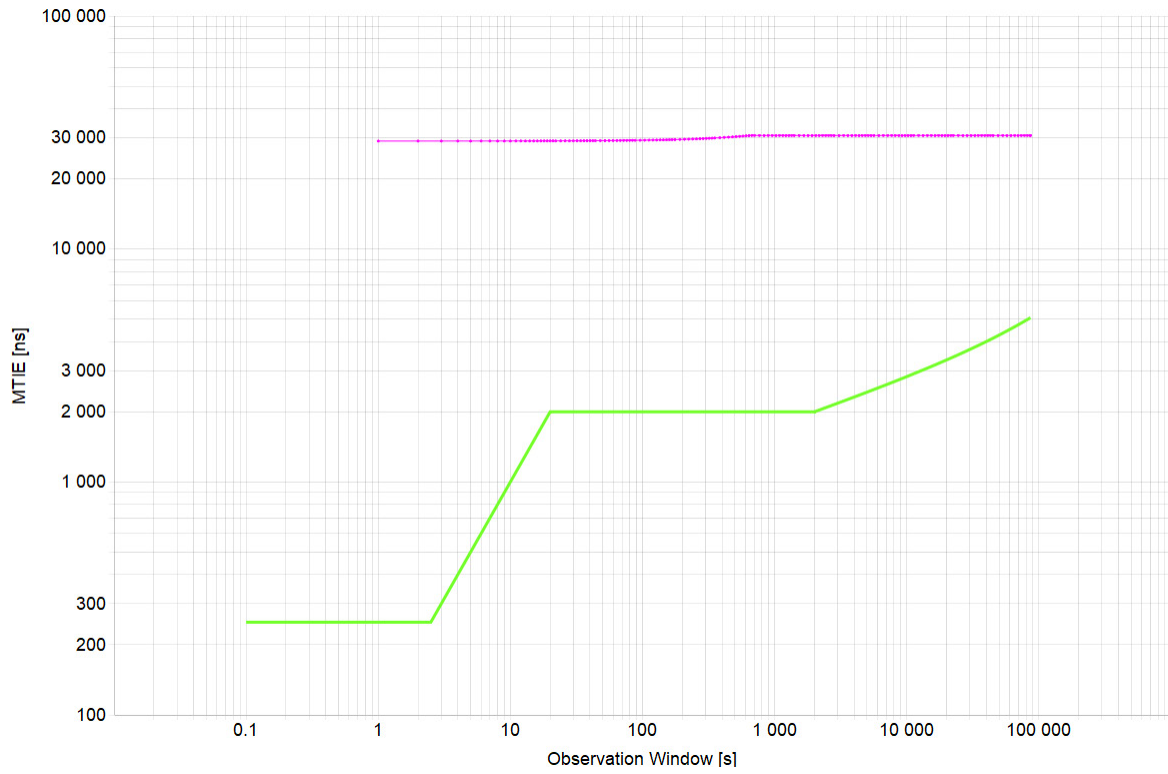
## 21.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	52ns



<b>Mean [ns]</b>	-4102.269
<b>Min [ns]</b>	-28266
<b>Max [ns]</b>	2442
<b>Max-Min [ns]</b>	30708

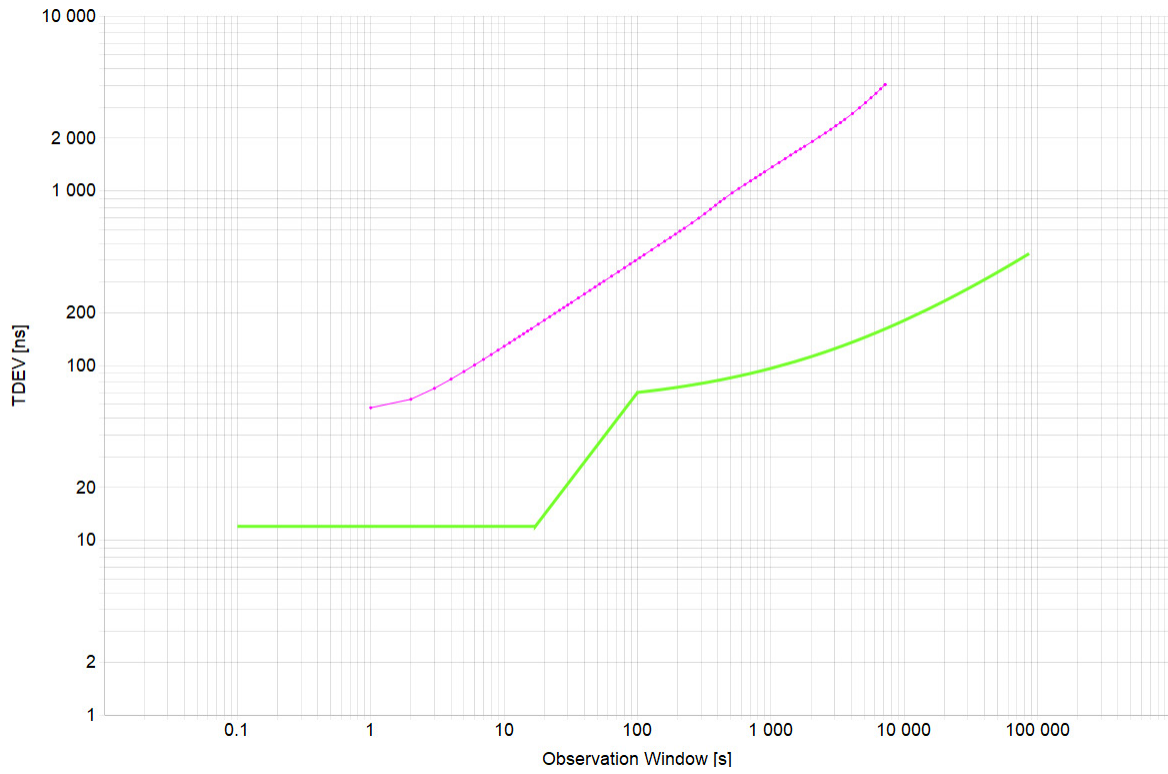
## 21.2 MTIE Analysis



<b>Min [ns]</b>	29056
<b>Max [ns]</b>	30708
<b>Max-Min [ns]</b>	1652



### 21.3 TDEV Analysis



<b>Min [ns]</b>	57.272
<b>Max [ns]</b>	4053.098
<b>Max-Min [ns]</b>	3995.826

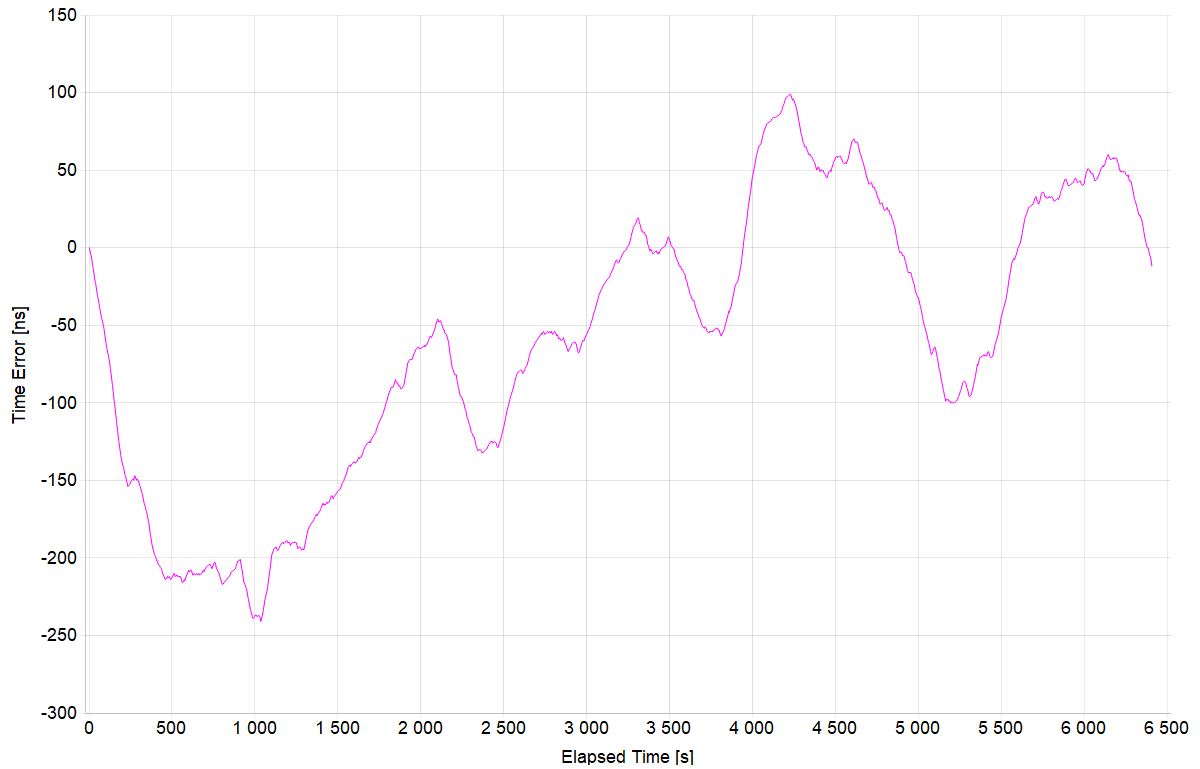
## 22. G.8261: Test Case 15b (SinglePath)

<b>Test Description</b>	Test Case 15 Network Traffic Model 2
<b>Report Date</b>	22-04-27_17-14-34
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:46:50
<b>Test Configuration</b>	2
<b>Time to Phase Lock (s)</b>	373
<b>Floor Delay Estimate (ns)</b>	6730

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

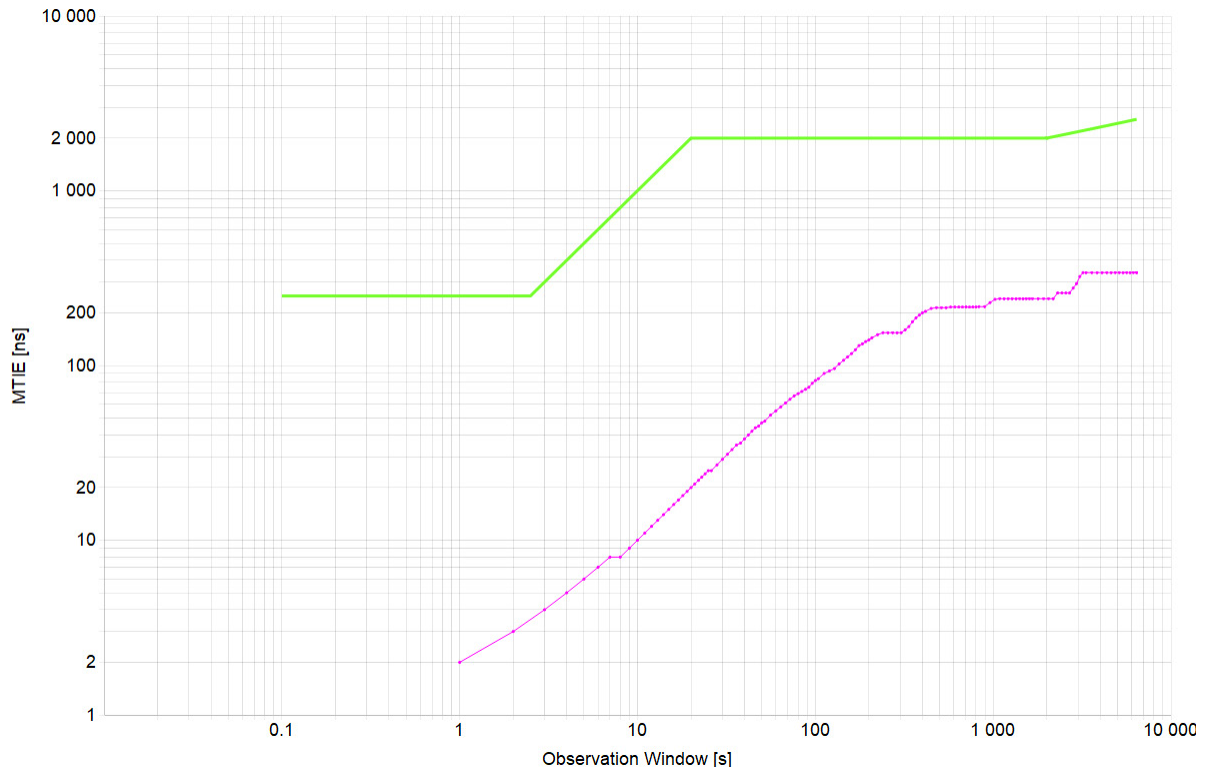
## 22.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	0ns



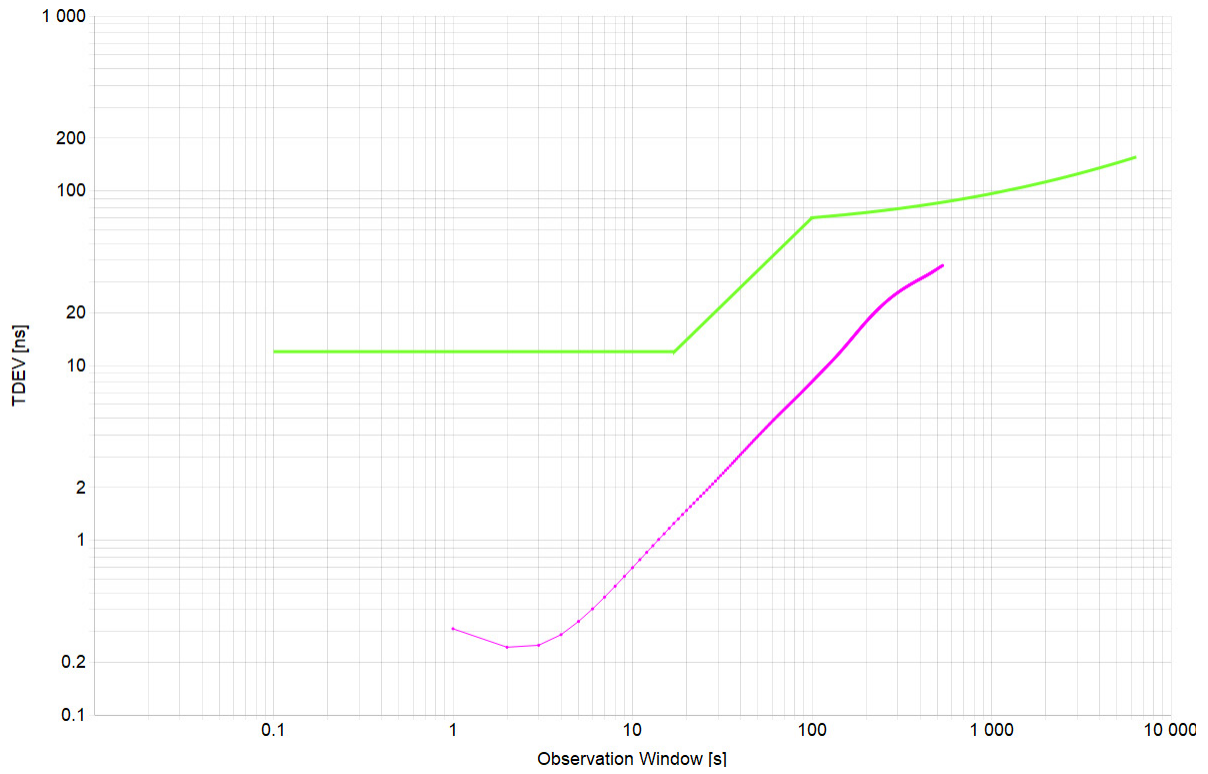
<b>Mean [ns]</b>	-59.645
<b>Min [ns]</b>	-241
<b>Max [ns]</b>	99
<b>Max-Min [ns]</b>	340

## 22.2 MTIE Analysis



<b>Min [ns]</b>	2
<b>Max [ns]</b>	340
<b>Max-Min [ns]</b>	338

## 22.3 TDEV Analysis



<b>Min [ns]</b>	0.244
<b>Max [ns]</b>	37.331
<b>Max-Min [ns]</b>	37.087

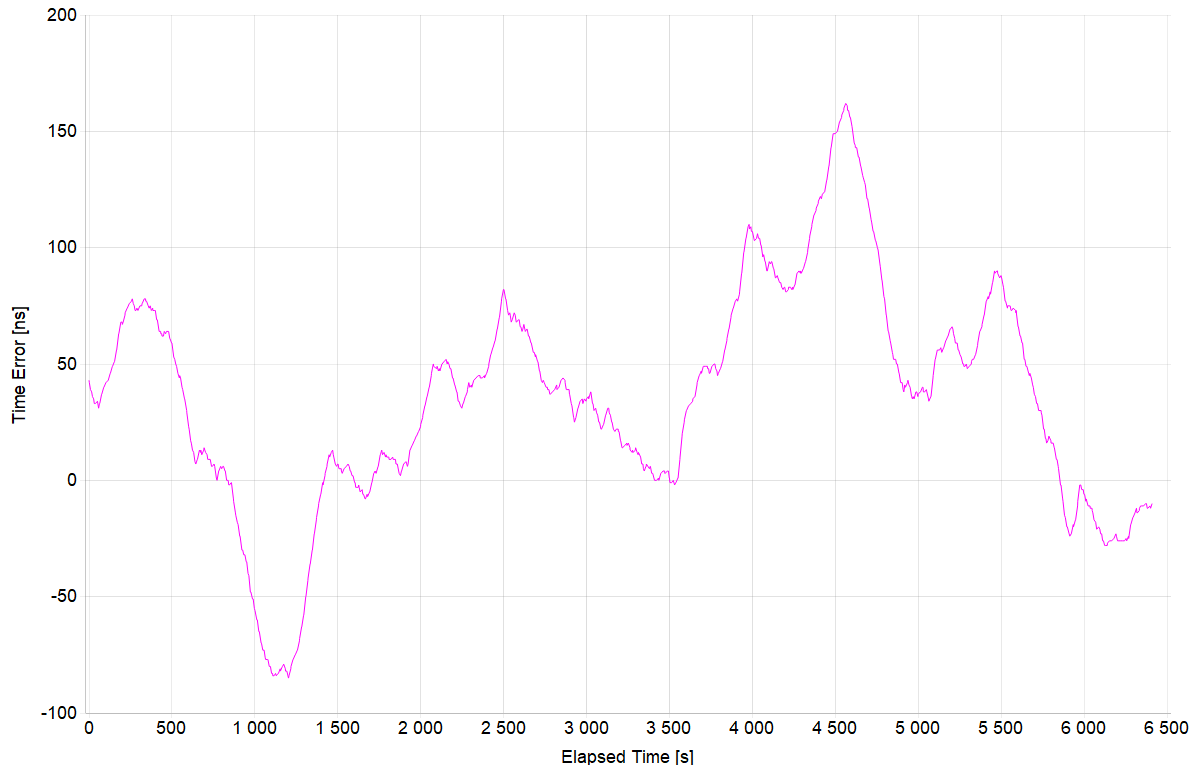
### 23. G.8261: Test Case 16b (SinglePath)

<b>Test Description</b>	Test Case 16 Network Traffic Model 2
<b>Report Date</b>	22-04-27_17-14-34
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:46:50
<b>Test Configuration</b>	2
<b>Time to Phase Lock (s)</b>	373
<b>Floor Delay Estimate (ns)</b>	6730

<b>All Mask Results</b>	<b>Pass</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Pass</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Pass</b>

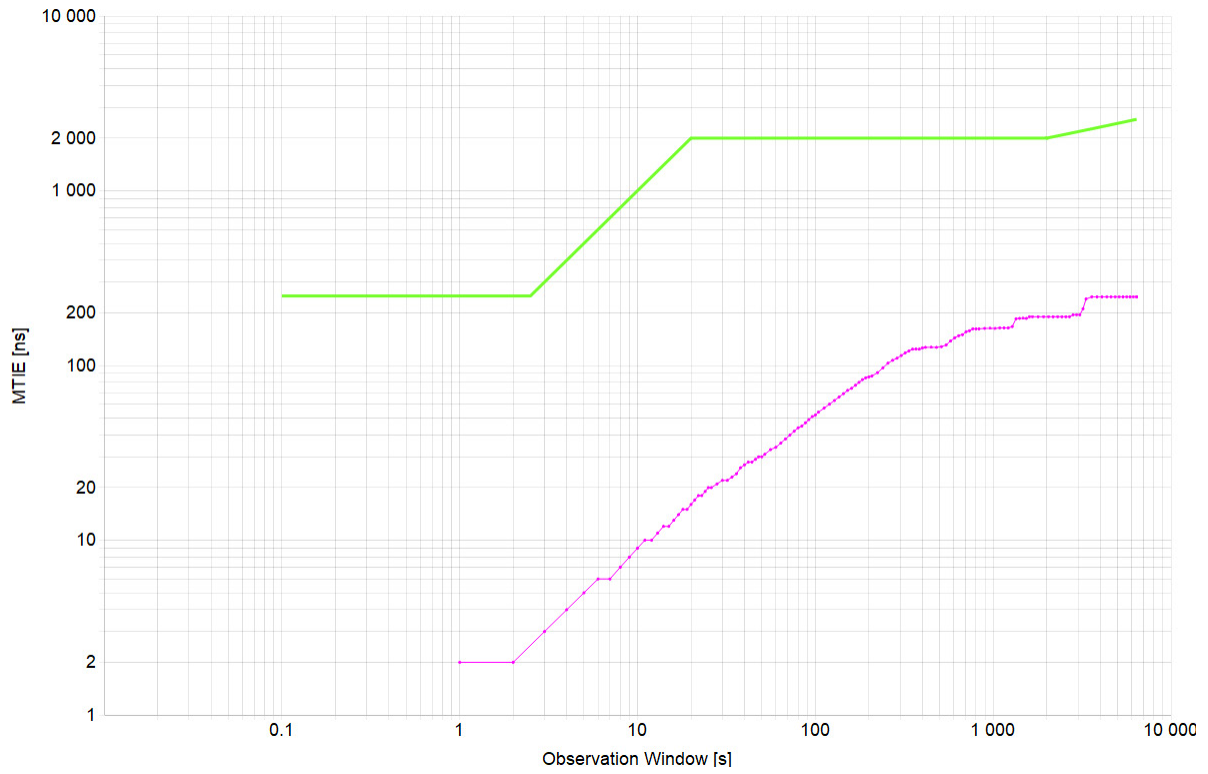
### 23.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	43ns



<b>Mean [ns]</b>	35.313
<b>Min [ns]</b>	-85
<b>Max [ns]</b>	162
<b>Max-Min [ns]</b>	247

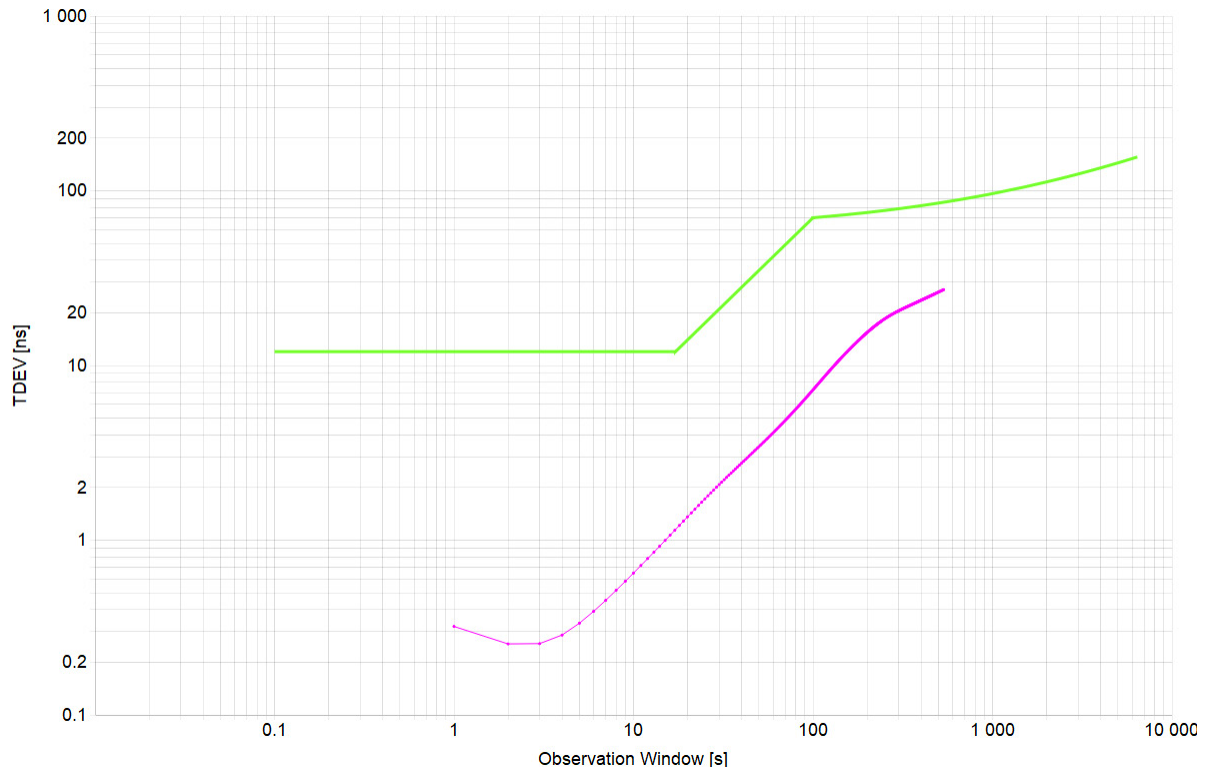
## 23.2 MTIE Analysis



<b>Min [ns]</b>	2
<b>Max [ns]</b>	247
<b>Max-Min [ns]</b>	245



### 23.3 TDEV Analysis



<b>Min [ns]</b>	0.255
<b>Max [ns]</b>	27.141
<b>Max-Min [ns]</b>	26.886

## 24. G.8261: Test Case 17b\_10us (SinglePath)

<b>Test Description</b>	Test Case17b (10 $\mu$ s) Network Traffic Model 2
<b>Report Date</b>	22-04-28_08-58-37
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:45:00
<b>Test Configuration</b>	2
<b>Time to Phase Lock (s)</b>	571
<b>Floor Delay Estimate (ns)</b>	6730

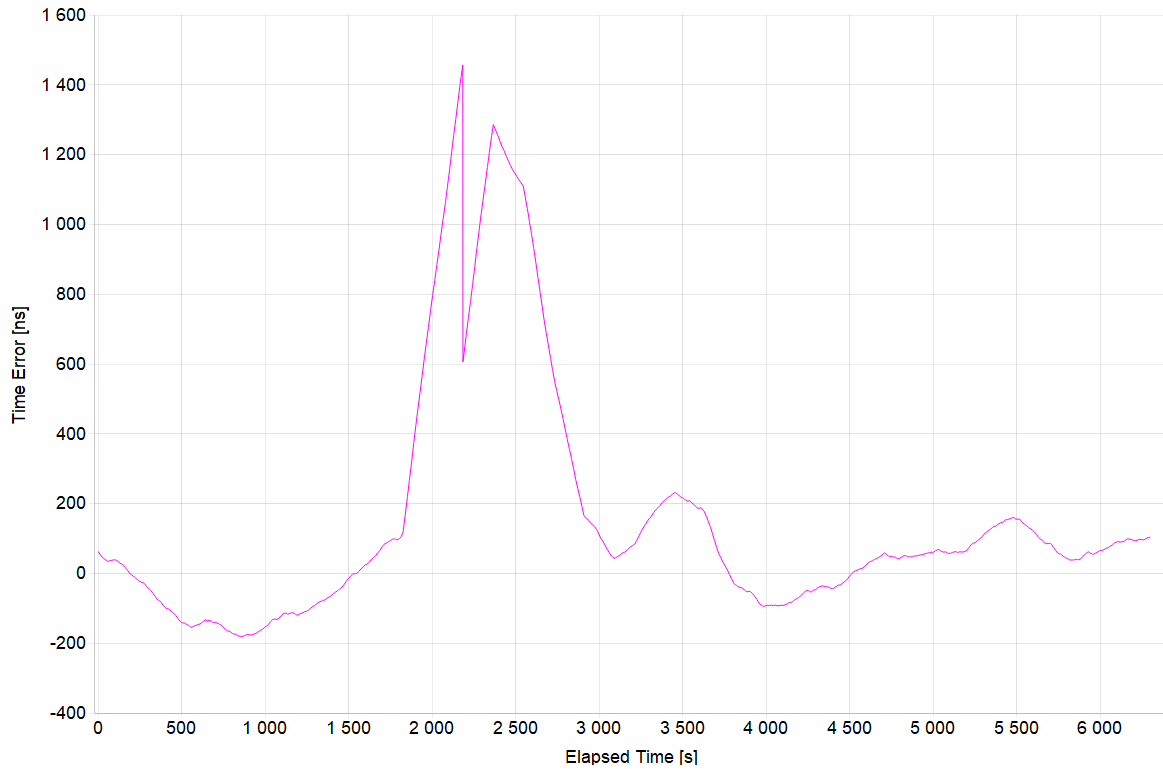
<b>All Mask Results</b>	<b>Fail</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Fail</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Fail</b>

1. This test is expected to fail the G.8261 EEC Option 1 masks.

In this PDV pattern there is a prolonged noise floor change of 10 $\mu$ s at 1800s. This causes a loss of lock. In single path mode, large changes in PDV floor cannot be tolerated. Once the noise floor returns to normal, time lock is restored.

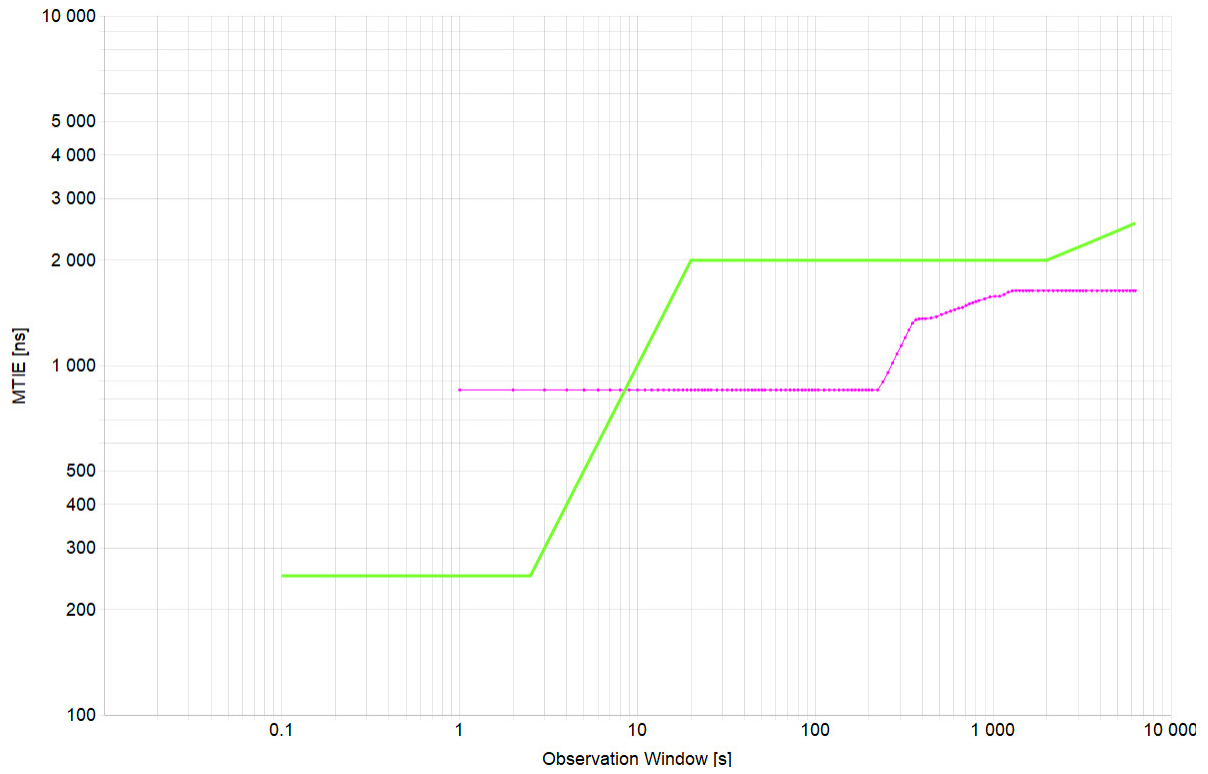
## 24.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	63ns



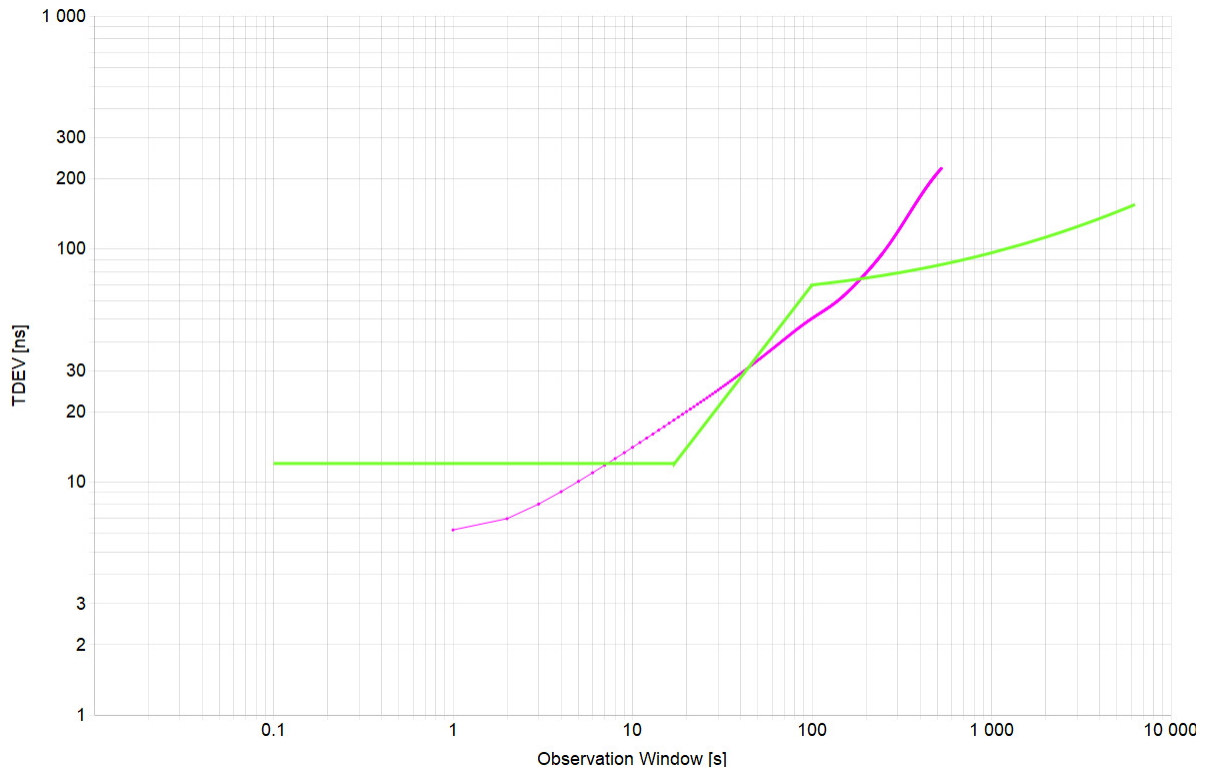
<b>Mean [ns]</b>	155.328
<b>Min [ns]</b>	-181
<b>Max [ns]</b>	1457
<b>Max-Min [ns]</b>	1638

## 24.2 MTIE Analysis



<b>Min [ns]</b>	851
<b>Max [ns]</b>	1638
<b>Max-Min [ns]</b>	787

### 24.3 TDEV Analysis



<b>Min [ns]</b>	6.225
<b>Max [ns]</b>	221.567
<b>Max-Min [ns]</b>	215.342

## 25. G.8261: Test Case 17b\_200us (SinglePath)

<b>Test Description</b>	Test Case17b (200µs) Network Traffic Model 2
<b>Report Date</b>	22-04-27_17-14-34
<b>Packet Rate (pkt/s)</b>	16
<b>Test Duration</b>	01:45:11
<b>Test Configuration</b>	1
<b>Time to Phase Lock (s)</b>	372
<b>Floor Delay Estimate (ns)</b>	8780

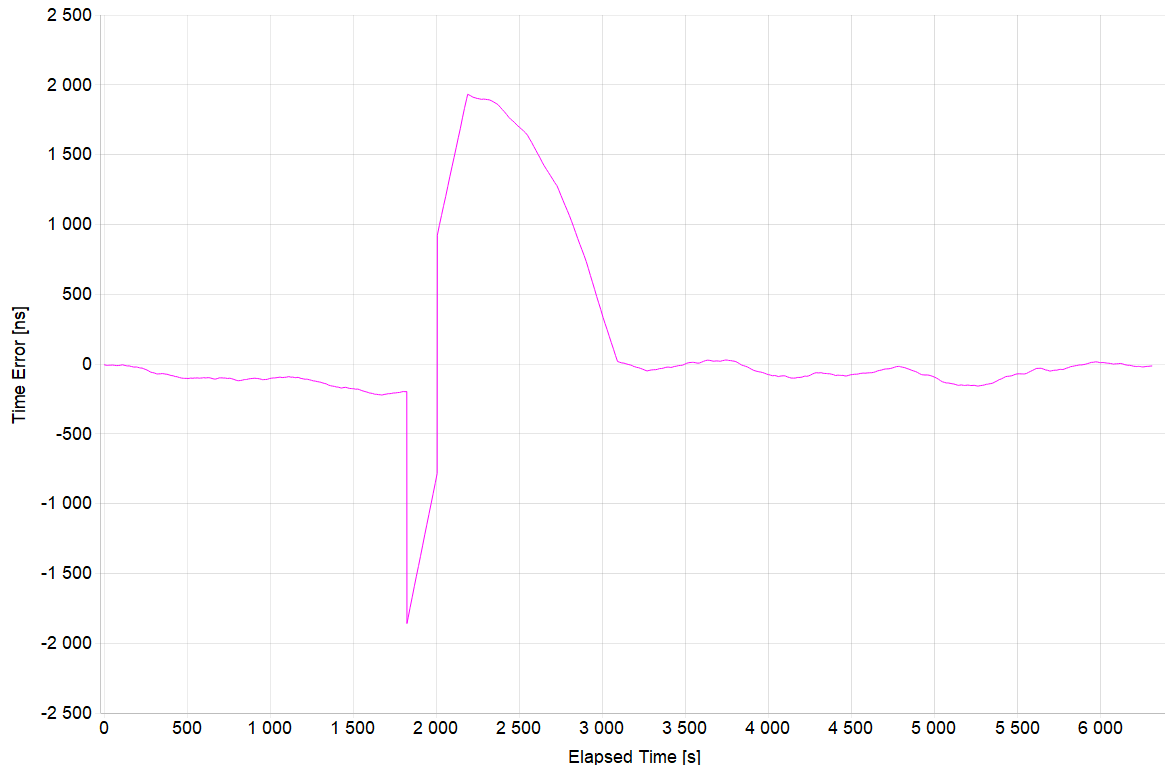
<b>All Mask Results</b>	<b>Fail</b>
<b>Mask ONEPPS</b>	-
<b>Mask ONEPPS Result</b>	No Mask
<b>Mask MTIE</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask MTIE Result</b>	<b>Fail</b>
<b>Mask TDEV</b>	G.8261 EEC Opt. 1 Wander Limit
<b>Mask TDEV Result</b>	<b>Fail</b>

1. This test is expected to fail the G.8261 EEC Option 1 masks.

In this PDV pattern there is a prolonged noise floor change of 200µs at 1800s. This causes a loss of lock. In single path mode, large changes in PDV floor cannot be tolerated. Once the noise floor returns to normal, time lock is restored.

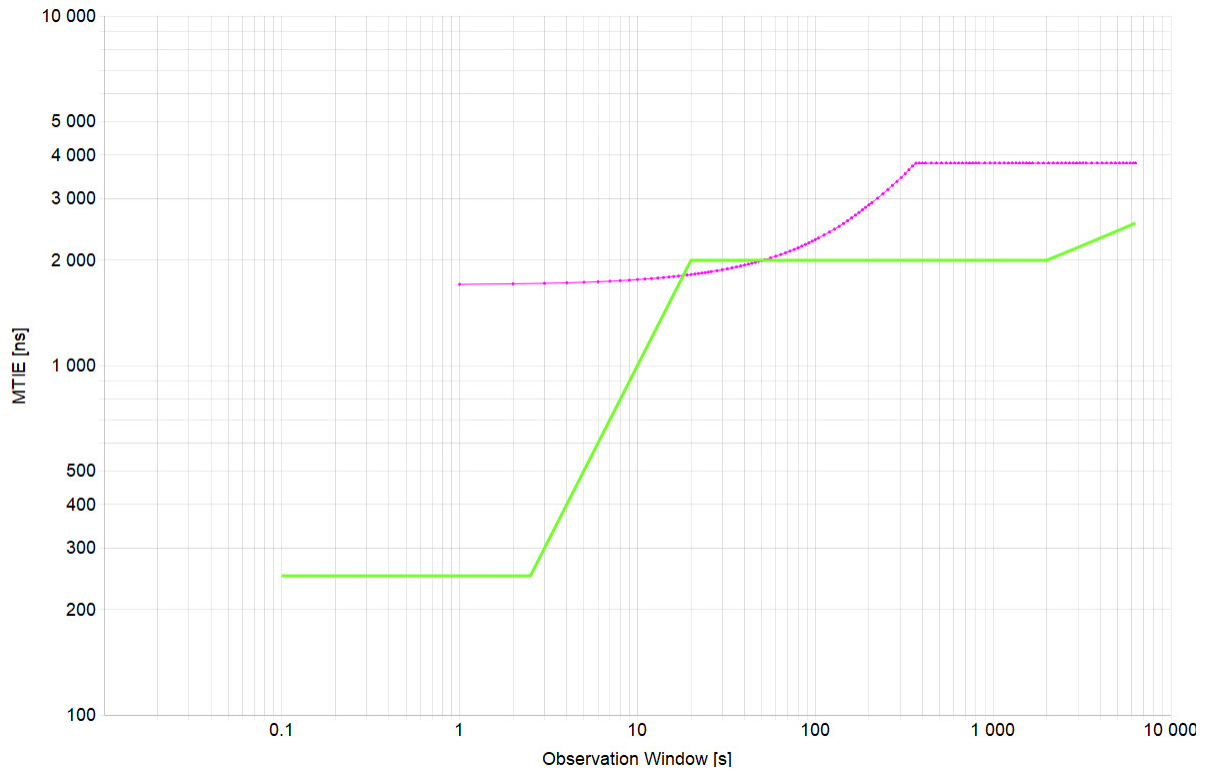
## 25.1 ONEPPS Analysis

<b>Offset Removal Applied</b>	Off
<b>Zero Offset</b>	-5.703ns



<b>Mean [ns]</b>	129.587
<b>Min [ns]</b>	-1859.203
<b>Max [ns]</b>	1932.297
<b>Max-Min [ns]</b>	3791.5

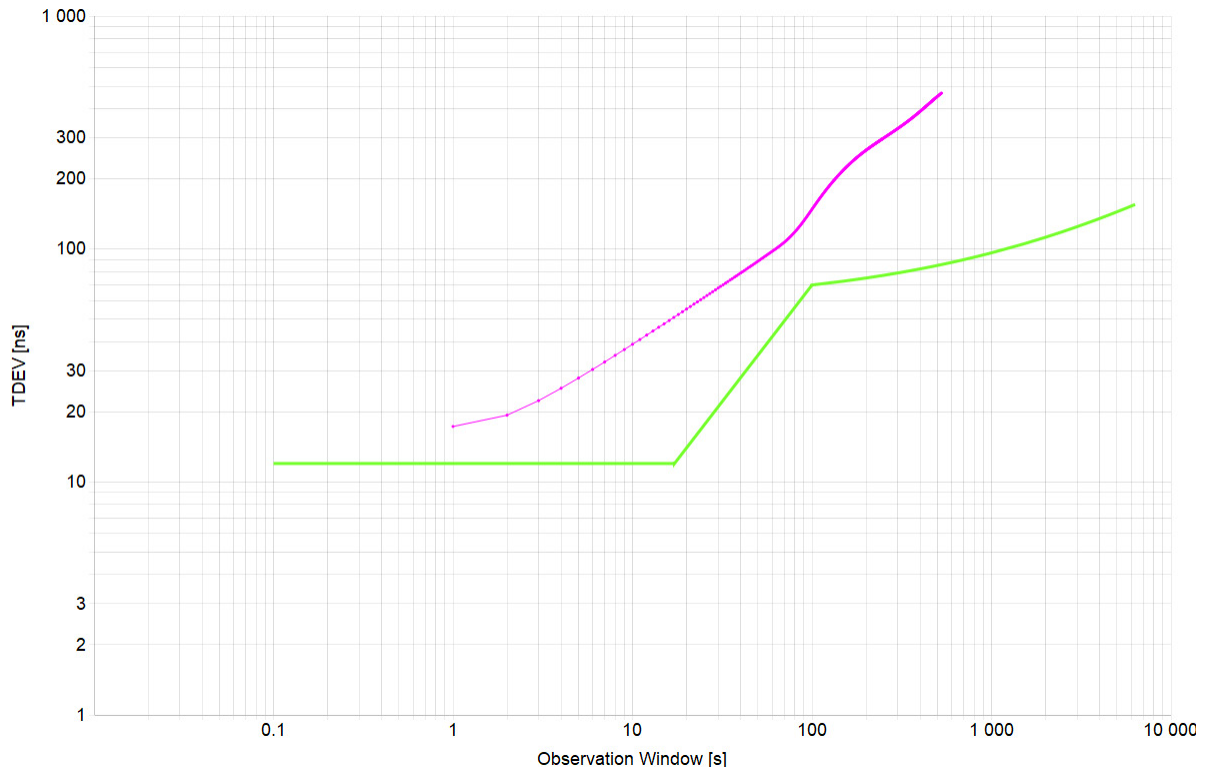
## 25.2 MTIE Analysis



<b>Min [ns]</b>	1706.25
<b>Max [ns]</b>	3791.5
<b>Max-Min [ns]</b>	2085.25



### 25.3 TDEV Analysis



<b>Min [ns]</b>	17.298
<b>Max [ns]</b>	466.594
<b>Max-Min [ns]</b>	449.296

## 26. Revision History

Revision	Date	Description
1.01	Jul 16, 2024	Replaced Xilinx with AMD throughout document.
1.00	May 3, 2023	Initial release.

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