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RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A082A/E	Rev.	1.00
Title	Usage Notes on Boot Mode		Information Category	Technical Notification		
Applicable Product	RX62T Group RX62G Group	Lot No. All lots	Reference Document	RX62T Group User's Manual: Hardware Rev. 1.30 (R01UH0034EJ0130) RX62G Group User's Manual: Hardware Rev. 1.00 (R01UH0321EJ0100)		

Thank you for your valued patronage and best wishes for your continued success in business.

We would like to inform you of some notes when the RX62T or RX62G Group product is used in boot mode.

1. Usage Notes

(1) When re-writing the flash memory in boot mode, the PD0 pin outputs a low-level signal after the boot mode is activated until it is ended.

(2) When re-writing the flash memory in boot mode, if the PD3/TXD1 pin is not pulled up, the pin is driven to the Hi-Z state during the following periods, and it may lead to erroneous detection of a start bit by the host.

- After the boot mode is activated until automatic adjustment of the bit rate is completed.
- Immediately after the response is returned to the new bit rate selection command.

2. Countermeasures

(1) Verify that no problems are caused by the PD0 pin outputting a low-level signal after the boot mode is activated until it is ended.

(2) Pull up the PD3/TXD1 pin when used in boot mode to the VCC via an appropriate resistor.

y˙ Permanent Measures

We are planning the revised version against the above restrictions.

Please contact Renesas sales office for more information on the revised version

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