

[Notes]

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Rev.1.00

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## CS+ and e<sup>2</sup> studio Integrated Development Environments

### (Note on the I/O Header File for the RX66N, RX72M and RX72N Groups)

#### Outline

When using the CS+ and e<sup>2</sup> studio IDEs, note the following point.

- Note on the I/O header file (iodefine.h) for the RX66N, RX72M and RX72N groups of the RX family

#### 1. Note on the I/O Header File (iodefine.h) for the RX66N, RX72M and RX72N Groups of the RX Family

##### 1.1 Applicable Products

CS+ for CC V8.04.00 and later versions

e<sup>2</sup> studio 2020-01 and later versions

##### 1.2 Applicable Devices

RX Family: RX66N, RX72M and RX72N groups

##### 1.3 Details

When a project for the RX66N group, RX72M group or RX72N group is to be generated by either of the applicable products, the generated I/O header file (iodefine.h) has the following incorrect statements.

- PSEL bit symbol of PF5PFS register in Multi-Function Pin Controller (MPC) is undefined.

Wrong	Right
<pre>union {     unsigned char BYTE;     struct {         unsigned char :1;         unsigned char ISEL:1;         unsigned char :6;     } BIT; } PF5PFS;</pre>	<pre>union {     unsigned char BYTE;     struct {         unsigned char :1;         unsigned char ISEL:1;         unsigned char PSEL:6;     } BIT; } PF5PFS;</pre>

##### 1.4 Conditions

This error will occur when a project for the RX66N group, RX72M group or RX72N group is generated by either of the applicable products.

## 1.5 Workaround

Refer to the description in section 1.3 and rewrite the iodef.h file accordingly. Otherwise, download the relevant file from the Web page for the [CS+](#) or [e<sup>2</sup> studio](#) IDE and substitute the iodef.h file which is generated after the applicable file listed below has been unzipped for the file having the same name in the project that is in use.

There are two types of file, one for CC-RX and the other for GCC.

- rx66n\_iodef.h\_CCRX\_GCCRX\_v1.00d.zip for RX66N  
rx66n\_iodef.h\_CCRX\_v1.00d.h for CC-RX  
rx66n\_iodef.h\_GCCRX\_v1.00d.h for GCC
- rx72m\_iodef.h\_CCRX\_GCCRX\_v1.00d.zip for RX72M  
rx72m\_iodef.h\_CCRX\_v1.00d.h for CC-RX  
rx72m\_iodef.h\_GCCRX\_v1.00d.h for GCC
- rx72n\_iodef.h\_CCRX\_GCCRX\_v1.00d.zip for RX72N  
rx72n\_iodef.h\_CCRX\_v1.00d.h for CC-RX  
rx72n\_iodef.h\_GCCRX\_v1.00d.h for GCC

If you are using the Renesas Board Support Package (BSP), refer to the application notes below for IO header file location.

[RX Family Board Support Package Module Using Firmware Integration Technology](#)

## 1.6 Schedule for Fixing the Problem

It is scheduled to be replaced in the version upgrade in April 2025.

**Revision History**

Rev.	Date	Description	
		Page	Summary
1.00	Nov.20.24	-	First edition issued

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