

[Notes]

e² studio Smart Configurator Plug-in

Outline

When using the e² studio Smart Configurator Plug-in, note the following point:

- 1. Using compare match timer W interrupt

1. Using Compare Match Timer W Interrupt

1.1 Applicable Products

- e² studio V5.2.0.20 (Smart Configurator Plug-in V.1.0.0)

1.2 Applicable MCUs

- RX Family: RX64M Group

1.3 Details

Even when compare match interrupt is allowed in the compare match timer W component settings, if input capture and output compare pins are not used, a code to enable compare match interrupt is not output and compare match interrupt does not work.

1.4 Condition

This problem occurs when [Enable compare match interrupt] is selected and all of the check boxes of the following pins are not selected in the compare match timer W component settings.

- When using CMTW0 unit: TIC0 pin, TIC1 pin, TOC0 pin, TOC1 pin
- When using CMTW1 unit: TIC2 pin, TIC3 pin, TOC2 pin, TOC3 pin

When using CMTW0 unit

Compare match setting	
Interval value	100
Register value (CMWCOR)	749
<input checked="" type="checkbox"/> Enable compare match interrupt (CMWI0)	Checked
Priority	Level 15 (highest)
Input capture setting	
<input type="checkbox"/> TIC0 pin	Unchecked
Input capture control 0	Rising edge
<input type="checkbox"/> TIC1 pin	Unchecked
Input capture control 1	Rising edge
<input checked="" type="checkbox"/> Enable input capture interrupt (IC0I0)	
Priority	Level 15 (highest)
<input checked="" type="checkbox"/> Enable input capture interrupt (IC1I0)	
Priority	Level 15 (highest)
Output compare setting	
<input type="checkbox"/> TOC0 pin	Unchecked
Output compare control 0	Retains the output value
Compare value 0	10
<input type="checkbox"/> TOC1 pin	Unchecked
Output compare control 1	Retains the output value
Compare value 1	10

When using CMTW1 unit

Compare match setting	
Interval value	100
Register value (CMWCOR)	749
<input checked="" type="checkbox"/> Enable compare match interrupt (CMWI1)	Checked
Priority	Level 15 (highest)
Input capture setting	
<input type="checkbox"/> TIC2 pin	Unchecked
Input capture control 0	Rising edge
<input type="checkbox"/> TIC3 pin	Unchecked
Input capture control 1	Rising edge
<input checked="" type="checkbox"/> Enable input capture interrupt (IC0I1)	
Priority	Level 15 (highest)
<input checked="" type="checkbox"/> Enable input capture interrupt (IC1I1)	
Priority	Level 15 (highest)
Output compare setting	
<input type="checkbox"/> TOC2 pin	Unchecked
Output compare control 0	Retains the output value
Compare value 0	10
<input type="checkbox"/> TOC3 pin	Unchecked
Output compare control 1	Retains the output value
Compare value 1	10

Figure 1 Compare Match Setting Menu on the Occurrence Condition

1.5 Workaround

Add a code to enable compare match interrupt as follows:

- Where to add

After the R_<CMTW-configuration-name>_Create function is called, and before the R_<CMTW-configuration-name>_Start function is called

- Code to be added

When using CMTW0 unit

```
CMTW0.CMWIOR.BIT.CMWE = 1;
```

When using CMTW1 unit

```
CMTW1.CMWIOR.BIT.CMWE = 1;
```

- Example of adding the code

When using the CMTW0 unit and the CMTW configuration name is Config_CMTW0

```
R_Config_CMTW0_Create();
CMTW0.CMWIOR.BIT.CMWE = 1;
R_Config_CMTW0_Start();
```

1.6 Schedule for Fixing the Problem

This problem will be fixed in a later version.

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Dec. 01, 2016	-	First edition issued

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061 Japan
 Renesas Electronics Corporation

■Inquiry

<http://www.renesas.com/contact/>

Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.

The past news contents have been based on information at the time of publication.

Now changed or invalid information may be included. The URLs in the Tool News also may be subject to change or become invalid without prior notice.

All trademarks and registered trademarks are the property of their respective owners.