

[Upgrade to Revision]

R20TS1072EJ0100

Rev.1.00

Programming GUI for PG-FP6 Flash Memory Programmer

Oct. 20, 2024

FP6 Terminal V1.14.00

Outline

FP6 Terminal, a programming GUI for PG-FP6 Flash Memory Programmer, has been updated from V1.13.00 to V1.14.00. For an overview of PG-FP6, refer to the link below.

<https://www.renesas.com/pg-fp6>

Connect the updated FP6 Terminal to PG-FP6 to update the firmware of PG-FP6.

1. Description

The following sections cover the main enhancements. For details, see the release note at the link below.

(The note is applicable as of October 22.)

<https://www.renesas.com/software-tool/pg-fp6#document>

1.1 Addition of Supported Devices

- RH850 Family

Group	Part number
RH850/U2B-FCC	R7F702Z23, R7F702Z28

1.2 Improved Functionality and Change

- Increasing the speed of reading memory from the target device

The speed of reading memory from the target device has now been improved.

- Clarified error message during selection of an SWD interface

An error message which is output when setting files that the firmware for the PG-FP6 does not support are downloaded to the PG-FP6 main unit has now been improved.

- Changed display format of the boundary data output by the gos command

Applies to: RA4E1, RA4M2, RA4M3, RA6E1, RA6M4, RA6M5, RA6T2, RA8D1, RA8M1, RA8T1

The format of the boundary data output by the gos command has now been changed.

Before the Change	After the Change
Code Flash Secure	Code Secure Size
Code Flash NSC	Code Non-secure callable Size
Data Flash Secure	Data Secure Size
SRAM Secure	SRAM Secure Size
SRAM NSC	SRAM Non-secure callable Size

2. Updating the Product

FP6 Terminal can be updated free of charge. Download the installer from the link below.

(The installer is available as of October 22.)

<https://www.renesas.com/software-tool/pg-fp6#download>

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Oct.20.24	-	First edition issued

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