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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

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HD29413

Quadruple Differential Line Receivers With 3 State Outputs

REJ03D0306–0200Z (Previous ADE-205-582 (Z)) Rev.2.00 Jul.16.2004

2.0

Description

The HD29413 features quadruple differential line receivers designed to meet the spec of EIA RS-422AandRS-423A. The device operates from a single 5 V power supply. The enable function is common to all four receivers and offer a choice of active high or active low inputs. (Complementary output enable input.) Faile safe circuit guarantees the outputs always at the high level when the inputs are open.

Features

• Ordering Information

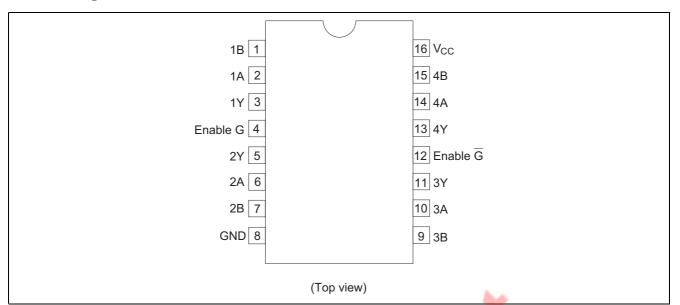
Part Name	Package Type	Package Code	Package Abbrevia <mark>ti</mark> on	Taping Abbreviation (Quantity)
HD29413P	DILP-16 pin	DP-16E, -16FV	Р	_

Logic Diaglam

Rev.2.00, Jul.16.2004, page 1 of 7



Pin Arrangement



Function Table

Differential Input	Enable		Output
$V_{IA} - V_{IB}$	G	G	Y
+	Н	x	Н
	Х		Н
_	Н	X	L
—	X	L L	L
Х	L	Н	Z

H : High level

L : Low level

X : Irrelevant

Z : High impedance

Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply Voltage	V _{CC} * ¹	+7	V
In Phase Input Voltage	V _{IC} * ²	–25 to +25	V
Differential Input Voltage	V _{ID} * ³	0 to +25	V
Enable Input Voltage	V _{IN}	+7	V
Output Sink Current	I _o	+50	mA
Operating Temperature	Topr	0 to +70	°C
Storage Temperature	Tstg	–65 to +150	°C

Notes: 1. All voltage values except for differential input voltage are with respect to ground terminal.

2. $V_{IC} = 1/2 (V_{IA} + V_{IB}) |V_{ID}| = |V_{IA} - V_{IB}|$

- 3. Differential input voltage is measured at the noninverting input with respect to the corresponding inverting input.
- 4. The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.



Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply Voltage	V _{cc}	4.75	5.0	5.25	V
In Phase Input Voltage	V _{IC}	-7	—	+7	V
Differential Input Voltage	V _{ID}	+0.3	—	+6.0	V
Output Current	I _{он}	—	—	-440	μA
	I _{OL}	—	—	8	mA
Operating Temperature	Topr	0	_	70	С

Electrical Characteristics (Ta = 0 to + 70°C)

ltem	Symbol	Min	Typ*1	Max	Unit	Conditions		
Differential Input High	V _{TH}	—	_	0.3	V	V _{cc} = 5 V ±5 %,	V _{OH} ≥ 2.7 V, I _{OH} =	-440 μA
Threshold Voltage						$V_{IC} = -7 \text{ to } +7 \text{ V}$		
Differential Input Low	V _{TL}	—	—	-0.3	V		V _{OL} ≤ 0.4 V, I _{OL} =	4 mA
Threshold Voltage								
Enable Input Voltage	V _{IH}	2.0	_	—	V	-		
	V _{IL}		—	0.8	V			
Enable Input Clamp	V _{IK}	—	—	-1.5	V	$V_{\rm CC}$ = 4.75 V, I _{IN} :	= –18 mA	
Voltage								-
Output Voltage	V _{OH}	2.7		_	V	V _{CC} = 4.75 V	∕ _{ID} = 0.3 to 6 V	I _{OH} = -440 μA
	V _{OL}	—	—	0.4	V	$V_{IL}(\overline{G}) = 0.8 V$	$V_{\rm ID} = -0.3 \text{ to } -6 \text{ V}$	I _{OL} = 4mA
		—		0.45	V	V _{IH} (G) = 2 V		I _{OL} = 8 mA
Off State (High	I _{oz}	—	_	20	μA	V _{cc} = 5.25 V		V _o = 2.4 V
impedance) Output		—	—	-20	μA	V _{IL} (G) <mark>= 0.8 V,</mark> V	/ _⊮ (G) = 2 V	V _o = 0.4 V
Current								
Line Input Current	I _{IN}	—	_	2.2	mA	V _{cc} = 5.25 V or \	$V_{\rm CC} = 0 V$	V ₁ = -10 V
		0	—	1.0	mA			V ₁ = 3 V
		0	—	-1.0	mA			$V_{1} = -3 V$
		—	—	-2.2	mA			V ₁ = -10 V
Enable Input Current	I _{I(EN)}	—	—	100	μA	V _{cc} = 5.25 V		V ₁ = 5.5 V
	I _{IH}	- (20	μA			V ₁ = 2.7 V
	I _{IL}	_	_	<mark>-0.3</mark> 6	mA]		V ₁ = 0.4 V
Short Circuit Output	I _{OS} * ²	-15		-85	mA	V _{cc} = 5.25 V, V _o	= 0 V	
Current								
Supply Current	I _{cc}		4	70	mA	V _{cc} = 5.25 V, V _I :	= 0 V (All Output D)isable)

Notes: 1. All typical values are at V_{CC} = 5 V, Ta = 25°C, V_{IC} = 0

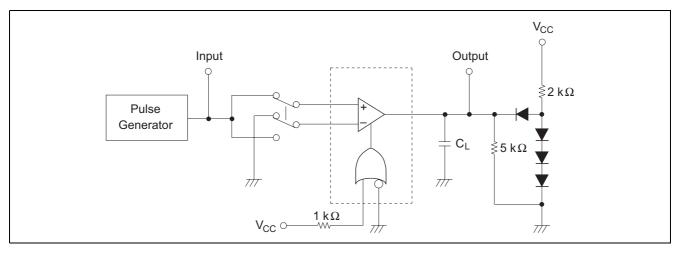
2. Not more than one output should be shorted at a time.

Switching Characteristics (V $_{\rm CC}$ = 5 V, Ta = 25°C)

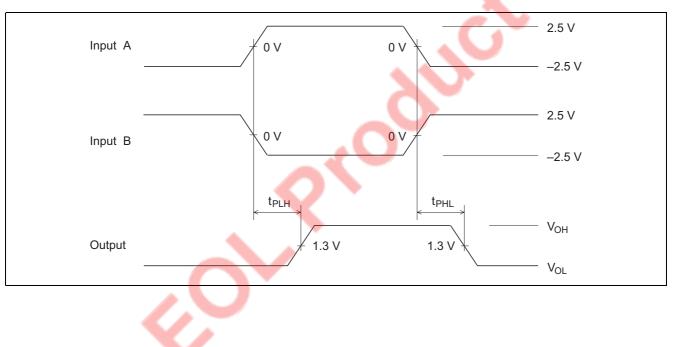
Item	Symbol	Min	Тур	Max	Unit	Conditions
Propagation Delay Time	t _{PLH} , t _{PHL}	_	17	25	ns	C _L = 15 pF
Output Enable Time	t _{zH} , t _{zL}	—	15	22	ns	
Output Disable Time	t _{HZ}	—	15	22	ns	C _L = 5 pF
	t _{LZ}	—	20	30	ns	

1. t_{PLH}, t_{PHL}

Test Circuit



Waveforms

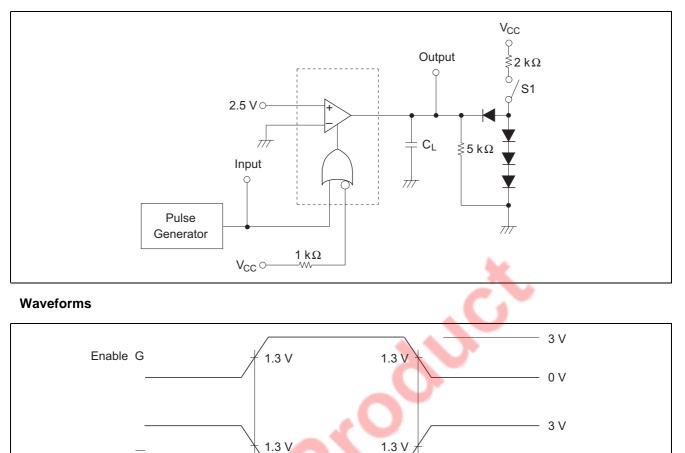




HD29413

 $2. t_{\text{HZ}}, t_{\text{ZH}}$

Test Circuit



S1 : Open t_{ZH}

1.3 V

- 0 V

V_{OH}

pprox 1.5 V 0 V

1.5 V

S1 : Closed

Enable G

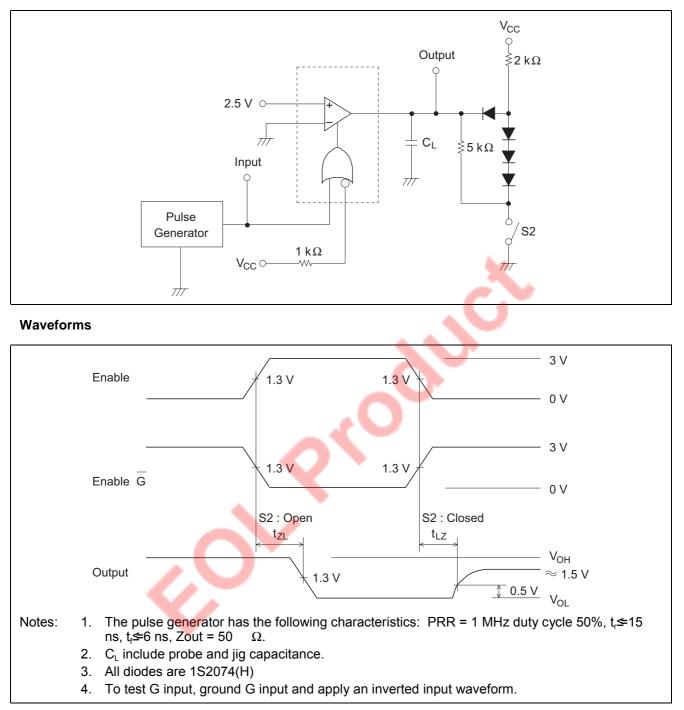
Output



HD29413

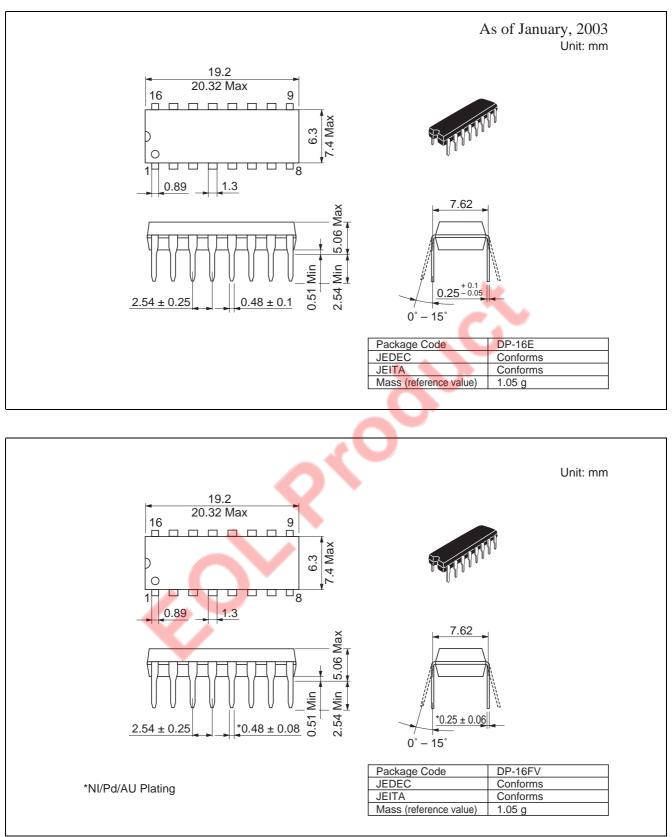
3. t_{LZ}, t_{ZL}

Test Circuit





Package Dimensions



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