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# HD74AC280 9-bit Parity Generator/Checker

REJ03D0266–0200Z (Previous ADE-205-387 (Z)) Rev.2.00 Jul.16.2004

# Description

The HD74AC280 is a high-speed parity generator/checker that accepts nine bits of input data and detects whether an even or an odd number of these inputs is High. If an even number of inputs is High, the Sum Even output is High. If an odd number is High, the Sum Even output is Low. The Sum Odd output is the complement of the Sum Even output.

### Features

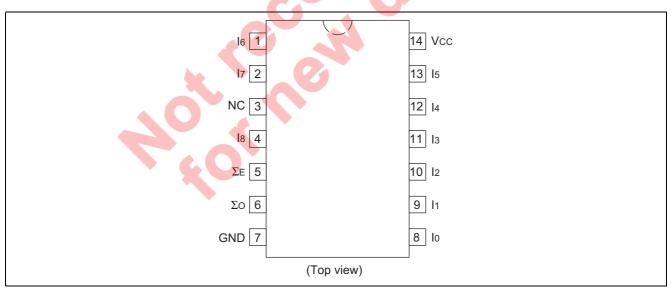
- Outputs Source/Sink 24 mA
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74AC280FPEL	SOP-14 pin (JEITA)	FP-14DAV	FP	EL (2,000 pcs/reel)
HD74AC280RPEL	SOP-14 pin (JEDEC)	FP-14DNV	RP	EL (2,500 pcs/reel)

Notes: 1. Please consult the sales office for the above package availability.

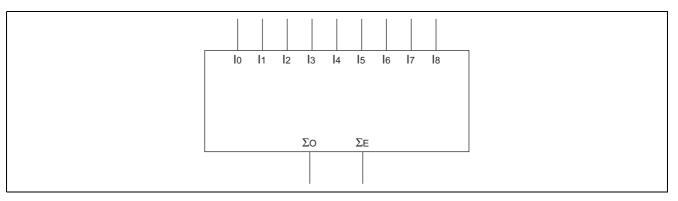
2. The packages with lead-free pins are distinguished from the conventional products by adding V at the end of the package code.

# **Pin Arrangement**





# Logic Symbol



# **Pin Names**

- $I_0 I_8$ Data Inputs
- $\Sigma_{\rm O}$ Odd Parity Output
- Even Parity Output  $\Sigma_{\rm E}$

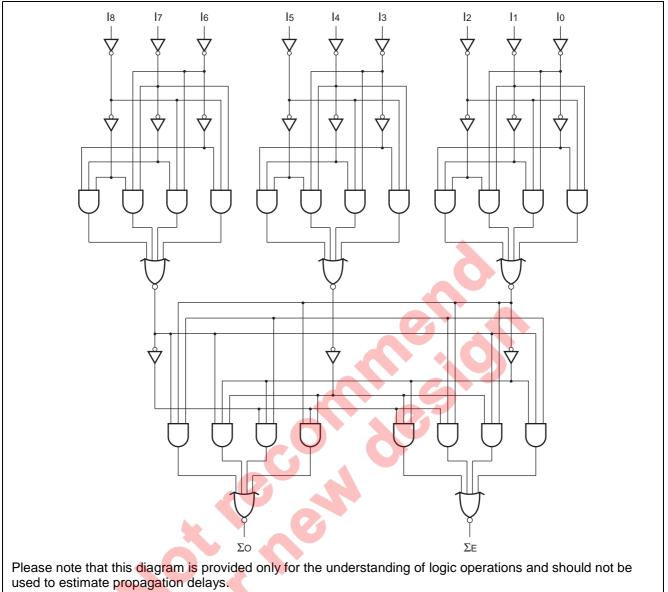
# **Truth Table**

	Outputs	
Number of High Inputs $I_0 - I_8$	ΣEven	ΣOdd
0, 2, 4, 6, 8	Н	L
1, 3, 5, 7, 9	L	H
H : High Voltage Level		
L : Low Voltage Level		

- Н: High Voltage Level
- L : Low Voltage Level



# Logic Diagram



# **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	Condition
Supply voltage	V <sub>cc</sub>	–0.5 to 7	V	
DC input diode current	I <sub>IK</sub>	-20	mA	$V_1 = -0.5V$
		20	mA	$V_{I} = Vcc+0.5V$
DC input voltage	VI	-0.5 to Vcc+0.5	V	
DC output diode current	I <sub>ок</sub>	-50	mA	$V_{0} = -0.5V$
		50	mA	$V_{O} = Vcc+0.5V$
DC output voltage	Vo	-0.5 to Vcc+0.5	V	
DC output source or sink current	I <sub>o</sub>	±50	mA	
DC $V_{cc}$ or ground current per output pin	I <sub>CC</sub> , I <sub>GND</sub>	±50	mA	
Storage temperature	Tstg	-65 to +150	°C	

# **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Condition	
Supply voltage	V <sub>cc</sub>	2 to 6	V		
Input and output voltage	V <sub>I</sub> , V <sub>O</sub>	0 to V <sub>cc</sub>	V		
Operating temperature	Та	-40 to +85	°C		
Input rise and fall time	tr, tf	8	ns/V	$V_{\rm CC} = 3.0 V$	
(except Schmitt inputs)				V <sub>cc</sub> = 4.5 V	
$V_{\rm IN}$ 30% to 70% $V_{\rm CC}$				V <sub>cc</sub> = 5.5 V	

# **DC Characteristics**

ltem	Sym- bol	Vcc (V)	1	Га = 25°(	C	Ta = -40 to +85°C		Unit	Condition	
			min.	typ.	max.	min.	max.			
Input Voltage	V <sub>IH</sub>	3.0	2.1	1.5	—	2.1	—	V	$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$	
		4.5	3.15	2.25	_	3.15	_			
		5.5	3.85	2.75	_	3.85	_			
	V <sub>IL</sub>	3.0	—	1.50	0.9	—	0.9		$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$	
		4.5	_	2.25	1.35	_	1.35			
		5.5	—	2.75	1.65	—	1.65			
Output voltage	V <sub>OH</sub>	3.0	2.9	2.99	—	2.9		V	$V_{IN} = V_{IL} \text{ or } V_{IH}$	
		4.5	4.4	4.49	—	4.4			I <sub>ουτ</sub> = -50 μA	
		5.5	5.4	5.49	Ι	5.4	—			
		3.0	2.58	—	-	2.48	- 0		$V_{IN} = V_{IL} \text{ or } V_{IH}$ $I_{OH} = -12 \text{ mA}$	
		4.5	3.94	—	ł	3.80			I <sub>он</sub> = –24 mА	
		5.5	4.94	—		4.80			I <sub>он</sub> = –24 mA	
	V <sub>OL</sub>	3.0	—	0.002	0.1		0.1		$V_{IN} = V_{IL} \text{ or } V_{IH}$	
		4.5	—	0.001	0.1		0.1		I <sub>OUT</sub> = 50 μA	
		5.5	-	0.001	0.1	—	0.1			
		3.0			0.32	-	0.37		$V_{IN} = V_{IL} \text{ or } V_{IH}$ $I_{OL} = 12 \text{ mA}$	
		4.5		—	0.32	—	0.37		I <sub>oL</sub> = 24 mA	
		5.5	_		0.32	_	0.37		I <sub>OL</sub> = 24 mA	
Input leakage	I <sub>IN</sub>	5.5	_		±0.1		±1.0	μA	$V_{IN} = V_{CC}$ or GND	
current										
Dynamic output	IOLD	5.5		—	—	86	—	mA	V <sub>OLD</sub> = 1.1 V	
current*	IOHD	5.5		—	—	-75	_	mA	V <sub>OHD</sub> = 3.85 V	
Quiescent supply current	I <sub>cc</sub>	5.5		—	8.0	—	80	μA	$V_{IN} = V_{CC}$ or ground	

\*Maximum test duration 2.0 ms, one output loaded at a time.

# **AC Characteristics**

			Ta = +25°C C <sub>L</sub> = 50 pF			C to +85°C 50 pF		
Item	Symbol	V <sub>cc</sub> (V)* <sup>1</sup>	Min	Тур	Max	Min	Max	Unit
Propagation delay	t <sub>PLH</sub>	3.3	1.0	14.5	17.0	1.0	18.5	ns
		5.0	1.0	11.0	13.0	1.0	14.5	
Propagation delay	t <sub>PHL</sub>	3.3	1.0	14.5	17.0	1.0	18.5	ns
		5.0	1.0	11.0	13.0	1.0	14.5	

Note: 1. Voltage Range 3.3 is  $3.3 \text{ V} \pm 0.3 \text{ V}$ Voltage Range 5.0 is  $5.0 \text{ V} \pm 0.5 \text{ V}$ 



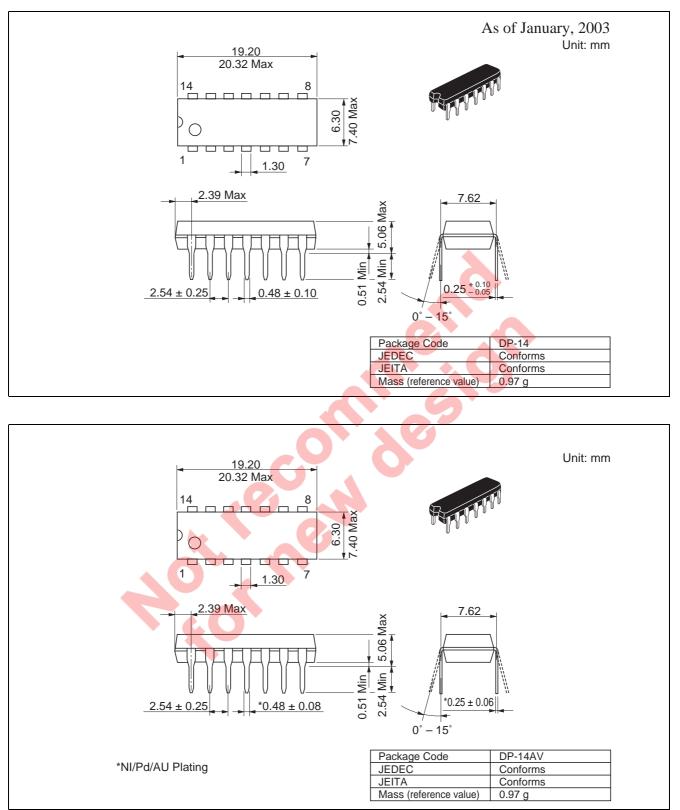
### Capacitance

Item	Symbol	Тур	Unit	Condition
Input capacitance	C <sub>IN</sub>	4.5	pF	V <sub>CC</sub> = 5.5 V
Power dissipation capacitance	C <sub>PD</sub>	60.0	pF	$V_{cc} = 5.0 V$

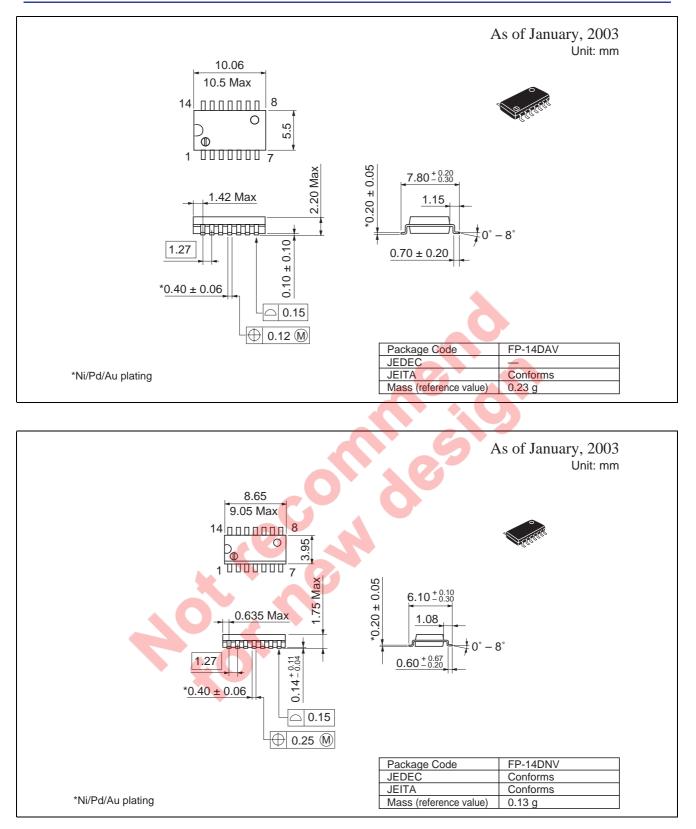




# **Package Dimensions**







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