

DA9080-63FCB2 Variant Overview

This document describes all the register default settings of the DA9080-63FCB2.

Contents

Contents1

Tables.....1

1. Terms and Definitions2

2. References.....2

3. Detailed Description2

4. Variant Table and Ordering Information.....3

Revision History4

Tables

Table 1. Register Settings DA9080-63FCB2.....2

Table 2. Variant Table3

1. Terms and Definitions

ADC	Analog to digital converter
CH<x>	Channel <x>, where x = 1 to 4
LDO	Low drop out (regulator)
OTP	One time programmable
PG	Power good
UQFN	Ultra-thin quad flat-pack no-lead (package)

2. References

[1] DA9080_Datasheet, Renesas Electronics.

Note 1 References are for the latest published version, unless otherwise indicated.

3. Detailed Description

Key settings:

- VCH1 = 3.30 V, VCH2 = 1.80 V, VCH3 = 1.10 V & VCH4 = 0.82 V
- VLDO = 3.30 V
- CH1 = SLOT4, CH2 = SLOT1, CH3 = SLOT3 & CH4 = SLOT2
- LDO = SLOT4
- ADC disabled
- CH<x> operates in AUTO mode, 2 MHz
- I2C standard speed. I2C slave address = 0x1B (7-bit)

Table 1. Register Settings DA9080-63FCB2

Register Address	Function	Default Value	Description
0x04	PMC_ADC_ENABLE	0x00	ADC disabled
0x05	PMC_CH_EN	0x1F	CH1, CH2, CH3, CH4 and LDO enabled
0x07	PMC_VOUT_BUCK1	0x3C	VCH1 = 3.30 V
0x08	PMC_VOUT_BUCK2	0x0F	VCH2 = 1.80 V
0x09	PMC_VOUT_BUCK3	0x28	VCH3 = 1.10 V
0x0A	PMC_VOUT_BUCK4	0x04	VCH4 = 0.82 V
0x0B	PMC_PHASE_INTERLEAVING	0x88	BUCK1_PHASE = 0° BUCK2_PHASE = 180° BUCK3_PHASE = 0° BUCK4_PHASE = 180°
0x0C	PMC_BUCK_SEQ_GRP	0x63	CH1 = SLOT4, CH2 = SLOT1 CH3 = SLOT3, CH4 = SLOT2
0x0D	PMC_LDO_SEQ_GRP	0x03	LDO = SLOT4
0x0E	PMC_PG1	0x08	CH3 assigned to PG1
0x0F	PMC_PG2	0x03	CH1 and LDO assigned to PG2
0x10	PMC_DISCHARGE	0x1F	CH<x> and LDO discharge enabled
0x62	OTP_CONFIG_ID	0x63	OTP variant number: DA9080-63FCB2

4. Variant Table and Ordering Information

Table 2. Variant Table

Part Number	Package	Size (mm)	Shipment Form	Pack Quantity
DA9080-63FCB2	32 UQFN	5.0 x 5.0 by 0.5 mm pitch	Tape & Reel	6000

Revision History

Revision	Date	Description
1.0	Oct 24, 2024	First version.

Status Definitions

Status	Definition
DRAFT	The content of this document is under review and subject to formal approval, which may result in modifications or additions.
APPROVED or unmarked	The content of this document has been approved for publication.

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